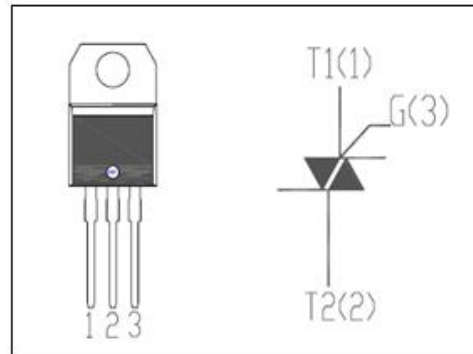


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FEATURES

- With TO-220AB insulated package.
- Off-State Voltages to 600 Volts
- Gate Triggering Guaranteed in Four Modes
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	600	V
V_{RRM}	Repetitive peak reverse voltage	600	V
$I_{T(RMS)}$	RMS on-state current (full sine wave) $T_j=80^\circ\text{C}$	25	A
I_{TSM}	Non-repetitive peak on-state current $t_p=8.3\text{ms}$	250	A
I^2t	Circuit Fusing($t=8.3\text{ms}$)	260	A ² S
T_j	Operating junction temperature	-40~125	°C
T_{stg}	Storage temperature	-40~150	°C
$R_{th(j-c)}$	Thermal resistance, junction to case	1.2	°C/W

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ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER		CONDITIONS	MAX	UNIT
I_{RRM}	Repetitive peak reverse current		$V_R=V_{RRM}$, $V_R=V_{RRM}$, $T_j=125^\circ\text{C}$	0.01 2	mA
I_{DRM}	Repetitive peak off-state current		$V_D=V_{DRM}$, $V_D=V_{DRM}$, $T_j=125^\circ\text{C}$	0.01 2	mA
I_{GT}	Gate trigger current	I	$V_D=12\text{V}$; $R_L=30\ \Omega$	50	mA
		II		50	
		III		50	
		IV		75	
I_H	Holding current		$I_{GT}=0.1\text{A}$, Gate Open	50	mA
V_{GT}	Gate trigger voltage all quadrant		$V_D=12\text{V}$; $R_L=30\ \Omega$	2	V
V_{TM}	On-state voltage		$I_T=35\text{A}$; $t_p=2\text{ms}$	1.85	V

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