

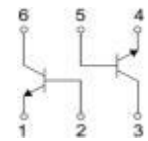
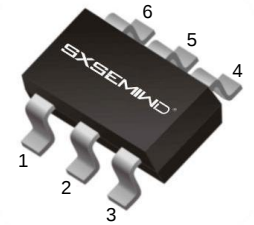
FEATURES

- Two 2SC2412K chips in a SOT-363 package
- Mounting possible with SOT-363 automatic mounting machines
- Transistor elements are independent, eliminating interference
- Mounting cost and area can be cut in half

MAXIMUM RATINGS (T_a=25°C unless otherwise noted)

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	60	V
V _{CEO}	Collector-Emitter Voltage	50	V
V _{EBO}	Emitter-Base Voltage	7	V
I _C	Collector Current -Continuous	150	mA
P _C	Collector Power Dissipation	150	mW
T _J , T _{stg}	Operation Junction and Storage Temperature Range	-55~+150	°C

SOT-363

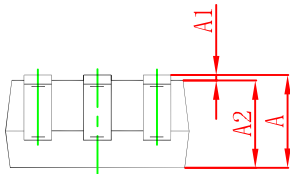
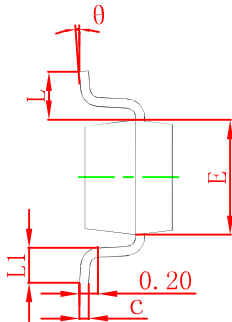
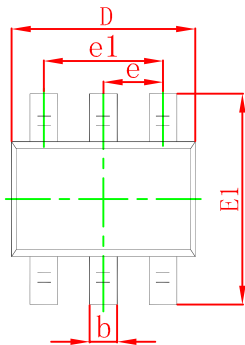


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ELECTRICAL CHARACTERISTICS (T_a=25°C unless otherwise specified)

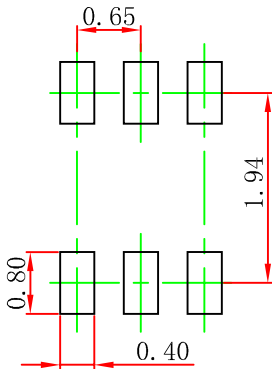
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C =50μA, I _E =0	60			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =1mA, I _B =0	50			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E =50μA, I _C =0	7			V
Collector cut-off current	I _{CBO}	V _{CB} =60V, I _E =0			0.1	μA
Emitter cut-off current	I _{EBO}	V _{EB} =7V, I _C =0			0.1	μA
DC current gain	h _{FE}	V _{CE} =6V, I _C =1mA	120		560	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =50mA, I _B =5mA			0.4	V
Transition frequency	f _T	V _{CE} =12V, I _C =2mA, f=100MHz		180		MHz
Collector output capacitance	C _{ob}	V _{CB} =12V, I _E =0, f=1MHz		2.0	3.5	pF

SOT-363 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

SOT-363 Suggested Pad Layout



Note:
1. Controlling dimension in millimeters.
2. General tolerance: ± 0.05mm.
3. The pad layout is for reference purposes only.