



钜地半导体
Tudi Semiconductor

Product Specification

TUDI-ICM7555

General Purpose Timers

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**semiconductor device
manufacturer**

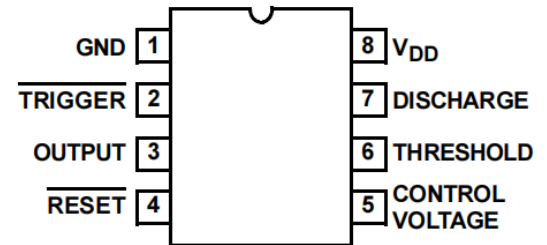
- Design
- research and development
- production
- and sales



Features

- Exact equivalent in most cases for SE/NE 555/556 or TLC555/556
- Low supply current
 - ICM7555 60 μ A
 - ICM7556 120 μ A
- Extremely low input currents 20pA
- High speed operation 1MHz
- Guaranteed supply voltage range 2V to 18V
- Temperature stability 0.005%/°C at +25°C
- Normal reset function - no crowbarring of supply during output transition
- Can be used with higher impedance timing elements than regular 555/556 for longer RC time constants
- Timing from microseconds through hours
- Operates in both astable and monostable modes
- Adjustable duty cycle
- High output source/sink driver can drive TTL/CMOS
- Outputs have very low offsets, HIGH and LOW
- Pb-free (RoHS Compliant)

8 LD PDIP, SOIC



Applications

- Precision timing
- Pulse generation
- Sequential timing
- Time delay generation
- Pulse width modulation
- Pulse position modulation
- Missing pulse detector

General Purpose Timers

The ICM7555 and ICM7556 are CMOS RC timers providing significantly improved performance over the standard SE/NE 555/556 and 355 timers, while at the same time being direct replacements for those devices in most applications. Improved parameters include low supply current, wide operating supply voltage range, low Threshold, Trigger and Reset currents, no crowbarring of the supply current during output transitions, higher frequency performance and no requirement to decouple Control Voltage for stable operation.



Specifically, the ICM7555 and ICM7556 are stable controllers capable of producing accurate time delays or frequencies. The ICM7556 is a dual ICM7555, with the two timers operating independently of each other, sharing only V+ and GND. In the one shot mode, the pulse width of each circuit is precisely controlled by one external resistor and capacitor. For astable operation as an oscillator, the free running frequency and the duty cycle are both accurately controlled by two external resistors and one capacitor. Unlike the regular bipolar SE/NE 555/556 devices, the Control Voltage terminal need not be decoupled with a capacitor. The circuits are triggered and reset on falling (negative) waveforms, and the output inverter can source or sink currents large enough to drive TTL loads, or provide minimal offsets to drive CMOS loads.



Absolute Maximum Ratings

Supply Voltage	+18V
Input Voltage	
Trigger, Control Voltage, Threshold, Reset (Note 1)	V+ +0.3V to GND -0.3V
Output Current	100mA

Operating Conditions

Temperature Range	
ICM7555C	0°C to +70°C
ICM7555I, ICM7556I	-25°C to +85°C
ICM7556M	-55°C to +125°C

Thermal Information

Thermal Resistance (Typical, Note 2)	θ_{JA} (°C/W)	θ_{JC} (°C/W)
14 Lead CERDIP Package	80	24
14 Lead PDIP Package*	115	N/A
8 Lead PDIP Package*	130	N/A
8 Lead SOIC Package	170	N/A

Maximum Junction Temperature (Hermetic Package)	+175°C
Maximum Junction Temperature (Plastic Package)	+150°C
Maximum Storage Temperature Range	-65°C to +150°C
Maximum Lead Temperature (Soldering 10s)	+300°C (SOIC - Lead Tips Only)

* Pb-free PDIPs can be used for through hole wave solder processing only. They are not intended for use in Reflow solder processing applications.

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

NOTES:

- Due to the SCR structure inherent in the CMOS process used to fabricate these devices, connecting any terminal to a voltage greater than V+ +0.3V or less than V- -0.3V may cause destructive latchup. For this reason it is recommended that no inputs from external sources not operating from the same power supply be applied to the device before its power supply is established. In multiple supply systems, the supply of the ICM7555 and ICM7556 must be turned on first.
- θ_{JA} is measured with the component mounted on a low effective thermal conductivity test board in free air. See Tech Brief 379 for details.

Electrical Specifications Applies to ICM7555 and ICM7556, unless otherwise specified

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A = +25^\circ\text{C}$			(NOTE 4) -55°C TO +125°C			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
Static Supply Current	I_{DD}	ICM7555 $V_{DD} = 5V$		40	200			300	μA
				60	300			300	μA
		ICM7556 $V_{DD} = 5V$		80	400			600	μA
				120	600			600	μA
Monostable Timing Accuracy		$R_A = 10K, C = 0.1\mu\text{F}, V_{DD} = 5V$		2					%
						858		1161	μs
Drift with Temperature (Note 3)		$V_{DD} = 5V$					150		ppm/°C
		$V_{DD} = 10V$					200		ppm/°C
		$V_{DD} = 15V$					250		ppm/°C
Drift with Supply (Note 3)		$V_{DD} = 5V$ to $15V$		0.5			0.5		%/V
Astable Timing Accuracy		$R_A = R_B = 10K, C = 0.1\mu\text{F}, V_{DD} = 5V$		2					%
						1717		2323	μs
Drift with Temperature (Note 3)		$V_{DD} = 5V$					150		ppm/°C
		$V_{DD} = 10V$					200		ppm/°C
		$V_{DD} = 15V$					250		ppm/°C
Drift with Supply (Note 3)		$V_{DD} = 5V$ to $15V$		0.5			0.5		%/V
Threshold Voltage	V_{TH}	$V_{DD} = 15V$	62	67	71	61		72	% V_{DD}
Trigger Voltage	V_{TRIG}	$V_{DD} = 15V$	28	32	36	27		37	% V_{DD}
Trigger Current	I_{TRIG}	$V_{DD} = 15V$			10			50	nA
Threshold Current	I_{TH}	$V_{DD} = 15V$			10			50	nA
Control Voltage	V_{CV}	$V_{DD} = 15V$	62	67	71	61		72	% V_{DD}



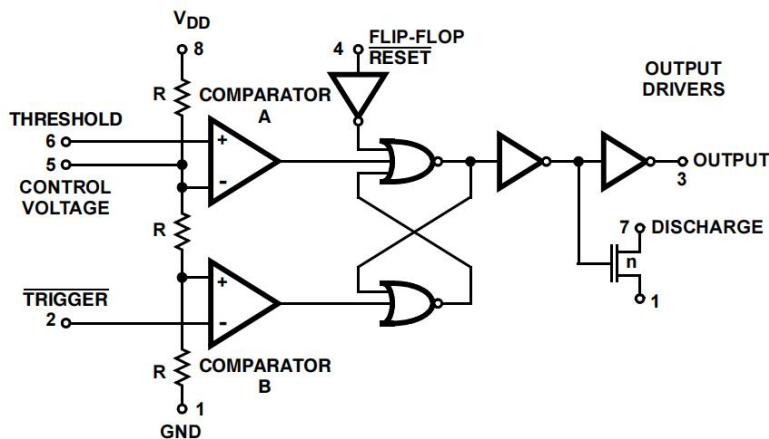
Electrical Specifications Applies to ICM7555 and ICM7556, unless otherwise specified (Continued)

PARAMETER	SYMBOL	TEST CONDITIONS	$T_A = +25^{\circ}\text{C}$			(NOTE 4) $-55^{\circ}\text{C TO } +125^{\circ}\text{C}$			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
Reset Voltage	V_{RST}	$V_{DD} = 2\text{V to } 15\text{V}$	0.4		1.0	0.2		1.2	V
Reset Current	I_{RST}	$V_{DD} = 15\text{V}$			10			50	nA
Discharge Leakage	I_{DIS}	$V_{DD} = 15\text{V}$			10			50	nA
Output Voltage	V_{OL}	$V_{DD} = 15\text{V}, I_{SINK} = 20\text{mA}$		0.4	1.0			1.25	V
		$V_{DD} = 5\text{V}, I_{SINK} = 3.2\text{mA}$		0.2	0.4			0.5	V
	V_{OH}	$V_{DD} = 15\text{V}, I_{SOURCE} = 0.8\text{mA}$	14.3	14.6		14.2			V
		$V_{DD} = 5\text{V}, I_{SOURCE} = 0.8\text{mA}$	4.0	4.3		3.8			V
Discharge Output Voltage	V_{DIS}	$V_{DD} = 5\text{V}, I_{SINK} = 15\text{mA}$		0.2	0.4			0.6	V
		$V_{DD} = 15\text{V}, I_{SINK} = 15\text{mA}$						0.4	V
Supply Voltage (Note 3)	V_{DD}	Functional Operation	2.0		18.0	3.0		16.0	V
Output Rise Time (Note 3)	t_R	$R_L = 10\text{M}, C_L = 10\text{pF}, V_{DD} = 5\text{V}$		75					ns
Output Fall Time (Note 3)	t_F	$R_L = 10\text{M}, C_L = 10\text{pF}, V_{DD} = 5\text{V}$		75					ns
Oscillator Frequency (Note 3)	f_{MAX}	$V_{DD} = 5\text{V}, R_A = 470\Omega, R_B = 270\Omega, C = 200\text{pF}$		1					MHz

NOTES:

- These parameters are based upon characterization data and are not tested.
- Applies only to military temperature range product (M suffix).

Functional Diagram



NOTE: This functional diagram reduces the circuitry down to its simplest equivalent components. Tie down unused inputs.

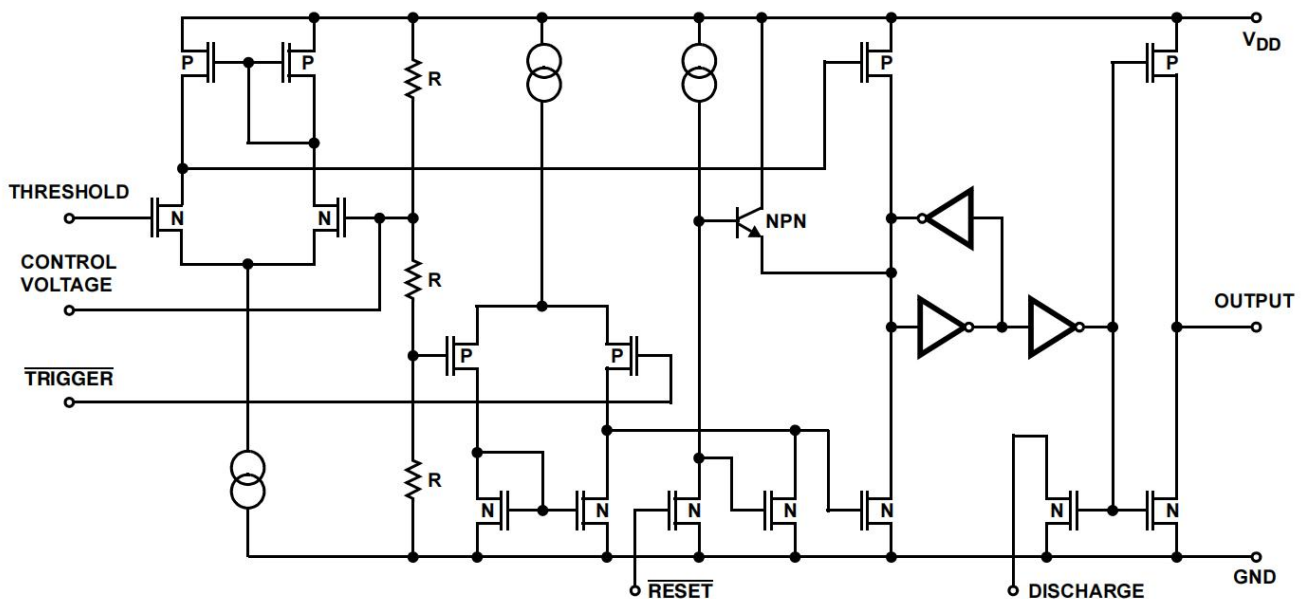
TRUTH TABLE

THRESHOLD VOLTAGE	TRIGGER VOLTAGE	$\overline{\text{RESET}}$	OUTPUT	DISCHARGE SWITCH
Don't Care	Don't Care	Low	Low	On
$> \frac{2}{3}V_+$	$> \frac{1}{3}V_+$	High	Low	On
$< \frac{2}{3}V_+$	$> \frac{1}{3}V_+$	High	Stable	Stable
Don't Care	$< \frac{1}{3}V_+$	High	High	Off

NOTE: $\overline{\text{RESET}}$ will dominate all other inputs: $\overline{\text{TRIGGER}}$ will dominate over THRESHOLD.



Schematic Diagram



$R = 100k\Omega \pm 20\%$ (TYP)

Application Information

General

The ICM7555 and ICM7556 devices are, in most instances, direct replacements for the NE/SE 555/6 devices. However, it is possible to effect economies in the external component count using the ICM7555 and ICM7556. Because the bipolar NE/SE 555/6 devices produce large crowbar currents in the output driver, it is necessary to decouple the power supply lines with a good capacitor close to the device. The ICM7555 and ICM7556 devices produce no such transients. See Figure 1.

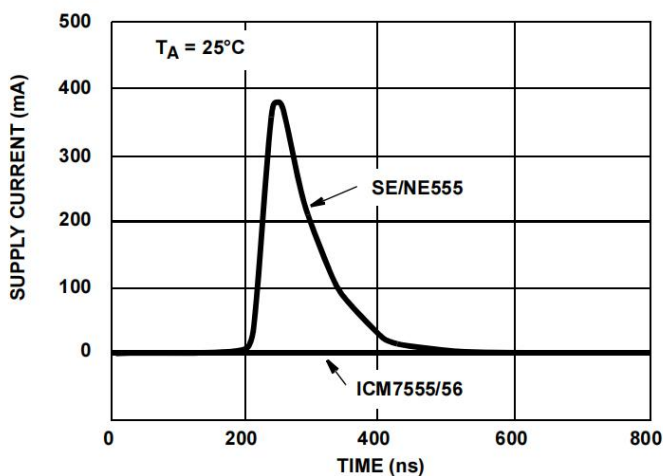


FIGURE 1. SUPPLY CURRENT TRANSIENT COMPARED WITH A STANDARD BIPOLAR 555 DURING AN OUTPUT TRANSITION

The ICM7555 and ICM7556 produce supply current spikes of only 2mA - 3mA instead of 300mA - 400mA and supply decoupling is normally not necessary. Also, in most instances, the CONTROL VOLTAGE decoupling capacitors are not required since the input impedance of the CMOS comparators on chip are very high. Thus, for many applications, two capacitors can be saved using an ICM7555 and three capacitors with an ICM7556.

POWER SUPPLY CONSIDERATIONS

Although the supply current consumed by the ICM7555 and ICM7556 devices is very low, the total system supply current can be high unless the timing components are high impedance. Therefore, use high values for R and low values for C in Figures 2A, 2B, and 3.

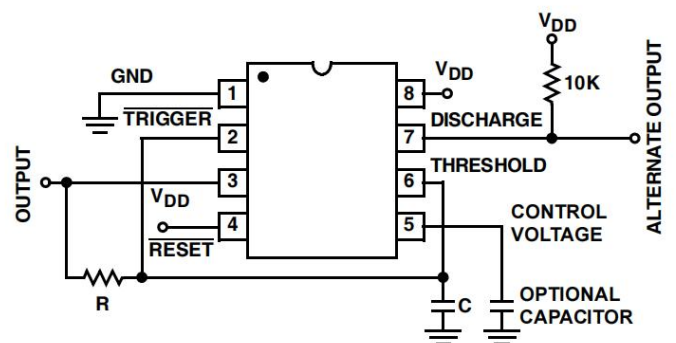


FIGURE 2A. ASTABLE OPERATION

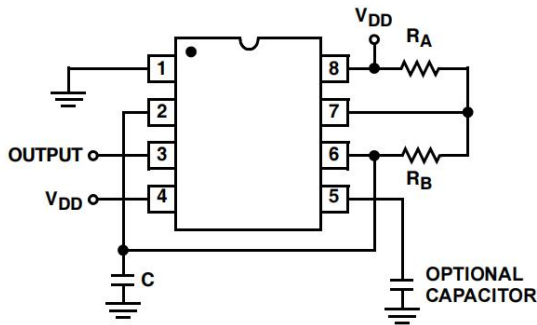


FIGURE 2B. ALTERNATE ASTABLE CONFIGURATION

OUTPUT DRIVE CAPABILITY

The output driver consists of a CMOS inverter capable of driving most logic families including CMOS and TTL. As such, if driving CMOS, the output swing at all supply voltages will equal the supply voltage. At a supply voltage of 4.5V or more, the ICM7555 and ICM7556 will drive at least two standard TTL loads.

ASTABLE OPERATION

The circuit can be connected to trigger itself and free run as a multivibrator, see Figure 2A. The output swings from rail to rail, and is a true 50% duty cycle square wave. (Trip points and output swings are symmetrical.) Less than a 1% frequency variation is observed over a voltage range of +5V to +15V.

$$f = \frac{1}{1.4 RC} \quad (\text{EQ. 1})$$

The timer can also be connected as shown in Figure 2B. In this circuit, the frequency is:

$$f = 1.44 / (R_A + 2R_B)C \quad (\text{EQ. 2})$$

The duty cycle is controlled by the values of R_A and R_B , by the equation:

$$D = (R_A + R_B) / (R_A + 2R_B) \quad (\text{EQ. 3})$$

MONOSTABLE OPERATION

In this mode of operation, the timer functions as a one-shot. See Figure 3. Initially the external capacitor (C) is held discharged by a transistor inside the timer. Upon application of a negative TRIGGER pulse to pin 2, the internal flip-flop is set which releases the short circuit across the external capacitor and drives the OUTPUT high. The voltage across the capacitor now increases exponentially with a time constant $t = R_A C$. When the voltage across the capacitor equals $\frac{2}{3} V^+$, the comparator resets the flip-flop, which in turn discharges the capacitor rapidly and also drives the OUTPUT to its low state. TRIGGER must return to a high state before the OUTPUT can return to a low state.

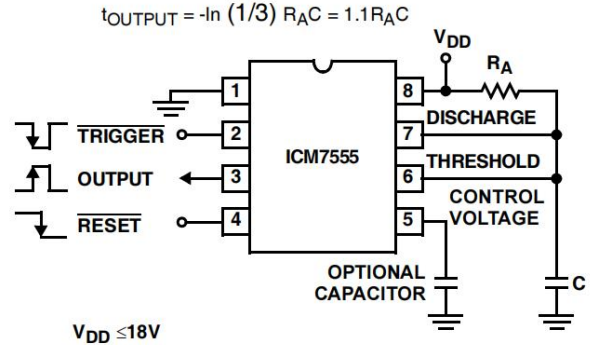


FIGURE 3. MONOSTABLE OPERATION

CONTROL VOLTAGE

The CONTROL VOLTAGE terminal permits the two trip voltages for the THRESHOLD and TRIGGER internal comparators to be controlled. This provides the possibility of oscillation frequency modulation in the astable mode or even inhibition of oscillation, depending on the applied voltage. In the monostable mode, delay times can be changed by varying the applied voltage to the CONTROL VOLTAGE pin.

RESET

The RESET terminal is designed to have essentially the same trip voltage as the standard bipolar 555/6, i.e., 0.6V to 0.7V. At all supply voltages it represents an extremely high input impedance. The mode of operation of the RESET function is, however, much improved over the standard bipolar NE/SE 555/6 in that it controls only the internal flip-flop, which in turn controls simultaneously the state of the OUTPUT and DISCHARGE pins. This avoids the multiple threshold problems sometimes encountered with slow falling edges in the bipolar devices.

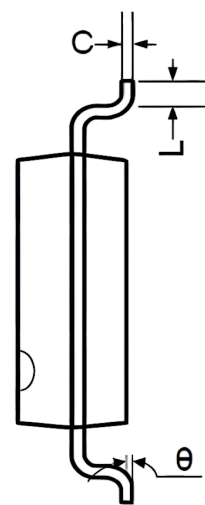
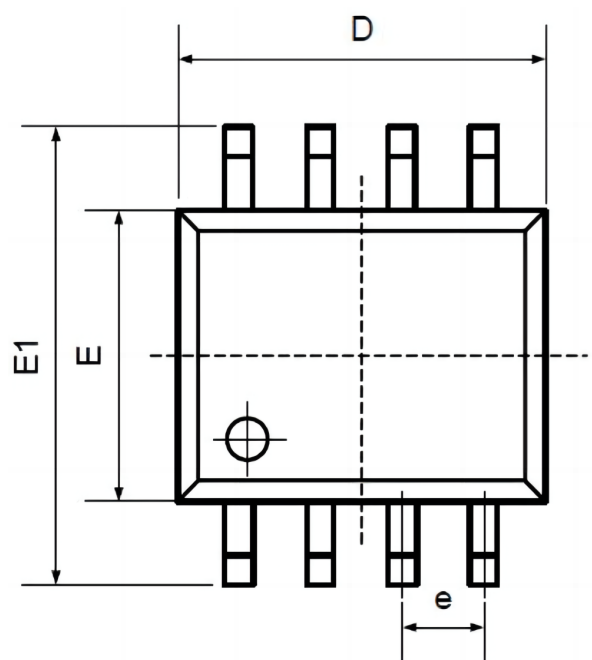


Order information

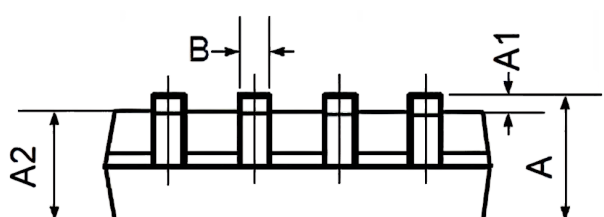
Order Number	Package	Package Quantity	Marking On The park
ICM7555CBAZ-T-TUDI	SOP8	Tape,Reel,2500	ICM7555CBAZ-T
ICM7555IBAZ-T-TUDI	SOP8	Tape,Reel,2500	ICM7555IBAZ-T
ICM7555IPAZ-TUDI	DIP8	Tube,50,A box of 2000	ICM7555IPAZ



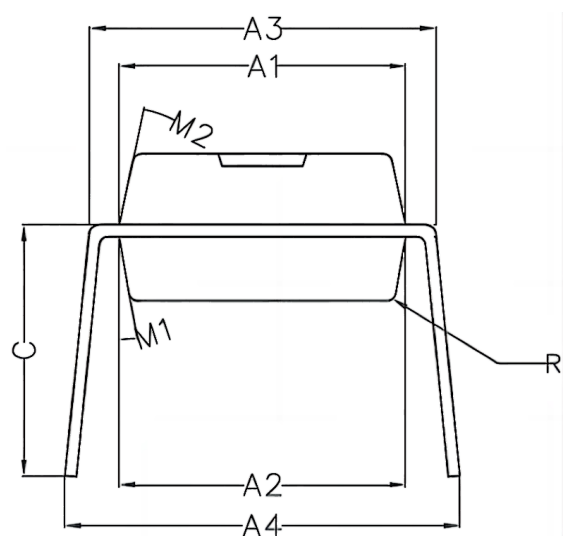
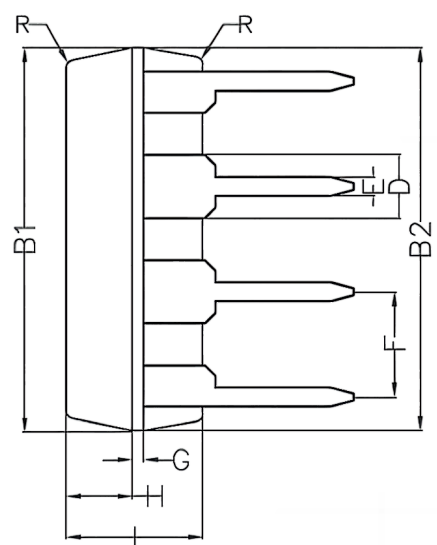
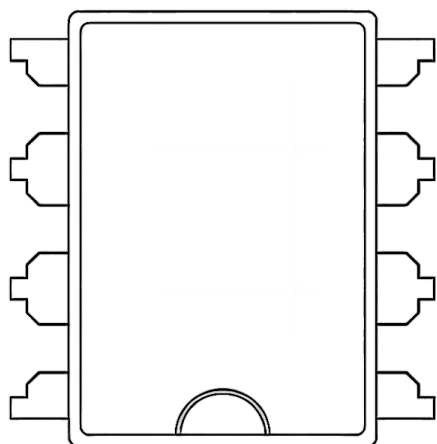
Package SOP8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
B	0.330	0.510	0.013	0.020
C	0.190	0.250	0.007	0.010
D	4.780	5.000	0.188	0.197
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
e	1.270TYP		0.050TYP	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Package DIP8



Symbol	Min	Non	Max
A1	6.28	6.33	6.38
A2	6.33	6.38	6.43
A3	7.52	7.62	7.72
A4	7.80	8.40	9.00
B1	9.15	9.20	9.25
B2	9.20	9.25	9.30
C		5.57	
D		1.52	
E	0.43	0.45	0.47
F		2.54	
G		0.25	
H	1.54	1.59	1.64
I	3.22	3.27	3.32
R		0.20	
M1	9°	10°	11°
M2	11°	12°	13°



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