

Low Sensitivity Micropower Omnipolar Hall-effect Switch

Features

- Low sensitivity omnipolar operation.
- Micropower operation:
Type 0.8 μ A(average :VDD=1.8V)
- Onboard voltage regulator for 1.6V to 5.5V range.
- Magnetic threshold:Bop= $\pm$ 70Gs, Brp= $\pm$ 50Gs
- Industry-leading ultra-low power consumption.
- Wide operating temperature range:
-40°C to 85°C.
- WBDNF 1.6mm $\times$ 1.2mm $\times$ 0.37mm-4L package

Applications

- Smartphone.
- Notebook computer.
- Handheld gaming consoles.
- Bluetooth headset.
- DV.
- Contact-less switch, Level, proximity and position switches in consumer products.

General Description

The AW86516EB device is an ultra-low-power digital-switch Hall effect sensor, designed for the most compact and battery-sensitive systems. The device is offered in multiple sampling rates, output drivers, and packages to accommodate various applications. The supply range of AW86516EB is 1.6V to 5.5V to support portable equipment. To minimize PCB space, the AW86516EB have the ultra-small package: WBDNF 1.6mm $\times$ 1.2mm $\times$ 0.37mm -4L.

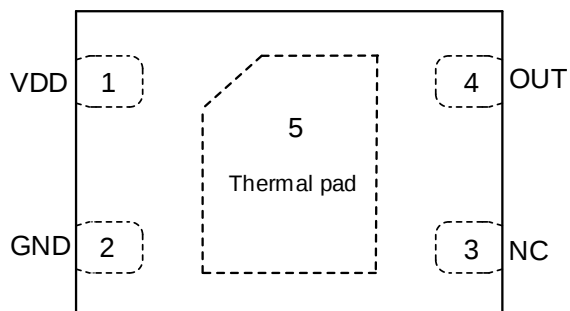
When the magnetic field strength is greater than Bop, then the device output is pulled low; When the magnetic field strength is less than Brp, then the device output is pulled high; When the magnetic field strength is between Bop and Brp, then the device output remains in the previous state.

Typical Application Circuit



Pin Configuration And Top Mark

AW86516EBCADNR
Top View



AW86516EBCADNR Marking
Top View

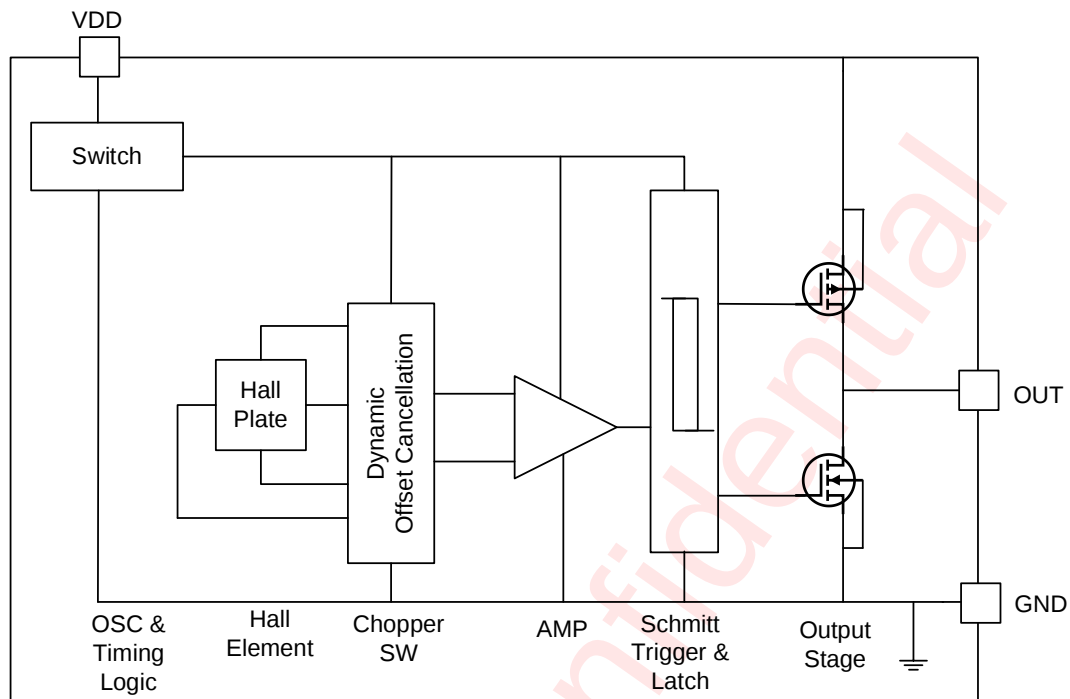


Y5RD-AW86516EBCADNR
XXXX-Production Tracing Code

Pin Definition

No.	NAME	DESCRIPTION
1	VDD	Power Supply
2	GND	Ground
3	NC	No Connection
4	OUT	Omnipolar output that responds to north and south magnetic
5	Thermal pad	No Connection

Functional Block Diagram

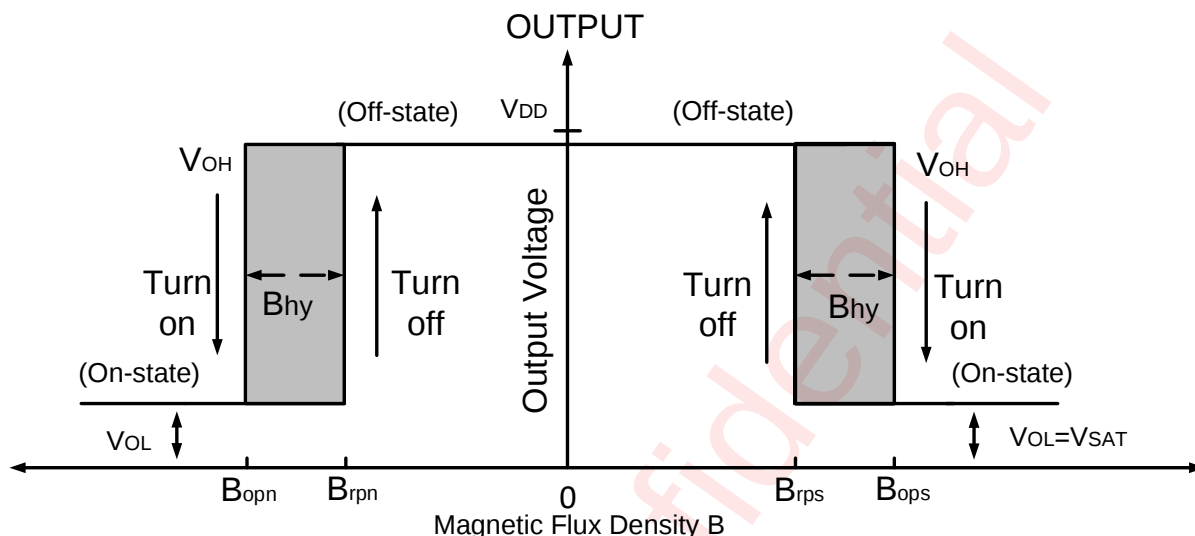


Ordering Information

Part Number	Temperature	Package	Marking	Moisture Sensitivity Level	Environmental Information	Delivery Form
AW86516EBCADNR	-40°C~85°C	WBDFN 1.6mm×1.2mm -4L	Y5RD	MSL1	ROHS+HF	3000 units/ Tape and Reel

Detailed Functional Description

When the magnetic field strength is greater than Bop, then the device output is pulled low; When the magnetic field strength is less than Brp, then the device output is pulled high; When the magnetic field strength is between Bop and Brp, then the device output remains in the previous state.



Absolute Maximum Ratings ^(Note1)

PARAMETERS	RANGE
Supply Voltage	-0.3V to 6V
Supply Current	4mA
Output Voltage	-0.4V to V _{DD} +0.4V
Output Current	4mA
Operating Ambient Temperature T _A	-40°C to 85°C
Storage Temperature T _{STG}	-65°C to 150°C
Junction temperature T _J	-50°C to 165°C
Magnetic Flux	No limit
Package Power Dissipation	230mW
ESD Rating ^(Note2 3)	
Human Body Model (HBM) ESD capability	±6kV
Charged-device model (CDM) ESD capability	±1.5kV
Latch-up	
Test Condition: JESD78E	+ IT: 200mA
	- IT: 200mA

Note1: Conditions out of those ranges listed in "absolute maximum ratings" may cause permanent damages to the device. In spite of the limits above, functional operation conditions of the device should within the ranges listed in "recommended operating conditions". Exposure to absolute-maximum-rated conditions for prolonged periods may affect device reliability.

Note2: The human body model is a 100pF capacitor discharged through a 1.5kΩ resistor into each pin. Test method: ESDA/JEDEC JS -001-2017.

Note3:Charge Device Model test method: ESDA/JEDEC JS-002-2018.

Electrical Characteristics

$V_{DD}=3.3V$ supply, $T_A=-40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$ (unless otherwise noted)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V_{DD}	Supply Voltage	Operating, $T_J < 165^{\circ}\text{C}$	1.6		5.5	V
$I_{DD}(\text{awake})$ (Note1)	Supply Current	During awake period, $T_A = 25^{\circ}\text{C}$, $V_{DD}=3.3V$	-	0.95	1.3	mA
$I_{DD}(\text{sleep})$		During sleep period, $T_A = 25^{\circ}\text{C}$, $V_{DD}=3.3V$	-	0.43	-	μA
$I_{DD}(\text{avg})$ (Note1)	Average supply current	$T_A = 25^{\circ}\text{C}$, $V_{DD} = 1.8V$, $f_S=20\text{Hz}$		0.8		μA
V_{OL}	Output low voltage(on)	$I_{OUT} = 1\text{ mA}$	-	0.1	0.2	V
V_{OH}	Output high voltage(off)	$I_{OUT} = -1\text{mA}$	$V_{DD}-0.2$	$V_{DD}-0.1$	-	V
T_{awake} (Note1)	Awake time	(note)	-	40	60	μs
T_{period}	Period	$f_S=20\text{Hz}$ (sampling rate)		50	75	ms
D.C.	Duty cycle	-	-	0.08	-	%
f_C	Chopping Frequency		-	500	-	kHz
I_{OFF} (Note1)	Output Leakage Current	$V_{OUT} = 5.5\text{ V}$; Switch state=off	-	-	0.1	μA

Note: Maximum and minimum parameters values over operating temperature range are not tested in production. They are guaranteed by design, characterization and process control. The magnetic field strength (Gauss) required to cause the switch to change state (operate and release) will be as specified in the magnetic characteristics. To test the switch against the specified magnetic characteristics, the switch must be placed in a uniform magnetic field.

Note1: Minimum and/or maximum limit is guaranteed by design and by statistical analysis of device characterization data. The specification is not guaranteed by production testing.

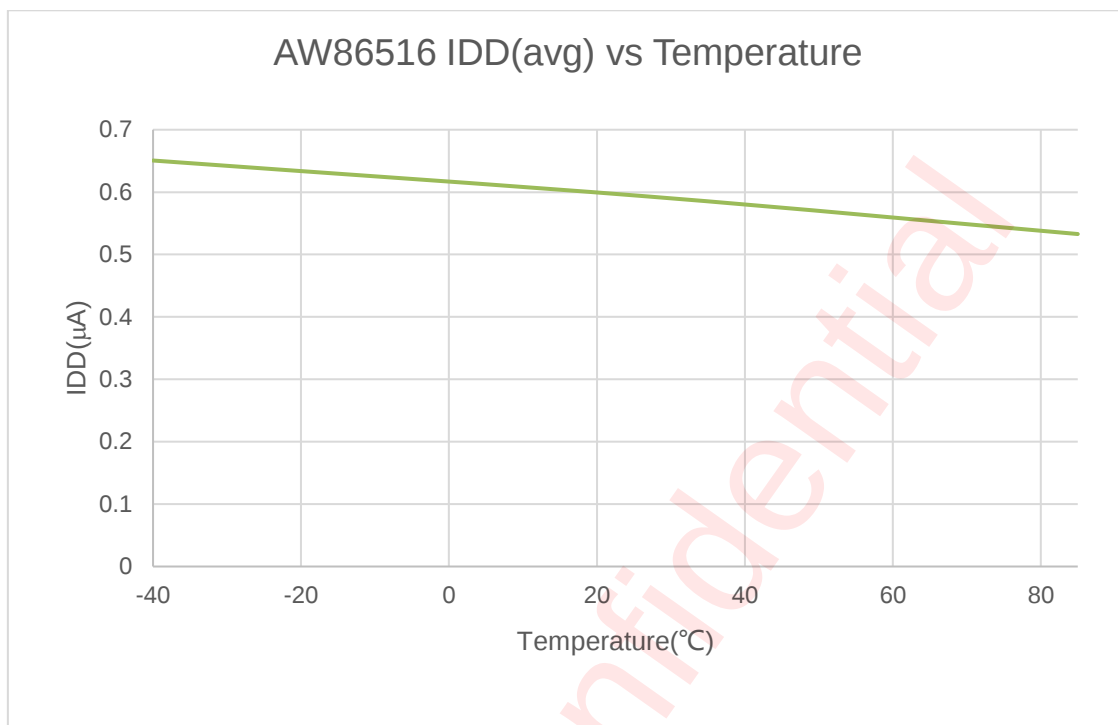
Magnetic Characteristics

$T_A=+25^{\circ}\text{C}$, $V_{DD}=3.3V$ (unless otherwise noted)

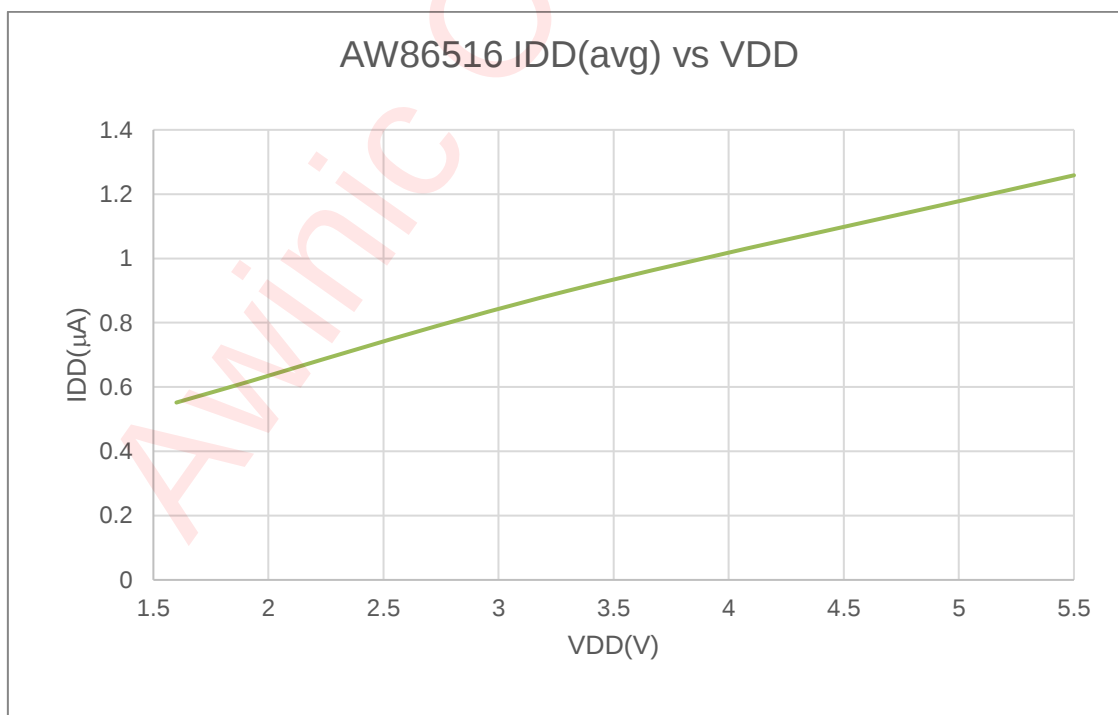
(1 mT=10 Gauss)

Symbol	Characteristics	Test condition	Min	Typ	Max	Unit
Bops(south pole to part marking side)	Operation Point	$T_A=+25^{\circ}\text{C}$, $V_{DD}=3.3V$	50	70	90	Gauss
Bopn(north pole to part marking side)		$T_A=+25^{\circ}\text{C}$, $V_{DD}=3.3V$	-90	-70	-50	
Brps(south pole to part marking side)	Release Point	$T_A=+25^{\circ}\text{C}$, $V_{DD}=3.3V$	30	50	70	
Brpn (north pole to part marking side)		$T_A=+25^{\circ}\text{C}$, $V_{DD}=3.3V$	-70	-50	-30	
Bhy ($ B_{opx} - B_{rpx} $)	Hysteresis		-	20	-	

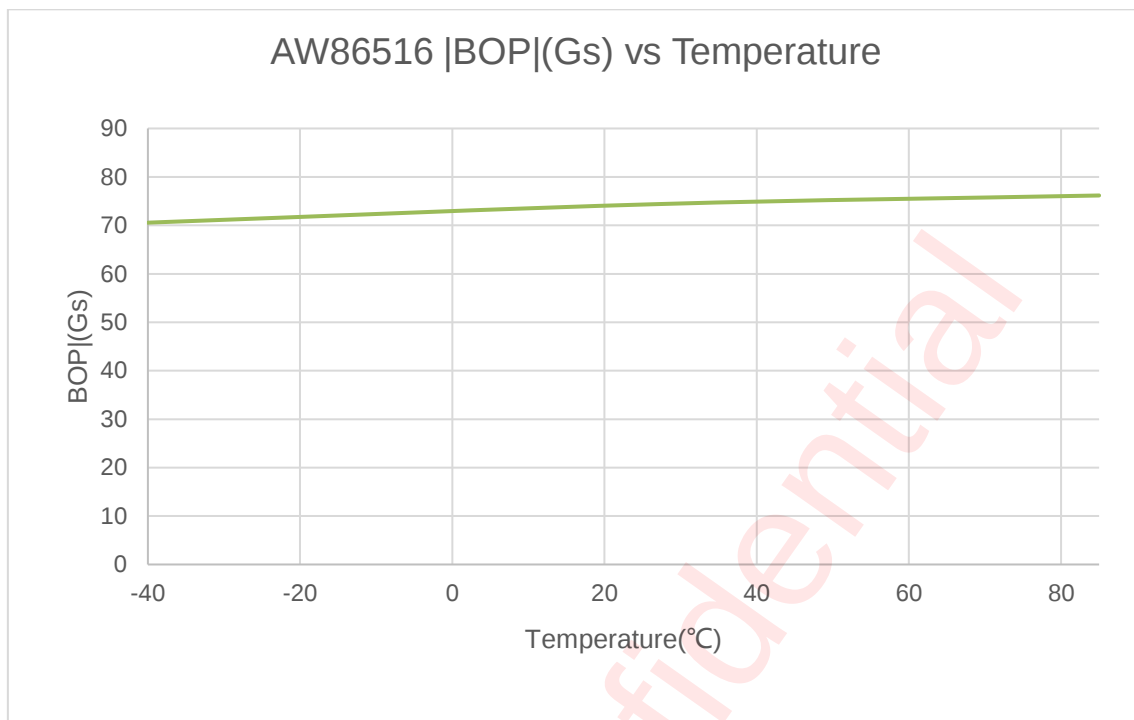
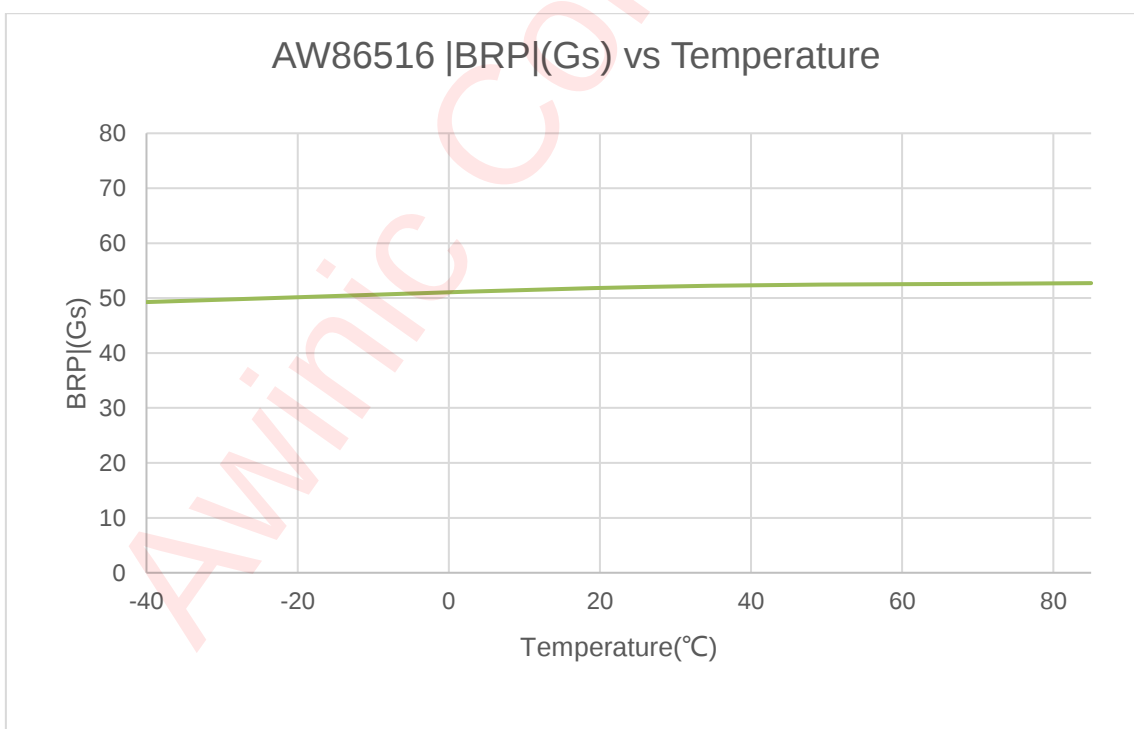
Typical Characteristics



Ambient Temperature T_a [°C] I_{DD} vs. T_a (VDD=1.8V)



Average Supply Current vs. Supply Voltage ($T_a=25^{\circ}\text{C}$)

Ambient Temperature T_a [°C] |Bop| vs. T_a ($V_{DD}=3.3V$)Ambient Temperature T_a [°C] |Brp| vs. T_a ($V_{DD}=3.3V$)

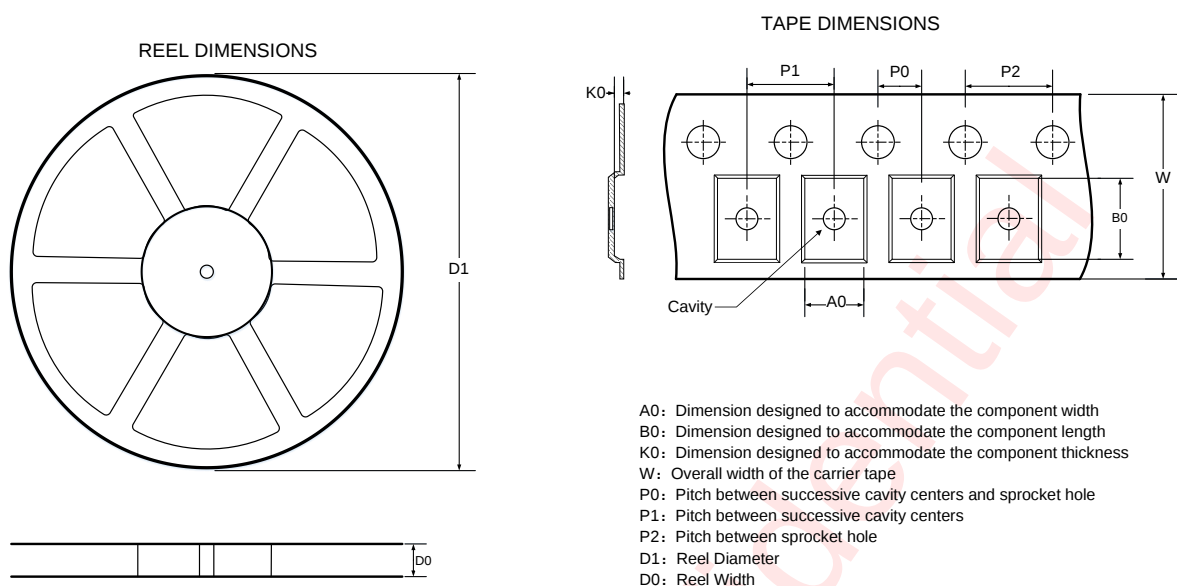
Application Information

It is recommended to connect an external capacitor of 0.1 μ F to VDD and GND. The noise of the injection device can be reduced.

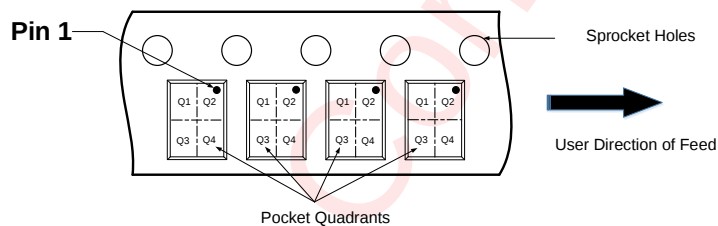


Figure 1 The Application Circuit of AW86515EBADNR

Tape And Reel Information



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



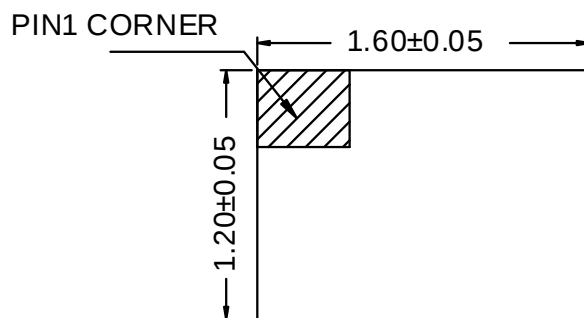
Note: The above picture is for reference only. Please refer to the value in the table below for the actual size

DIMENSIONS AND PIN1 ORIENTATION

D1 (mm)	D0 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
178	8.4	1.37	1.77	0.55	2	4	4	8	Q2

All dimensions are nominal

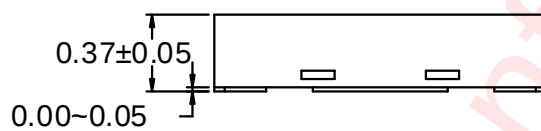
Package Description



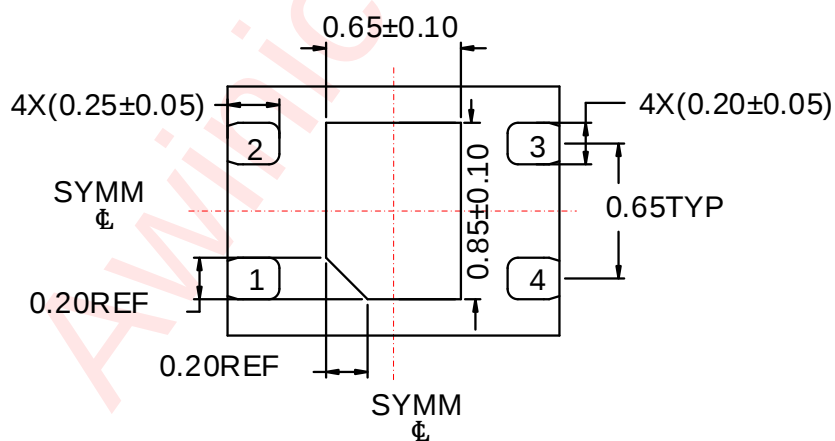
Top View



Side View



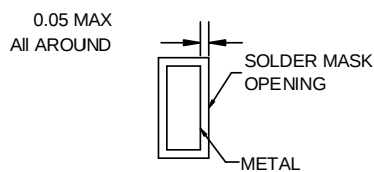
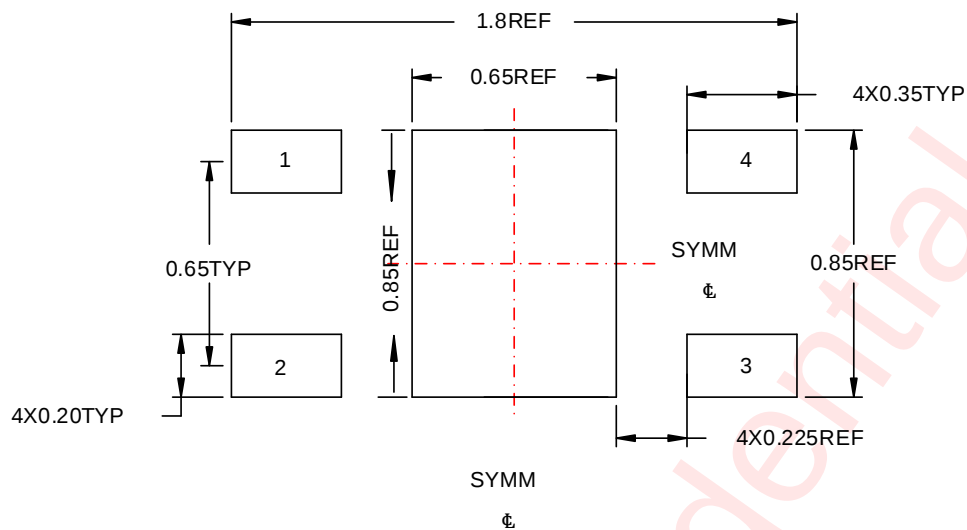
Side View



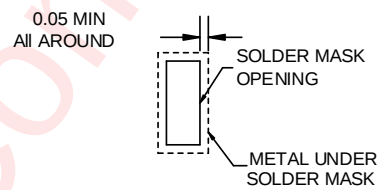
Bottom View

Unit: mm

Land Pattern Data



NON SOLDER MASK DEFINED



SOLDER MASK DEFINED

Unit: mm

REVISION HISTORY

Version	Date	Change Record
V1.0	Aug. 2025	Officially released

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