GENERAL DESCRIPTION

The LW83XX Series is a group of voltage regulators with high accuracy, high speed, low drop-out, reverse current protection, high ripple rejection, fast discharge and power-good function.

The series consists of a voltage reference, an error amplifier, a driver transistor, a current limiter, a thermal protection circuit, a power-good circuit, a reverse current protection circuit and a phase compensation circuit.

The LW83XX Series has a power good state pin(PG) which used to indicate the status of the output voltage.

Output voltage is selectable from 0.8V to 5.0V which fixed by laser trimming technologies, Step=100mV.

The LW83XX Series is available in DFN2x2-6L packages.

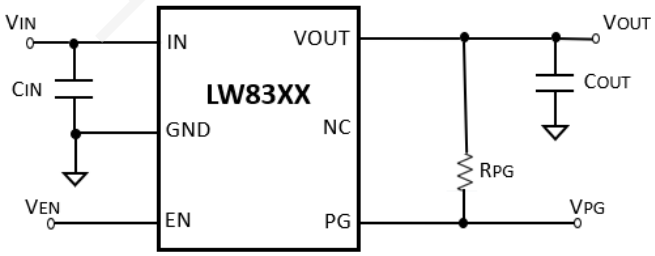
FEATURES

- Output Accuracy: ±1.0%
- Low Quiescent Current: 28uA
- Low Dropout Voltage: 310mV@1000mA/3.3V
- High PSRR: 78dB@1KHz, 10mA
- Output Current: 1000mA
- Excellent Line and Load Transient Response
- Operating Voltage Range: from 1.8V to 7.0V
- Output Voltage Range: from 0.8V to 5.0V
- Over-Temperature Protection
- Current Limiting Protection
- Output Short-Circuit Protection
- Reverse Current Protection Function
- Support Power-Good Indicator Function
- Available in DFN2x2-6L Package

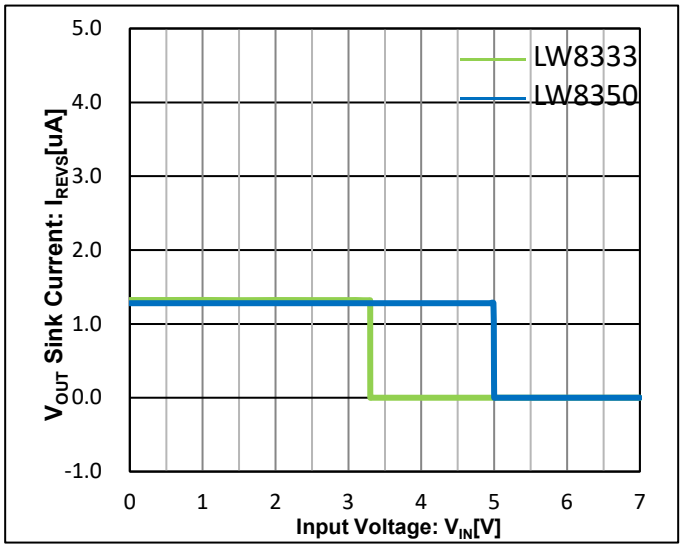
APPLICATIONS

- Battery-Powered Devices
- Reference Voltage Sources
- USB products and HDMI Equipments
- Other Low Voltage Power Suppliers

TYPICAL APPLICATION CIRCUIT



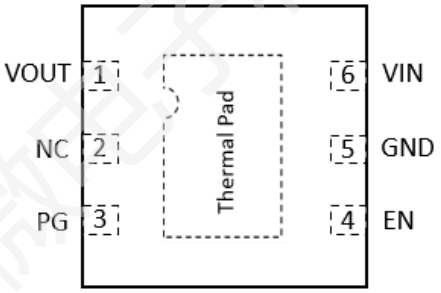
TYPICAL PERFORMANCE CHARACTERISTICS



PIN DESCRIPTION:

PIN No	SYMBOL	DESCRIPTTION
DFN2x2-6L		
1	VOUT	Output
2	NC	Not Connected
3	PG	Power Good
4	EN	Chip Enable
5	GND	Ground
6	VIN	Power Supply Input
Thermal pad	Pad	Connect the thermal pad to a large area GND plane

PIN ASSIGNMENT



MARK INFORMATION:

DFN2x2-6L

XX: VOLTAGE

YY: DATE CODE

ABSOLUTE MAXIMUM RATINGS (1):

(T_A =25℃, unless otherwise specified.)

SYMBOL	ITEM		RATING	UNIT
V_{IN}	Supply Voltage		-0.3~8.0	V
V_{EN}	EN Pin Voltage		-0.3~8.0	V
V_{PG}	PG pin Voltage		-0.3~8.0	V
V_{OUT}	VOUT pin Voltage		-0.3~ (V_{IN}+0.3)	
$V_{(ESD)}$	ESD Susceptibility, HBM(2)		±4000	V
P_D	Maximum Power Dissipation	DFN2x2-6L	1.56	W
$R_{\theta JA}$	Package Thermal Resistance Θ_{JA}	DFN2x2-6L	80	℃/W
T_J	Junction Temperature Range		-40~150	℃
T_{STG}	Storage Temperature Range		-40~150	℃
T_{SOLDER}	Lead Temperature (Soldering)		260℃, 10s	

Note:

1. Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability

2: per ANSI/ESDA/JEDEC JS-001

RECOMMANDED OPERATING RANGE:

SYMBOL	ITEM	VALUE	UNIT
V_{IN}	VIN Supply Voltage	1.8~7.0	V
V_{EN}	EN Pin Voltage	0~7.0	V
V_{PG}	PG Pin Voltage	0~7.0	V
V_{OUT}	V_{OUT} Pin Voltage	0.8~5.0	V
I_{OUT}	Output Current	0~1.0	A
T_J	Junction Temperature Range	-40~125	℃

ELECTRICAL CHARACTERISTICS:

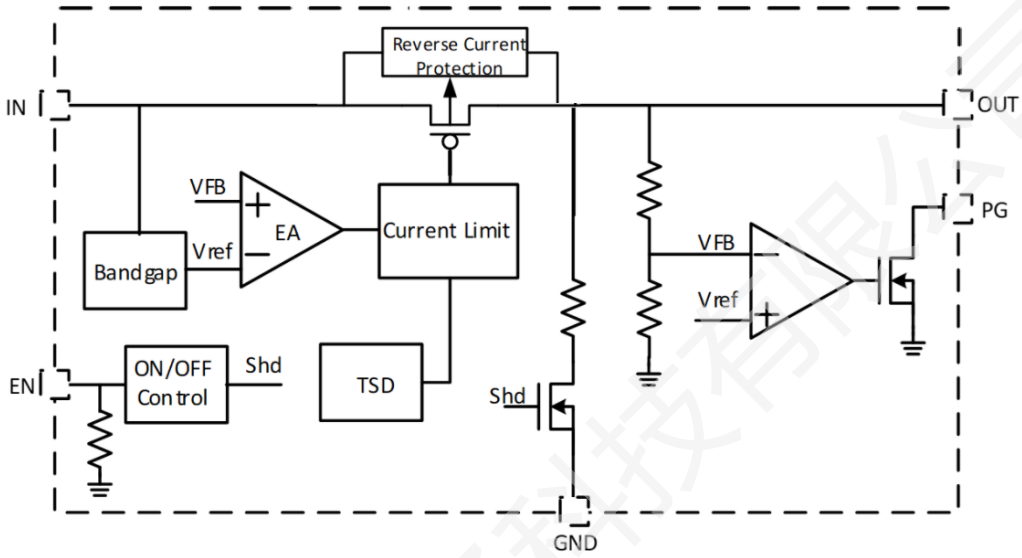
(V_{IN}=V_{OUT}+1V, V_{OUT}=3.3V, C_{IN}= C_{OUT}=1uF, T_A =25℃, unless otherwise specified.)

Symbol	Parameter	Conditions		MIN	TYP	MAX	Units
V_{IN}	Input Voltage			1.8		7.0	V
V_{OUT}	Output Accuracy	I_{OUT}= 1mA		-1.0		+1.0	%
I_{LIM}	Current Limit(1)	V_{IN}=4.3V, V_{OUT} =3.3V		1.1	1.45		A
I_Q	Quiescent Current	V_{IN}=V_{EN}=V_{OUT}+1V, No Load			28	45	μA
I_{SHD}	Shutdown Current	V_{IN}=7.0V, V_{EN}=0V				0.1	μA
V_{DROP}	Dropout Voltage(2)	I_{OUT}=300mA			84		mV
		I_{OUT}=500mA			143		
		I_{OUT}=1000mA			310		
S_{LINE}	Line Regulation	V_{IN}= V_{OUT} +1V to 7.0V, I_{OUT}=1mA			0.05	0.1	%/V
S_{LOAD}	Load Regulation	1mA≤I_{OUT}≤1000mA			0.001	0.01	%/mA
I_{SHORT}	Short Current	V_{OUT}=0V			70		mA
V_{ENH}	EN High Voltage	V_{IN}=1.8V to 7.0V, I_{OUT}=1mA		1.5			V
V_{ENL}	EN Low Voltage					0.5	V
T_{STR}	Startup Time	From V_{EN} 'L'→'H' to 95%*V_{OUT}, C_{OUT}=1uF, No Load			50		μs
PSRR	Power Supply Rejection Ratio	C_{IN} =None, V_{OUT}=3.3V, I_{OUT}=10mA	f=217Hz		79		dB
			f=1KHz		78		
			f=10KHz		72		
I_{REV}	Reverse Current(3)	V_{IN}=0V, V_{OUT}=7.0V			0.01	0.5	μA
I_{REVS}	VOUT Pin Sink Current(4)	V_{IN}=6.0V, V_{OUT}=7.0V			1.5	2.5	μA
PG_{HTH}	PG high threshold	V_{OUT} increasing		87	91	95	%V_{OUT}
PG_{LTH}	PG low threshold	V_{OUT} decreasing		82	86	90	%V_{OUT}
$V_{OL(PG)}$	PG pin low-level output voltage	V_{OUT} ＜PG_{HTH},I_{SINK} = 1 mA			120	400	mV
$I_{lkg(PG)}$	PG pin leakage current	V_{OUT} > PG_{HTH}, V_{PG} = 7.0 V			50	500	nA
t_{PGDH}	PG delay time rising	Time from 92% V_{OUT} to 20% of PG			200		μs
t_{PGDL}	PG delay time falling	Time from 90% V_{OUT} to 80% of PG			5		μs
T_{SD}	Thermal Shut Down	Temperature rising			160		℃
△T_{SD}	TSD Hysteresis	Temperature falling			20		℃
$R_{DISCHRG}$	R_{ON} of Discharge MOSFET	V_{EN}=0V			80		Ω

NOTES:

1. Guaranteed by design
2. The dropout voltage is defined as V_{IN} - V_{OUT}, when V_{OUT} =95%*$V_{OUT(NOM)}$
3. Reverse Current (I_{REV}) flows from V_{OUT} to V_{IN}
4. V_{OUT} Pin Sink Current (I_{REVS}) flows from V_{OUT} to GND

SIMPLIFIED BLOCK DIAGRAM:



DETAIL OPERATION DESCRIPTION:

The LW83XX Series is a low noise, high PSRR, low drop-out voltage regulator. It consists of a current limiter circuit, a driver transistor, a precision voltage reference, a thermal protection circuit, a power-good circuit and an error correction circuit, and is compatible with low ESR ceramic capacitors. The current limiter's fold-back circuit operates as a short circuit protection as well as the output current limiter.

Reverse Current Protection

The LW83XX series includes reverse current protection to prevent the damage as a result of current flow from VOUT pin to the VIN pin when the power supply is connected to the VOUT pin. When VIN is smaller than VOUT, the reverse current protection works and suppress the reverse current to 0.5μA (MAX.). When VIN is smaller than VOUT, the VOUT pin sink current flowing from the VOUT pin to the GND pin is 1.5μA (TYP.) as the IC operation current. Please also note when an external power supply is connected to the VOUT pin, please use it within 7.0V.

Current Limiting and Short-Circuit Protection

The current limit circuitry prevents damage to the MOSFET switch and the hub downstream port but can deliver load current up to the current limit threshold through the switch. When a heavy load or short circuit is applied to an enabled switch, a large transient current may flow until the current limit circuitry responds. Once this current limit threshold is exceeded the device enters constant current mode until the thermal shutdown occurs or the fault is removed.

Thermal Shutdown

When the junction temperature of the built-in driver transistor reaches the temperature limit level (160℃ TYP.), the thermal shutdown circuit operates and the driver transistor will be turned OFF. The IC resumes its operation when the thermal shutdown function is released and the IC's operation is automatically restored because the junction temperature drops to the level of 140℃ (TYP.) as the thermal shutdown release threshold.

Chip Enable

The LW83XX series can be turned off via the EN pin signal. In shutdown mode, output at the VOUT pin will be pulled down to the GND.

The LW83XX series has a pull-down resistor in order to operate normally even the EN pin is floating.

If the EN pin voltage is set with the specified voltage range, the logic is fixed and the IC will operate normally.

Power-Good Function

The power-good circuit monitors the voltage at the VOUT pin to indicate the status of the output voltage. When the output voltage falls below the PG threshold voltage (PG_{LTH}), the PG pin open-drain output engages and pulls the PG pin close to GND. When the output voltage exceeds PG_{HTH}, the PG pin becomes high impedance. By connecting a pullup resistor to an external supply, any downstream device can receive power-good as a logic signal that can be used for sequencing. Make sure that the external pullup supply voltage results in a valid logic signal for the receiving device. Using a pullup resistor from 10 kΩ to 100 kΩ is recommended.

TYPICAL OPERATING CHARACTERISTICS:

Tested under V_{IN}=V_{OUT}+1.0V or 2.0V (whichever is greater), C_{IN}=C_{OUT}=1uF and T_A =25°C, unless otherwise specified

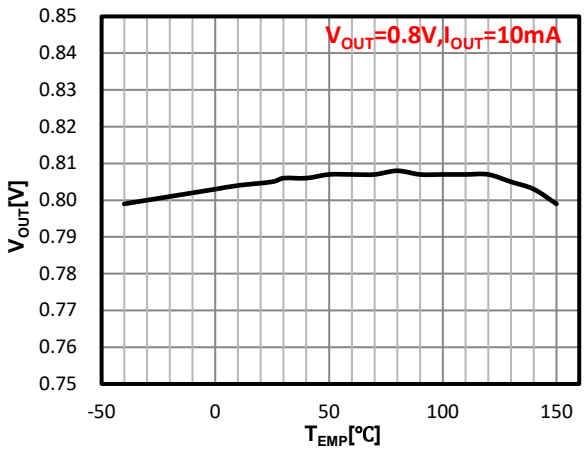


Figure 1. V_{OUT} vs Temperature

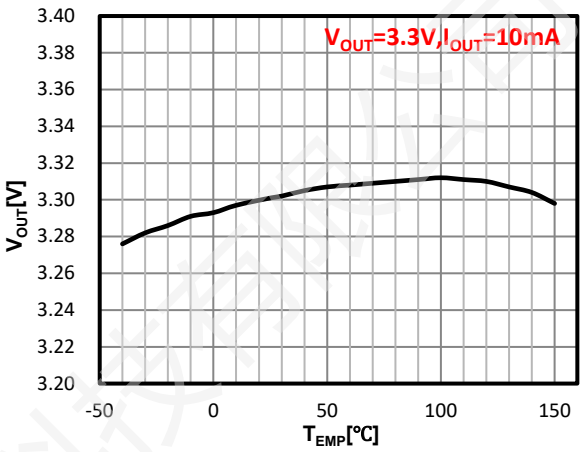


Figure 2. V_{OUT} vs Temperature

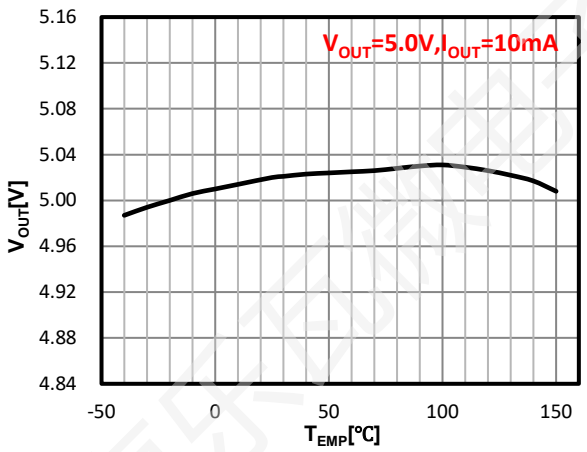
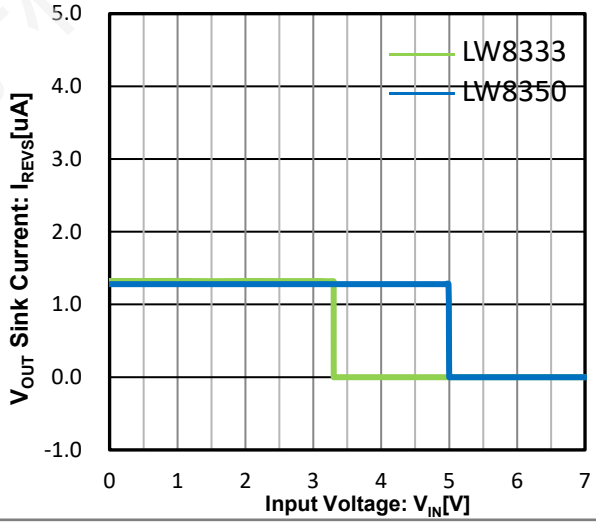


Figure 3. V_{OUT} vs Temperature



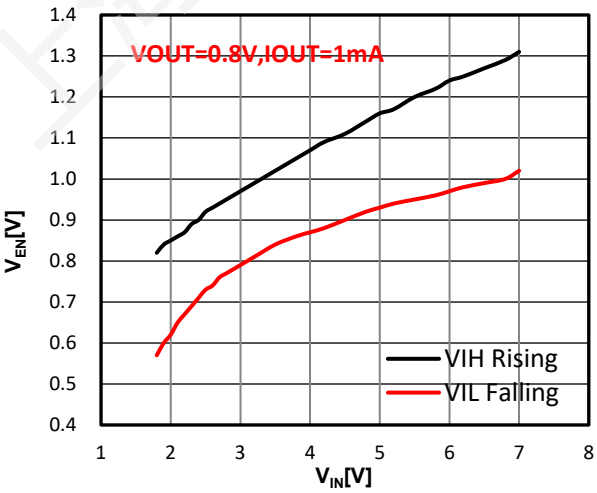


Figure 5. V_{EN} vs V_{IN}

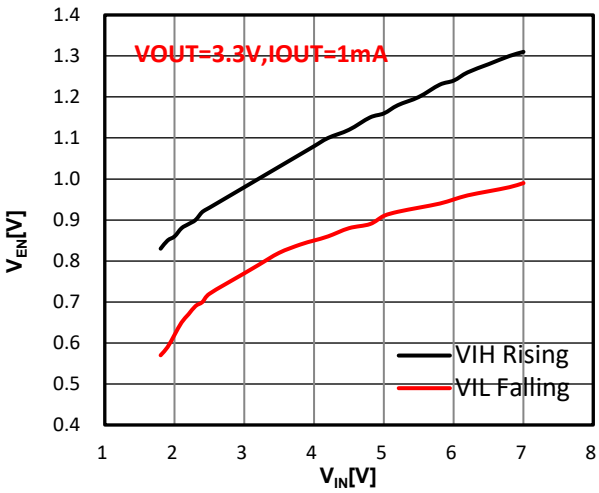


Figure 6. V_{EN} vs V_{IN}

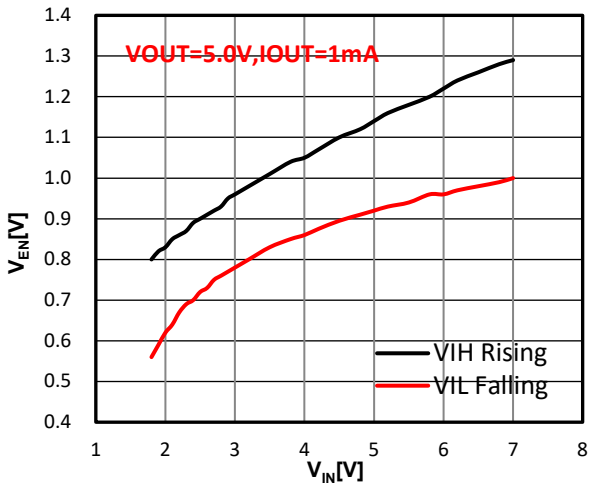


Figure 7. V_{EN} vs V_{IN}

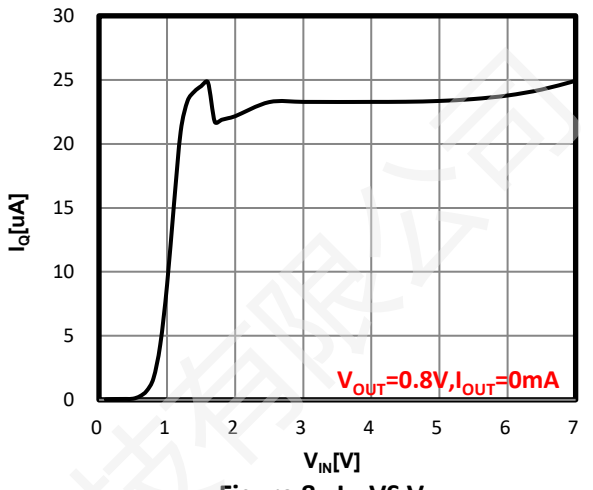


Figure 8. I_Q VS V_{IN}

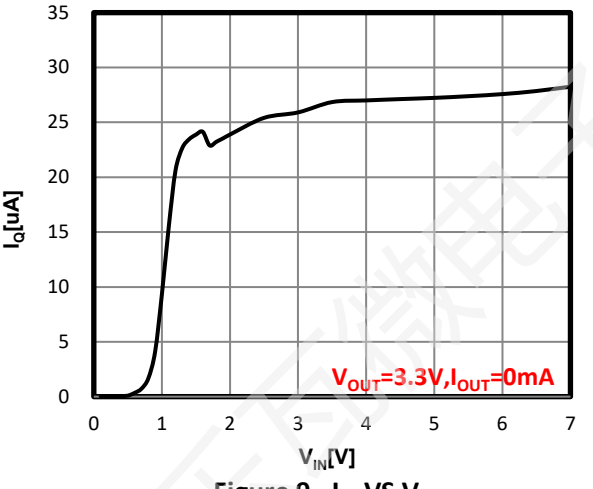


Figure 9. I_Q VS V_{IN}

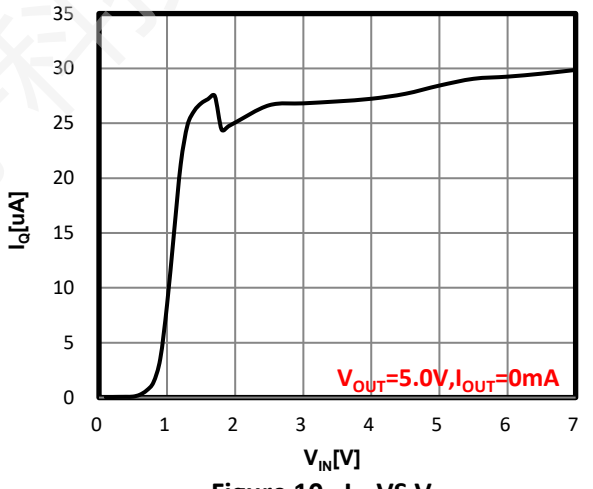


Figure 10. I_Q VS V_{IN}

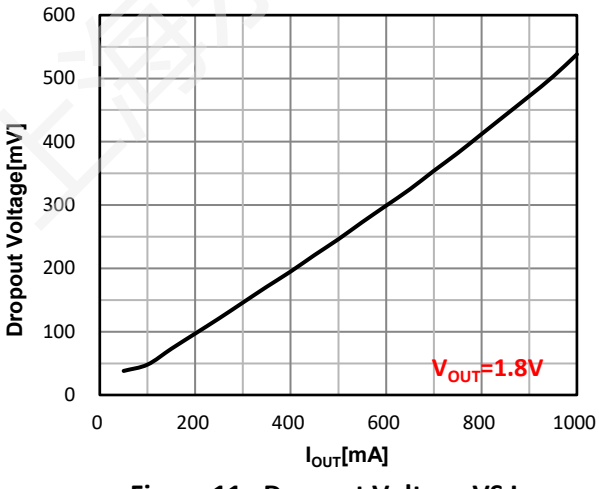


Figure 11. Dropout Voltage VS I_{OUT}

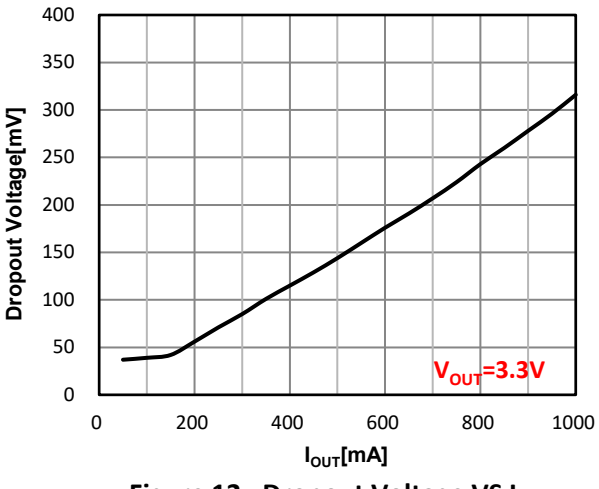


Figure 12. Dropout Voltage VS I_{OUT}

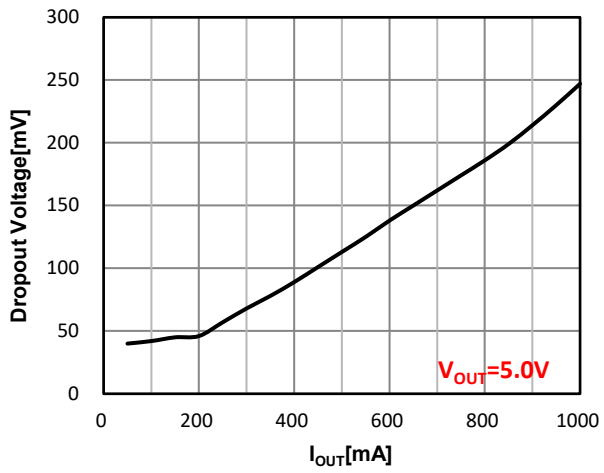


Figure 13. Dropout Voltage VS I_{OUT}

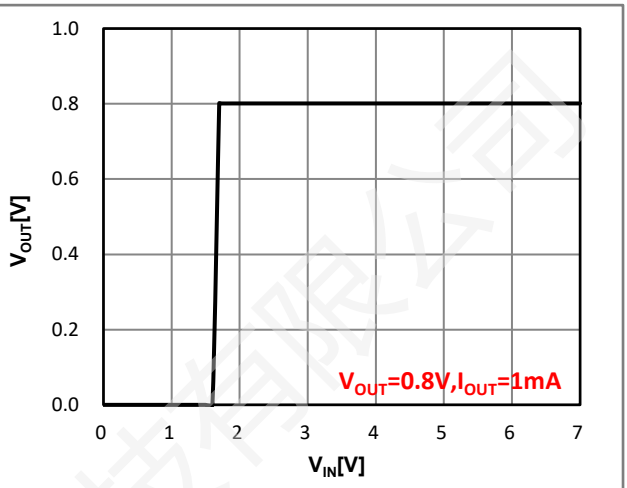


Figure 14. V_{OUT} VS V_{IN}

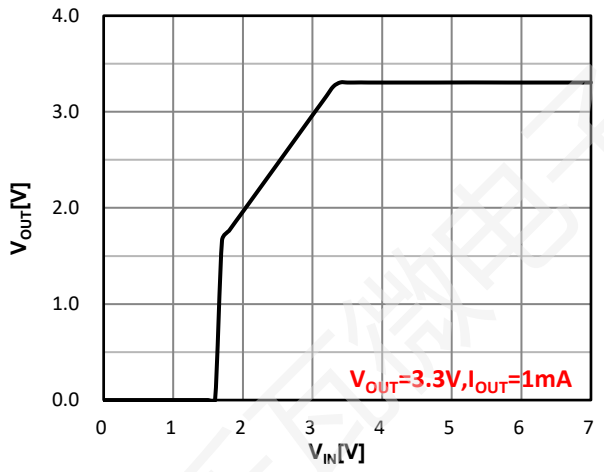


Figure 15. V_{OUT} VS V_{IN}

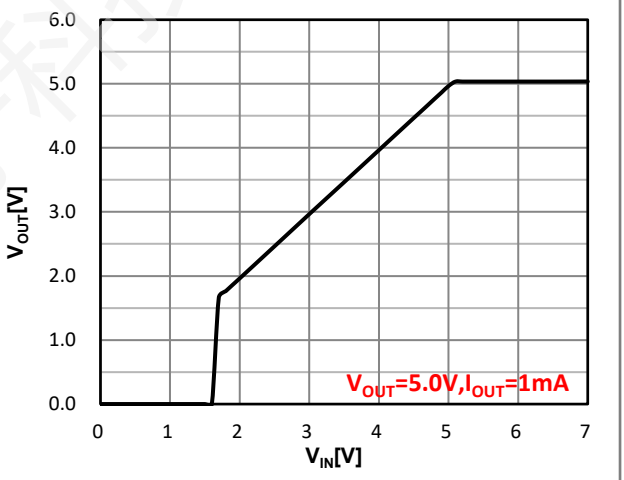
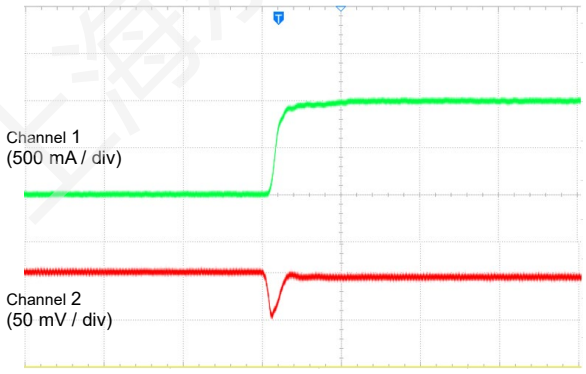
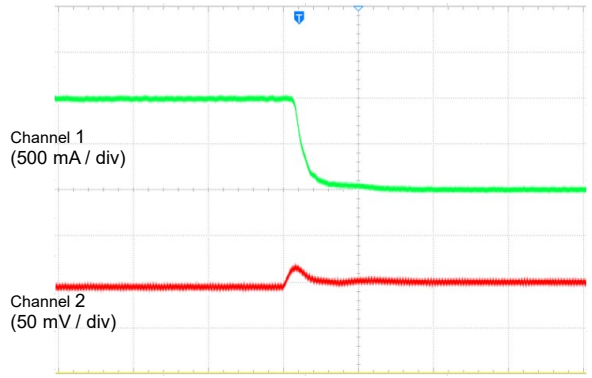


Figure 16. V_{OUT} VS V_{IN}



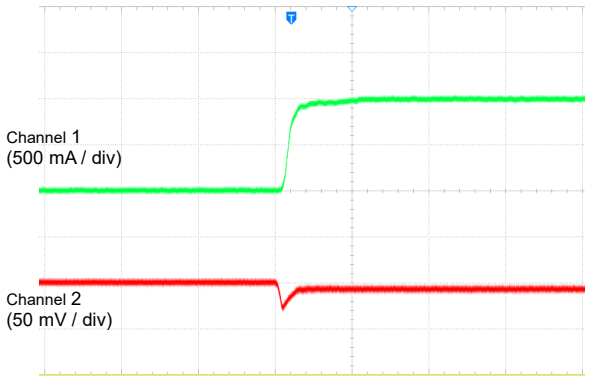
Channel 1 = I_{OUT}, channel 2 =V_{OUT}, V_{IN}=1.8V, V_{OUT}=0.8V

Figure 17. Load Transient (1 mA to 1000 mA)



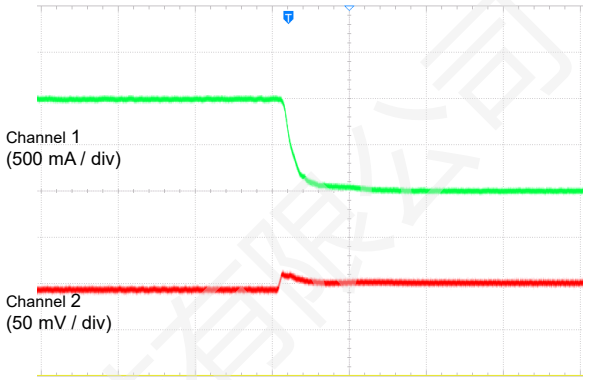
Channel 1 = I_{OUT}, channel 2 =V_{OUT}, V_{IN}=1.8V, V_{OUT}=0.8V

Figure 18. Load Transient (1000 mA to 1 mA)



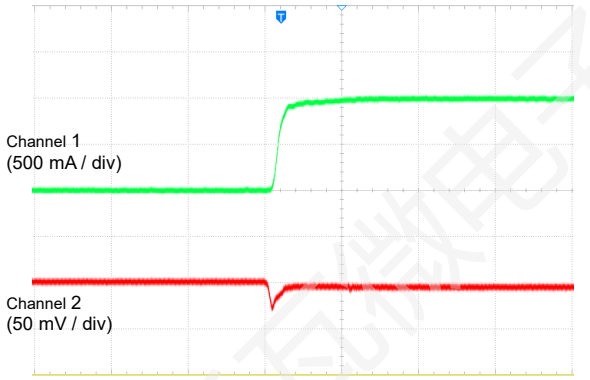
Channel 1 = I_{OUT}, channel 2 =V_{OUT}, V_{IN}=4.3V, V_{OUT}=3.3V

Figure 19. Load Transient (1 mA to 1000 mA)



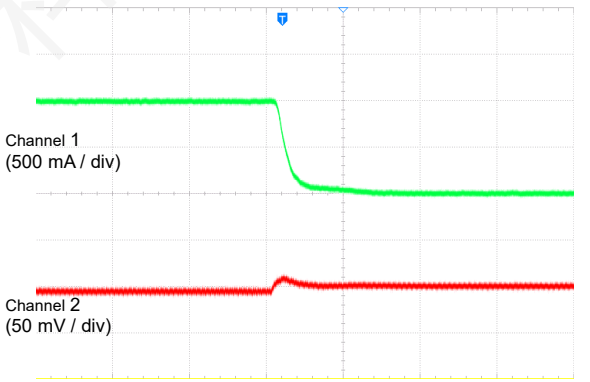
Channel 1 = I_{OUT}, channel 2 =V_{OUT}, V_{IN}=4.3V, V_{OUT}=3.3V

Figure 20. Load Transient (1000 mA to 1 mA)



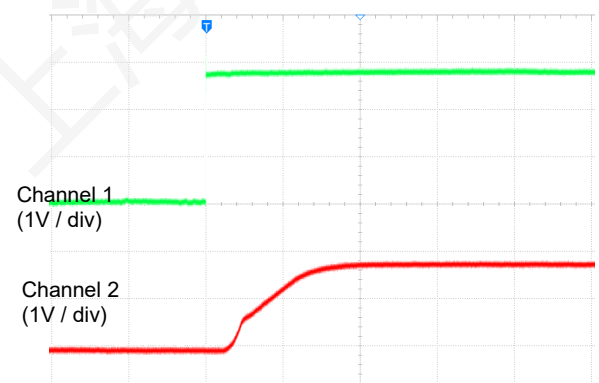
Channel 1 = I_{OUT}, channel 2 =V_{OUT}, V_{IN}=6.0V, V_{OUT}=5.0V

Figure 21. Load Transient (1 mA to 1000 mA)



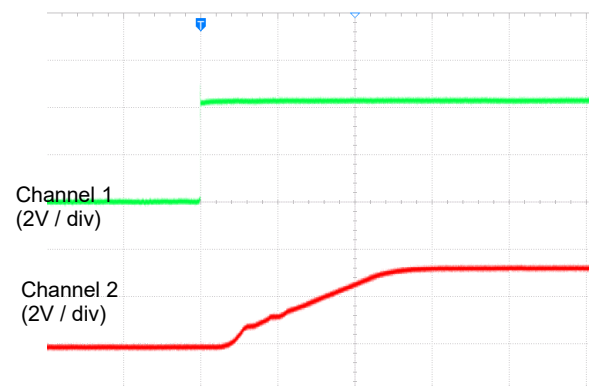
Channel 1 = I_{OUT}, channel 2 =V_{OUT}, V_{IN}=6.0V, V_{OUT}=5.0V

Figure 22. Load Transient (1000 mA to 1 mA)



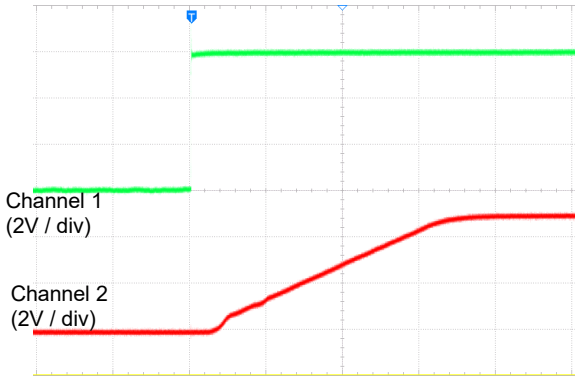
Channel 1 = En, channel 2 =V_{OUT}, V_{OUT}=1.8V, No Load

Figure 23. Power-Up with Enable



Channel 1 = En, channel 2 =V_{OUT}, V_{OUT}=3.3V, No Load

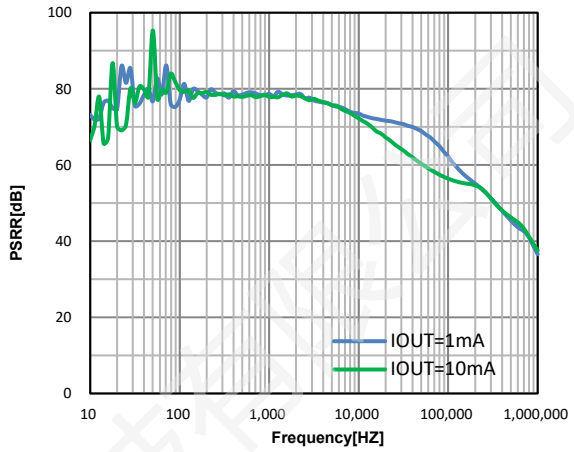
Figure 24. Power-Up with Enable



Time (20 us / div)

Channel 1 = En, channel 2 =V_{OUT}, V_{OUT}=5.0V, No Load

Figure 25. Power-Up with Enable



V_{OUT}=3.3V, V_{IN}=4.3V, V_{PP}=0.2V,C_{IN}=non, C_{OUT}=1uF

Figure 26. PSRR vs Frequency

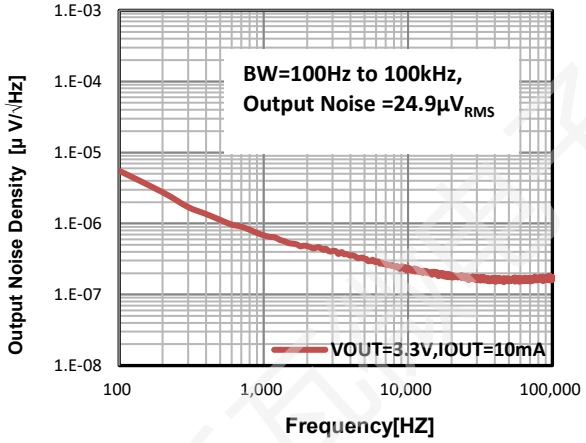


Figure 27. Output Noise Density vs Frequency

APPLICATION INFORMATION:

- **Input Capacitor Selection**

Like any low-dropout regulator, the external capacitors used with the LW83XX Series must be carefully selected for regulator stability and performance. Using a capacitor whose value is >=1μF on the LW83XX Series input and the amount of capacitance can be increased without limit. An at least 10uF input capacitor is needed if input ripple voltage V_{PP}>1V. The input capacitor must be located a distance less than 0.5 inch from the input pin of the IC and returned to a clean analog ground. Any good quality ceramic or tantalum can be used for this capacitor. The capacitor with larger value and lower ESR (equivalent series resistance) provides better PSRR and line-transient response.

- **Output Capacitor Selection**

The output capacitor must meet both requirements for minimum amount of capacitance and ESR in all LDOs application. The LW83XX Series is designed specifically to work with low ESR ceramic output capacitor in space-saving and performance consideration. Using a ceramic capacitor whose value is at least 1μF on the LW83XX Series output ensures stability. An appropriate output capacitor can reduce noise and improve load transient response and PSRR. The output capacitor should be located not more than 0.5 inch from the VOUT pin of the LW83XX Series and returned to a clean analog ground.

- **Layout considerations**

To improve ac performance such as PSRR, output noise, and transient response, it is recommended that the PCB be designed with separate ground planes for VIN and VOUT, with each ground plane connected only at the GND pin of the device.

ORDER INFORMATION:

LW83①②③④⑤⑥

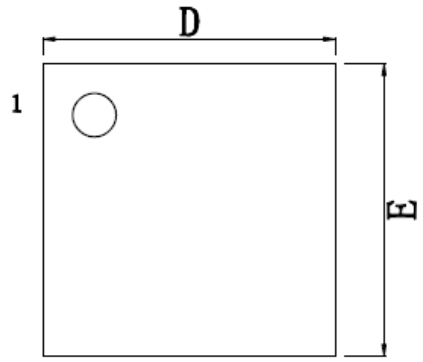
Designator	Item	Symbol	Description
①②	Output Voltage	08~50	e.g.1.2V→①=1,②=2
③④⑤⑥	Packages	T22G	DFN2x2-6L(Type-T)

Part #	Output Voltage	Package	Shipping
LW8308T22G	0.8V	DFN2x2-6L	3000 Pcs/ Tape & Reel
LW8312T22G	1.2V		
LW8315T22G	1.5V		
LW8318T22G	1.8V		
LW8328T22G	2.8V		
LW8330T22G	3.0V		
LW8333T22G	3.3V		
LW8336T22G	3.6V		
LW8342T22G	4.2V		
LW8350T22G	5.0V		

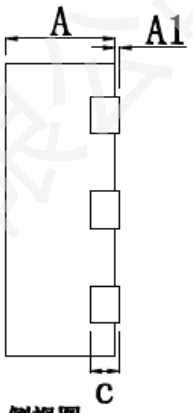
If customers have special output voltage requirements, please contact us.

PACKAGE OUTLINE:

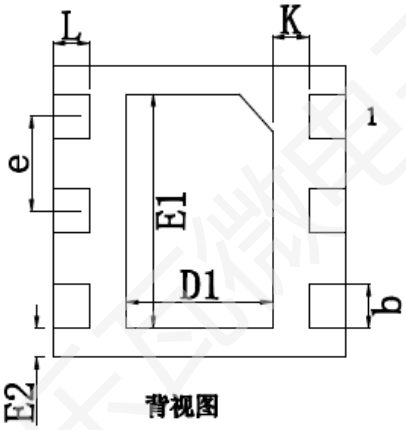
DFN2x2-6L Package



俯视图



侧视图



背视图

SYMBOL	MILLMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	—	0.05
b	0.25	0.30	0.35
c	0.203 TYP		
D	1.95	2.00	2.05
D1	0.90	1.00	1.10
E	1.95	2.00	2.05
E1	1.55	1.60	1.65
E2	0.20 REF		
e	0.65 BSC		
K	0.25 REF		
L	0.20	0.25	0.30

Revision History:

Revision	Date	Descriptions
Rev 0.1	Sep.2023	Initial Version
Rev 1.0	Nov.2024	Formal Version

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Mailing Address: Unit 02&04&05, 10th Floor, Building 5, No.666 Shengxia Road, No.122 Yindong Road,
China (Shanghai) Pilot Free Trade Zone
Shanghai Lewa Micro-electronics Technology Co., Ltd