

## TK39J60W5,S1VQ-VB Datasheet

### N-Channel 600V (D-S) Super Junction Power MOSFET

#### PRODUCT SUMMARY

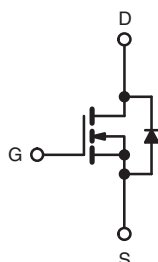
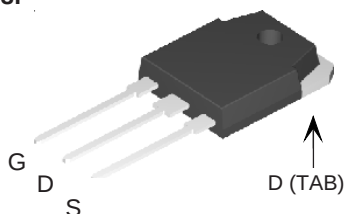
|                                    |                 |      |
|------------------------------------|-----------------|------|
| $V_{DS}$ (V) at $T_J$ max.         | 600             |      |
| $R_{DS(on)}$ at 25 °C ( $\Omega$ ) | $V_{GS} = 10$ V | 0.06 |
| $Q_g$ max. (nC)                    | 273             |      |
| $Q_{gs}$ (nC)                      | 46              |      |
| $Q_{gd}$ (nC)                      | 79              |      |
| Configuration                      | Single          |      |

#### FEATURES

- Low figure-of-merit (FOM)  $R_{on} \times Q_g$
- Low input capacitance ( $C_{iss}$ )
- Reduced switching and conduction losses
- Ultra low gate charge ( $Q_g$ )
- Avalanche energy rated (UIS)



TO-3P



N-Channel MOSFET

#### APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
  - High-intensity discharge (HID)
  - Fluorescent ballast lighting
- Industrial
  - Welding
  - Induction heating
  - Motor drives
  - Battery chargers
  - Renewable energy
  - Solar (PV inverters)

#### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

| PARAMETER                                                 | SYMBOL           | LIMIT          | UNIT |
|-----------------------------------------------------------|------------------|----------------|------|
| Drain-Source Voltage                                      | $V_{DS}$         | 600            | V    |
| Gate-Source Voltage                                       | $V_{GS}$         | $\pm 30$       |      |
| Continuous Drain Current ( $T_J = 150$ °C)                | $V_{GS}$ at 10 V | $T_C = 25$ °C  | A    |
|                                                           |                  | $T_C = 100$ °C |      |
| Pulsed Drain Current <sup>a</sup>                         | $I_{DM}$         | 142            |      |
| Linear Derating Factor                                    |                  | 3.3            | W/°C |
| Single Pulse Avalanche Energy <sup>b</sup>                | $E_{AS}$         | 1410           | mJ   |
| Maximum Power Dissipation                                 | $P_D$            | 415            | W    |
| Operating Junction and Storage Temperature Range          | $T_J, T_{stg}$   | -55 to +150    | °C   |
| Drain-Source Voltage Slope                                | $dV/dt$          | $T_J = 125$ °C | V/ns |
| Reverse Diode $dV/dt$ <sup>d</sup>                        |                  |                |      |
| Soldering Recommendations (Peak Temperature) <sup>c</sup> |                  | for 10 s       |      |

#### Notes

- Repetitive rating; pulse width limited by maximum junction temperature.
- $V_{DS} = 50$  V, starting  $T_J = 25$  °C,  $L = 28.2$  mH,  $R_g = 25$   $\Omega$ ,  $I_{AS} = 10$  A.
- 1.6 mm from case.
- $I_{SD} \leq I_D$ ,  $dI/dt = 100$  A/ $\mu$ s, starting  $T_J = 25$  °C.

**THERMAL RESISTANCE RATINGS**

| PARAMETER                        | SYMBOL     | TYP. | MAX. | UNIT |
|----------------------------------|------------|------|------|------|
| Maximum Junction-to-Ambient      | $R_{thJA}$ | -    | 40   | °C/W |
| Maximum Junction-to-Case (Drain) | $R_{thJC}$ | -    | 0.3  |      |

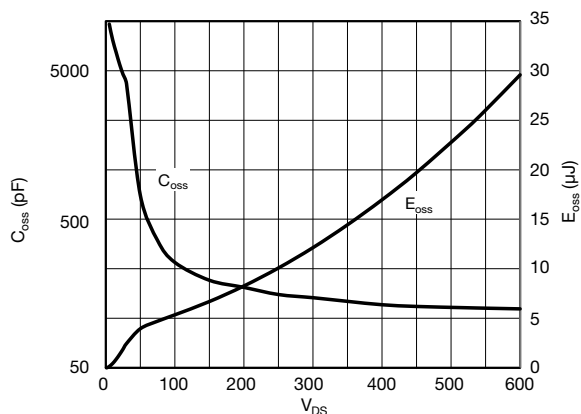
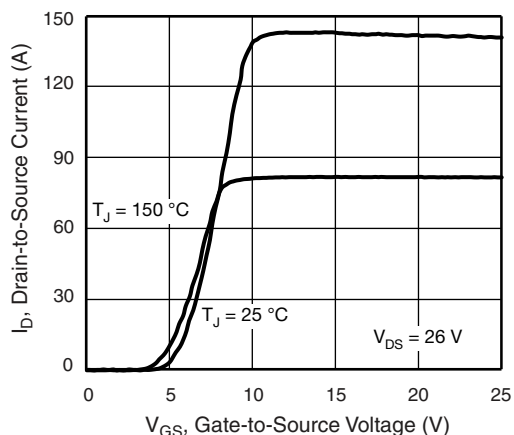
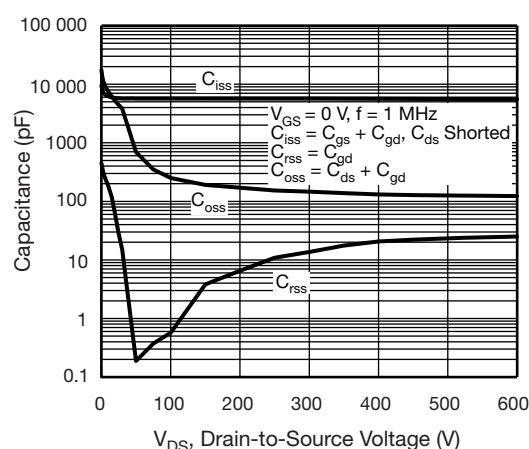
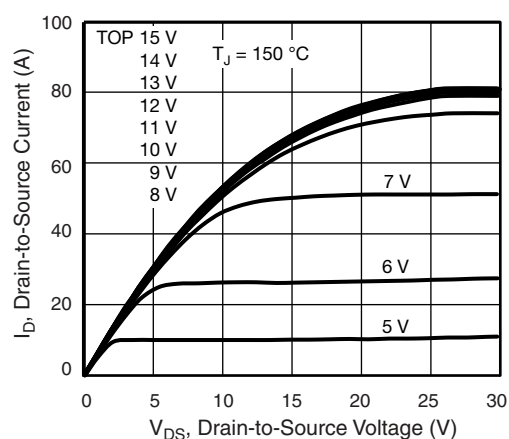
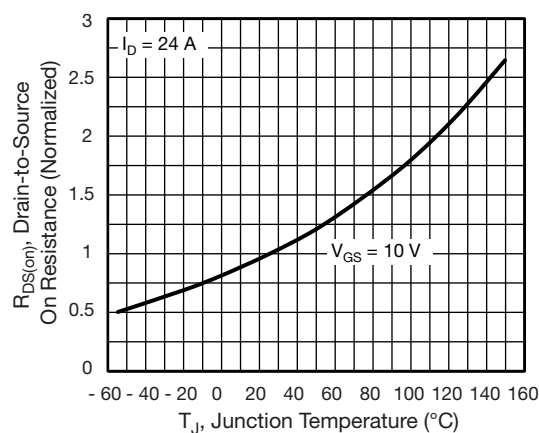
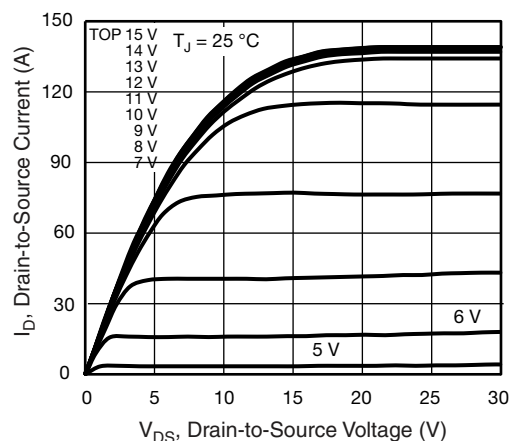
**SPECIFICATIONS** ( $T_J = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

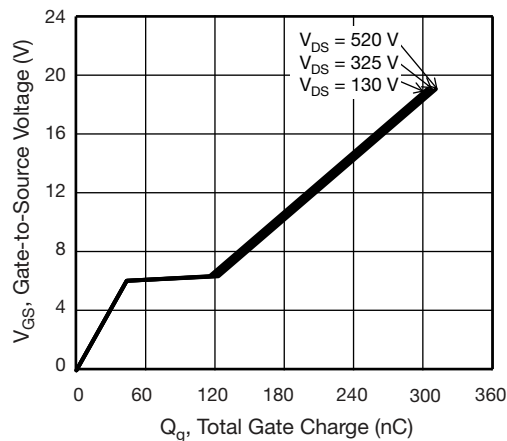
| PARAMETER                                                 | SYMBOL              | TEST CONDITIONS                                                                                                                                          |                                               | MIN. | TYP. | MAX.      | UNIT                  |
|-----------------------------------------------------------|---------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------|------|------|-----------|-----------------------|
| Static                                                    |                     |                                                                                                                                                          |                                               |      |      |           |                       |
| Drain-Source Breakdown Voltage                            | $V_{DS}$            | $V_{GS} = 0\text{ V}$ , $I_D = 250\text{ }\mu\text{A}$                                                                                                   |                                               | 600  | -    | -         | V                     |
| $V_{DS}$ Temperature Coefficient                          | $\Delta V_{DS}/T_J$ | Reference to $25\text{ }^{\circ}\text{C}$ , $I_D = 1\text{ mA}$                                                                                          |                                               | -    | 0.70 | -         | V/ $^{\circ}\text{C}$ |
| Gate-Source Threshold Voltage (N)                         | $V_{GS(th)}$        | $V_{DS} = V_{GS}$ , $I_D = 250\text{ }\mu\text{A}$                                                                                                       |                                               | 2    | -    | 4         | V                     |
| Gate-Source Leakage                                       | $I_{GSS}$           | $V_{GS} = \pm 20\text{ V}$                                                                                                                               |                                               | -    | -    | $\pm 100$ | nA                    |
|                                                           |                     | $V_{GS} = \pm 30\text{ V}$                                                                                                                               |                                               | -    | -    | $\pm 1$   | $\mu\text{A}$         |
| Zero Gate Voltage Drain Current                           | $I_{DSS}$           | $V_{DS} = 650\text{ V}$ , $V_{GS} = 0\text{ V}$                                                                                                          |                                               | -    | -    | 1         | $\mu\text{A}$         |
|                                                           |                     | $V_{DS} = 520\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 125\text{ }^{\circ}\text{C}$                                                                    |                                               | -    | -    | 25        |                       |
| Drain-Source On-State Resistance                          | $R_{DS(on)}$        | $V_{GS} = 10\text{ V}$                                                                                                                                   | $I_D = 24\text{ A}$                           | -    | 0.06 | -         | $\Omega$              |
| Forward Transconductance                                  | $g_{fs}$            | $V_{DS} = 30\text{ V}$ , $I_D = 24\text{ A}$                                                                                                             |                                               | -    | 16.7 | -         | S                     |
| Dynamic                                                   |                     |                                                                                                                                                          |                                               |      |      |           |                       |
| Input Capacitance                                         | $C_{iss}$           | $V_{GS} = 0\text{ V}$ ,<br>$V_{DS} = 100\text{ V}$ ,<br>$f = 1\text{ MHz}$                                                                               |                                               | -    | 5682 | -         | pF                    |
| Output Capacitance                                        | $C_{oss}$           |                                                                                                                                                          |                                               | -    | 251  | -         |                       |
| Reverse Transfer Capacitance                              | $C_{rss}$           |                                                                                                                                                          |                                               | -    | 1    | -         |                       |
| Effective Output Capacitance, Energy Related <sup>a</sup> | $C_{o(er)}$         | $V_{DS} = 0\text{ V to } 520\text{ V}$ , $V_{GS} = 0\text{ V}$                                                                                           |                                               | -    | 192  | -         |                       |
| Effective Output Capacitance, Time Related <sup>b</sup>   | $C_{o(tr)}$         |                                                                                                                                                          |                                               | -    | 665  | -         |                       |
| Total Gate Charge                                         | $Q_g$               | $V_{GS} = 10\text{ V}$                                                                                                                                   | $I_D = 24\text{ A}$ , $V_{DS} = 520\text{ V}$ | -    | 182  | 273       | nC                    |
| Gate-Source Charge                                        | $Q_{gs}$            |                                                                                                                                                          |                                               | -    | 46   | -         |                       |
| Gate-Drain Charge                                         | $Q_{gd}$            |                                                                                                                                                          |                                               | -    | 79   | -         |                       |
| Turn-On Delay Time                                        | $t_{d(on)}$         | $V_{DD} = 520\text{ V}$ , $I_D = 6\text{ A}$ ,<br>$V_{GS} = 10\text{ V}$ , $R_g = 9.1\text{ }\Omega$                                                     |                                               | -    | 47   | 94        | ns                    |
| Rise Time                                                 | $t_r$               |                                                                                                                                                          |                                               | -    | 87   | 131       |                       |
| Turn-Off Delay Time                                       | $t_{d(off)}$        |                                                                                                                                                          |                                               | -    | 156  | 234       |                       |
| Fall Time                                                 | $t_f$               |                                                                                                                                                          |                                               | -    | 103  | 206       |                       |
| Gate Input Resistance                                     | $R_g$               | $f = 1\text{ MHz}$ , open drain                                                                                                                          |                                               | -    | 0.64 | -         | $\Omega$              |
| Drain-Source Body Diode Characteristics                   |                     |                                                                                                                                                          |                                               |      |      |           |                       |
| Continuous Source-Drain Diode Current                     | $I_S$               | MOSFET symbol showing the integral reverse p - n junction diode<br> |                                               | -    | -    | 47        | A                     |
| Pulsed Diode Forward Current                              | $I_{SM}$            |                                                                                                                                                          |                                               | -    | -    | 139       |                       |
| Diode Forward Voltage                                     | $V_{SD}$            | $T_J = 25\text{ }^{\circ}\text{C}$ , $I_S = 24\text{ A}$ , $V_{GS} = 0\text{ V}$                                                                         |                                               | -    | 0.9  | 1.2       | V                     |
| Reverse Recovery Time                                     | $t_{rr}$            | $T_J = 25\text{ }^{\circ}\text{C}$ , $I_F = I_S = 24\text{ A}$ ,<br>$di/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 25\text{ V}$                             |                                               | -    | 753  | 1506      | ns                    |
| Reverse Recovery Charge                                   | $Q_{rr}$            |                                                                                                                                                          |                                               | -    | 14   | 28        | $\mu\text{C}$         |
| Reverse Recovery Current                                  | $I_{RRM}$           |                                                                                                                                                          |                                               | -    | 28   | -         | A                     |

**Notes**

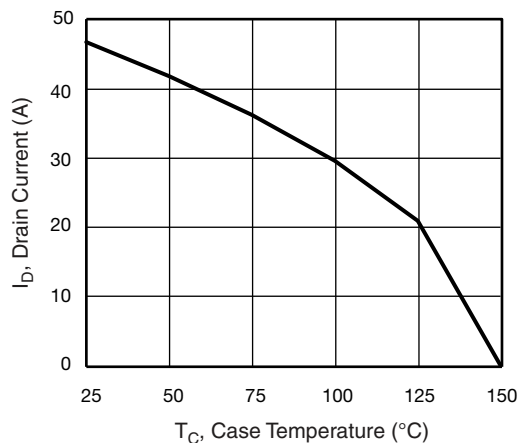
- a.  $C_{oss(er)}$  is a fixed capacitance that gives the same energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DSS}$ .  
 b.  $C_{oss(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DSS}$ .

## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

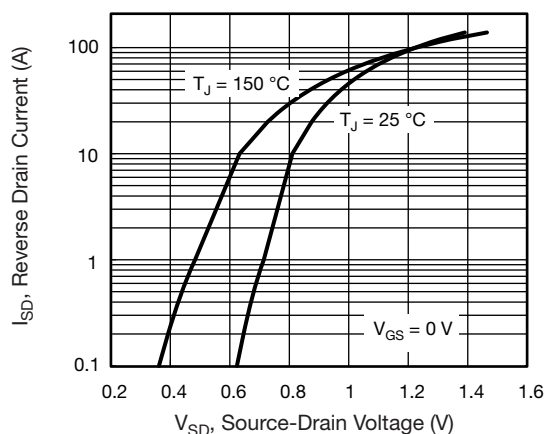




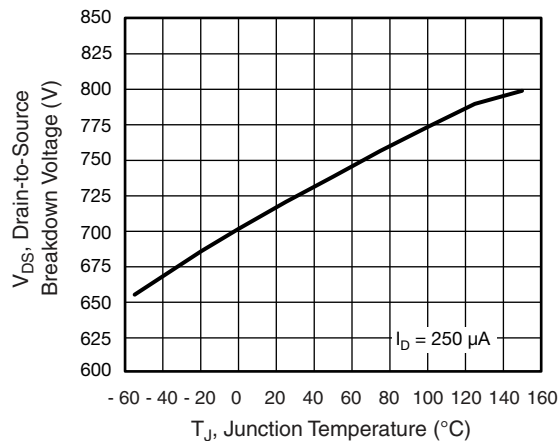
**Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage**



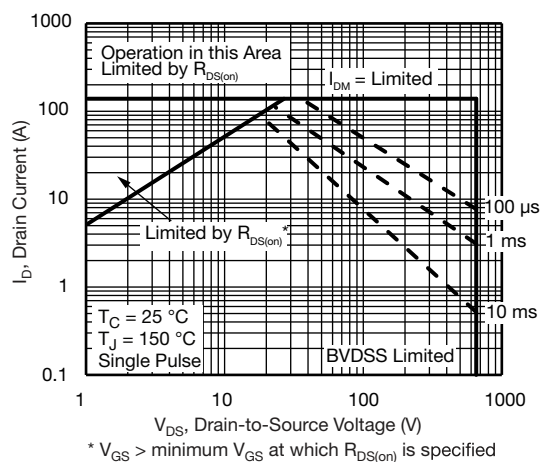
**Fig. 10 - Maximum Drain Current vs. Case Temperature**



**Fig. 8 - Typical Source-Drain Diode Forward Voltage**



**Fig. 11 - Temperature vs. Drain-to-Source Voltage**



**Fig. 9 - Maximum Safe Operating Area**

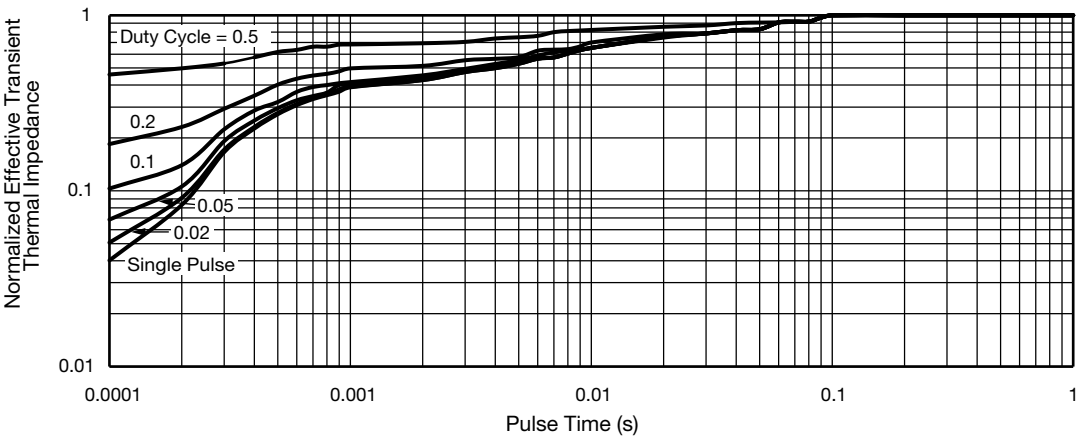


Fig. 12 - Normalized Thermal Transient Impedance, Junction-to-Case

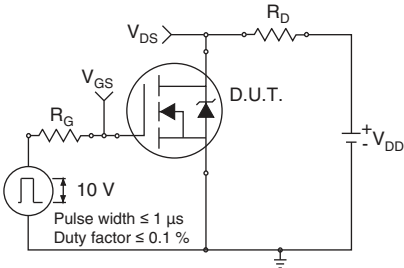


Fig. 13 - Switching Time Test Circuit

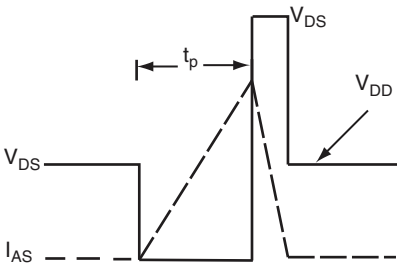


Fig. 16 - Unclamped Inductive Waveforms

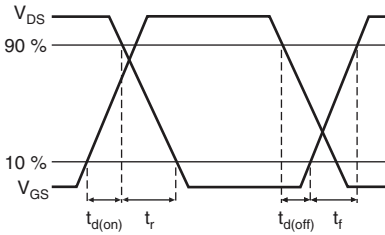


Fig. 14 - Switching Time Waveforms

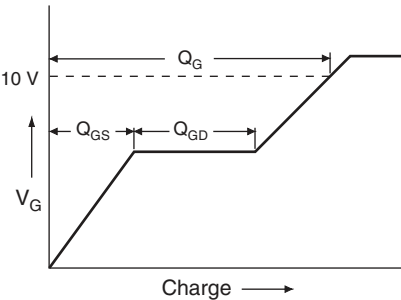


Fig. 17 - Basic Gate Charge Waveform

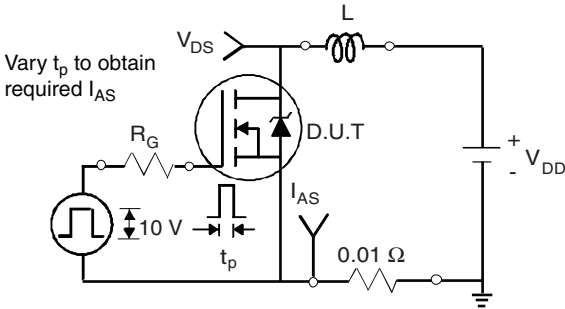


Fig. 15 - Unclamped Inductive Test Circuit

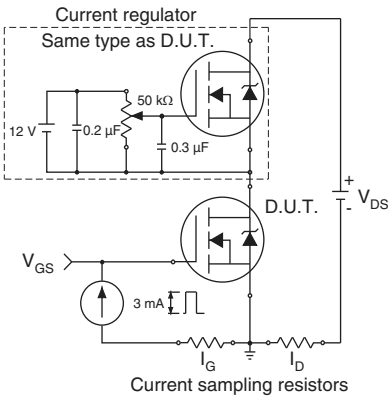


Fig. 18 - Gate Charge Test Circuit

**Note**a.  $V_{GS} = 5\text{ V}$  for logic level devices**Fig. 19 - For N-Channel**

# Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Shenzhen VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Shenzhen VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Shenzhen VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Shenzhen VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental ; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Shenzhen demand that the Shenzhen VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Shenzhen VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Shenzhen VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Shenzhen VBsemi such applications using client did not express their own risk. Contact your authorized Shenzhen VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

## Material Category Policy

Shenzhen VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Shenzhen VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Shenzhen VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Shenzhenese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.