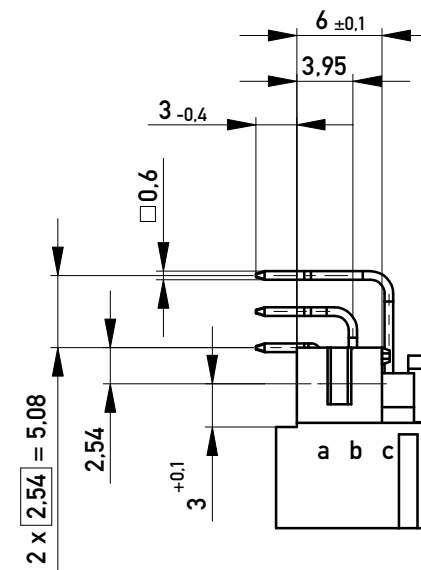




L = late mate, early break / nacheilend

The diagram illustrates a 32-bit bus architecture. At the top, 32 bit positions are labeled from 32 down to 1. Below these labels, there are 32 parallel data paths. Each path is represented by a 3x3 grid of small squares, each containing the letter 'S'. These grids are connected to a central horizontal bus structure. The entire assembly is enclosed in a rectangular frame with a central cutout.

Lötanschluss 0,6x0,6mm / solder terminal 0,6x0,6mm

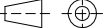
Technical drawing of a 3D printed part with a grid of holes. The part has a total length of 88.9 mm and a width of 5.3 ± 0.1 mm. It features a grid of 32 columns and 3 rows of holes. The holes are numbered 1 to 32 from right to left. The drawing includes dimensions for hole diameter (Ø 2.8 ± 0.1), hole spacing (2.54 mm), and a total length of 88.9 mm. A note indicates that all holes are 0.05 mm in diameter.

Dimensions and Tolerances:

- Hole Diameter: $\varnothing 2.8 \pm 0.1$
- Hole Spacing: 2.54
- Total Length: 88.9
- Total Width: 5.3 ± 0.1
- Hole Diameter (all holes): 0.05

Note: alle Löcher / all holes

marking see drawing 294905

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	All rights reserved. Only for Information. To ensure that this is the latest version of this drawing, please contact one of the ERNI companies before using.			Subject to modification without prior notice. Drawing will not be updated.		Designation Messerleiste C 96 Löt <i>Male Connector C96 solder</i>	
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	b		22.06.2017			Class STVC	A3
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