

General Description

The AGML315ME combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

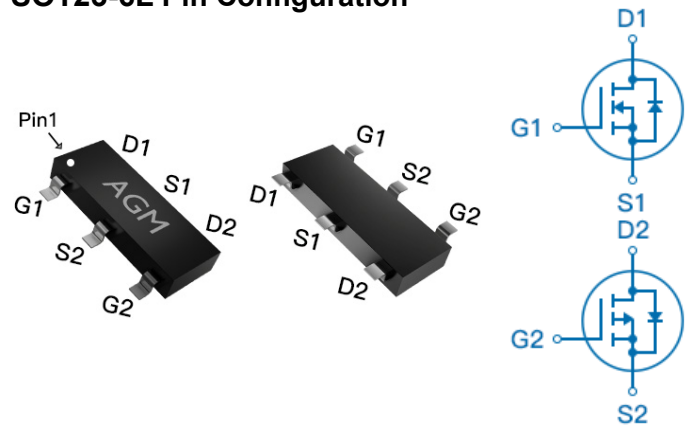
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
30V	25mΩ	5.6A
-30V	43mΩ	-4.2A

SOT23-6L Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGML315ME	AGML315ME	SOT23-6L	178mm	8mm	3000

Table 1. Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$)

Symbol	Parameter	Rating		Units
		N-Ch	P-Ch	
V_{DS}	Drain-Source Voltage ($V_{GS}=0V$)	30	-30	V
V_{GS}	Gate-Source Voltage ($V_{DS}=0V$)	± 12	± 12	V
I_D	Drain Current-Continuous($T_A=25^{\circ}\text{C}$) (Note 1)	5.6	-4.2	A
	Drain Current-Continuous($T_A=100^{\circ}\text{C}$)	3.5	-2.7	A
IDM (pluse)	Drain Current-Pulsed (Note 2)	23	-17	A
P_D	Total Power Dissipation($T_A=25^{\circ}\text{C}$)	1.28	1.28	W
EAS	Avalanche energy (Note 3)	4.0	4.0	mJ
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 To 150	-55 To 150	$^{\circ}\text{C}$

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State) ¹	---	98	$^{\circ}\text{C/W}$

Table 3. N- Channel Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	30	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=30V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±12V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	0.5	0.9	1.3	V
gFS	Forward Transconductance	VDS=5V,ID=3A	--	2	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=4A	--	25	29	mΩ
		VGS=4.5V, ID=3A	--	27	32	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=15V,VGS=0V, F=1MHZ	--	562	--	pF
Coss	Output Capacitance		--	71	--	pF
Crss	Reverse Transfer Capacitance		--	55	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	1.3	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=15V, RGEN=3Ω,ID=5.6A	--	4.4	--	nS
tr	Turn-on Rise Time		--	28	--	nS
td(off)	Turn-Off Delay Time		--	16	--	nS
tf	Turn-Off Fall Time		--	26	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=15V,ID=5.6A	--	17	--	nC
Qgs	Gate-Source Charge		--	2.1	--	nC
Qgd	Gate-Drain Charge		--	2.0	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	5.6	A
VSD	Forward on Voltage	VGS=0V,IS=4A	--	--	1.2	V
trr	Reverse Recovery Time	IF=4A , dl/dt=100A/μs , TJ=25℃	--	1.1	--	ns
Qrr	Reverse Recovery Charge		--	13.1	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: T_J=25°C, V_{DD}=15V, V_{gs}=10V, I_D=4A, L=0.5mH, R_G=25ohm

Table 3. P-Channel Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-30	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-30V,VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±12V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-0.6	--	-1.3	V
gFS	Forward Transconductance	VDS=-5V,ID=-3A	--	5	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-4A	--	43	46	mΩ
		VGS=-4.5V, ID=-3A	--	51	55	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-15V,VGS=0V, F=1MHZ	--	475	--	pF
Coss	Output Capacitance		--	74	--	pF
Crss	Reverse Transfer Capacitance		--	62	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	10.8	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-15V,VDS=-10V, ID=-4.2A,RGEN=6Ω	--	1.4	--	nS
tr	Turn-on Rise Time		--	31	--	nS
td(off)	Turn-Off Delay Time		--	43.6	--	nS
tf	Turn-Off Fall Time		--	8.5	--	nS
Qg	Total Gate Charge	VGS=-10V, VDS=-15V, ID=-4.2A	--	8.0	--	nC
Qgs	Gate-Source Charge		--	1.6	--	nC
Qgd	Gate-Drain Charge		--	2.4	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-4.2	A
VSD	Forward on Voltage	VGS=0V,IS=-4A	--	-0.8	-1.2	V
trr	Reverse Recovery Time	IF=-4A , dl/dt=100A/μs , TJ=25℃	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C,VDD=-15V,Vgs=-10V,ID=-4A, L=0.5mH,RG=25ohm

N-Channel Typical Performance Characteristics

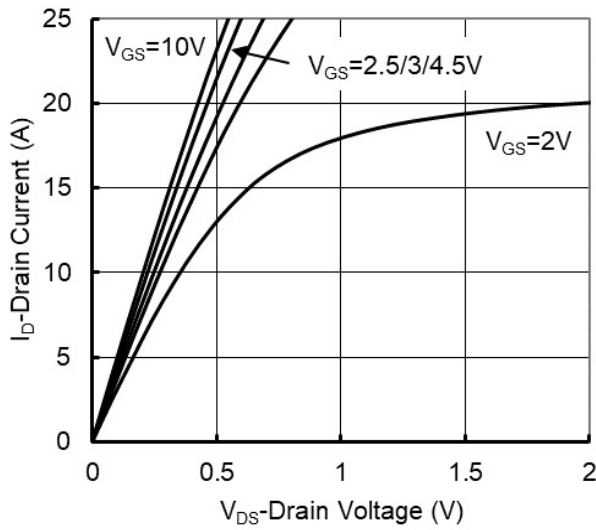


Figure1. Output Characteristics

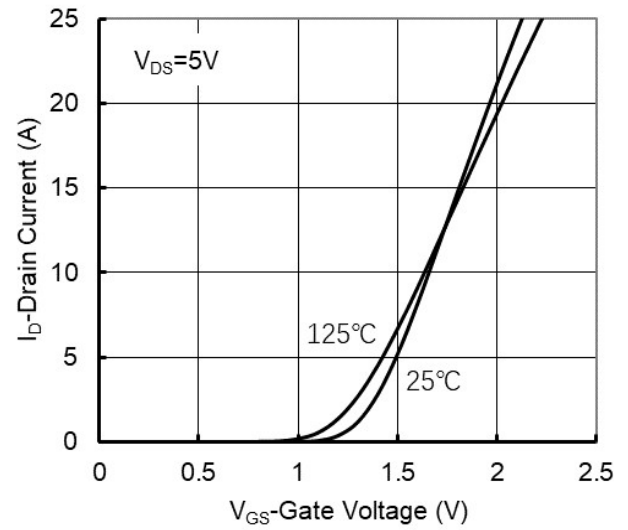


Figure2. Transfer Characteristics

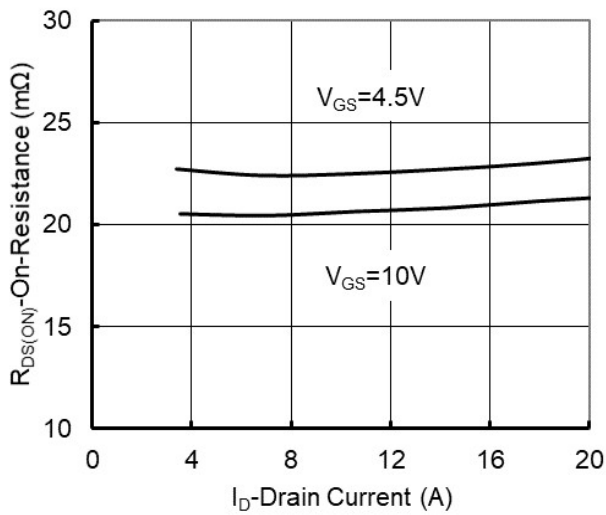


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

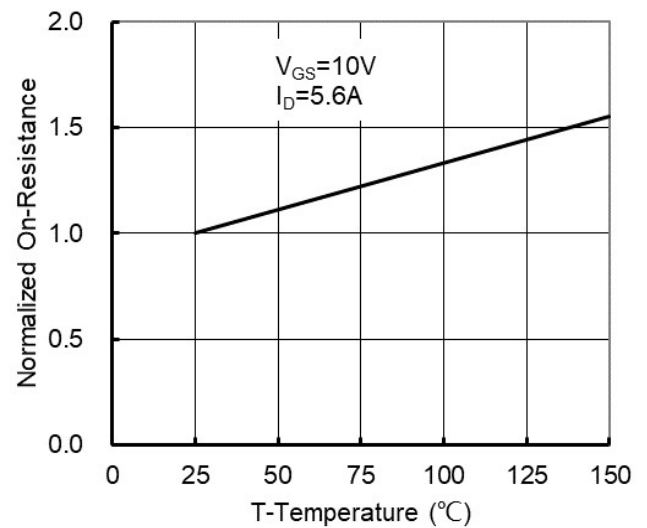


Figure 4: On-Resistance vs. Junction Temperature

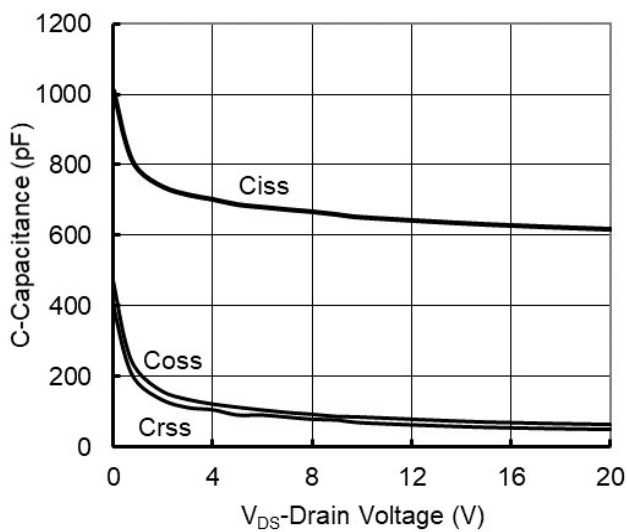


Figure5. Capacitance Characteristics

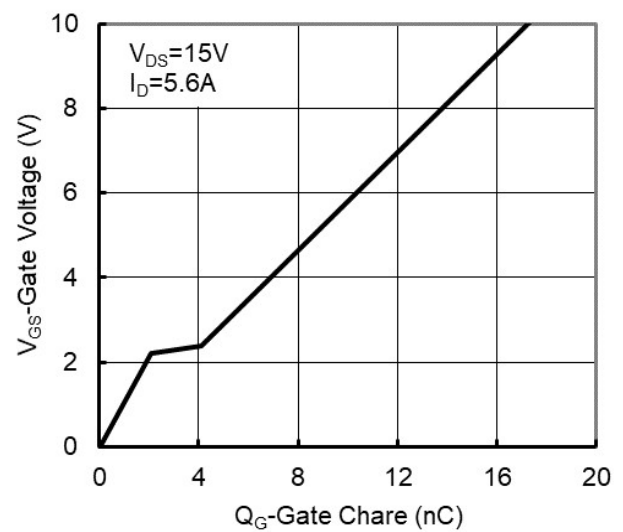


Figure6. Gate Charge

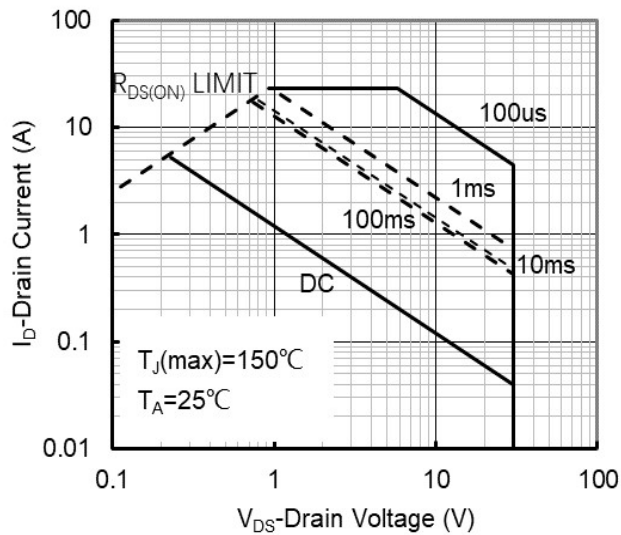


Figure7. Safe Operation Area

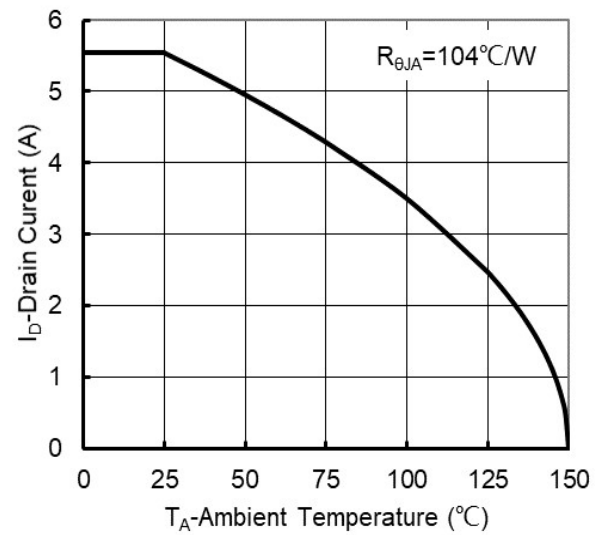


Figure8. Maximum Continuous Drain Current vs Ambient Temperature

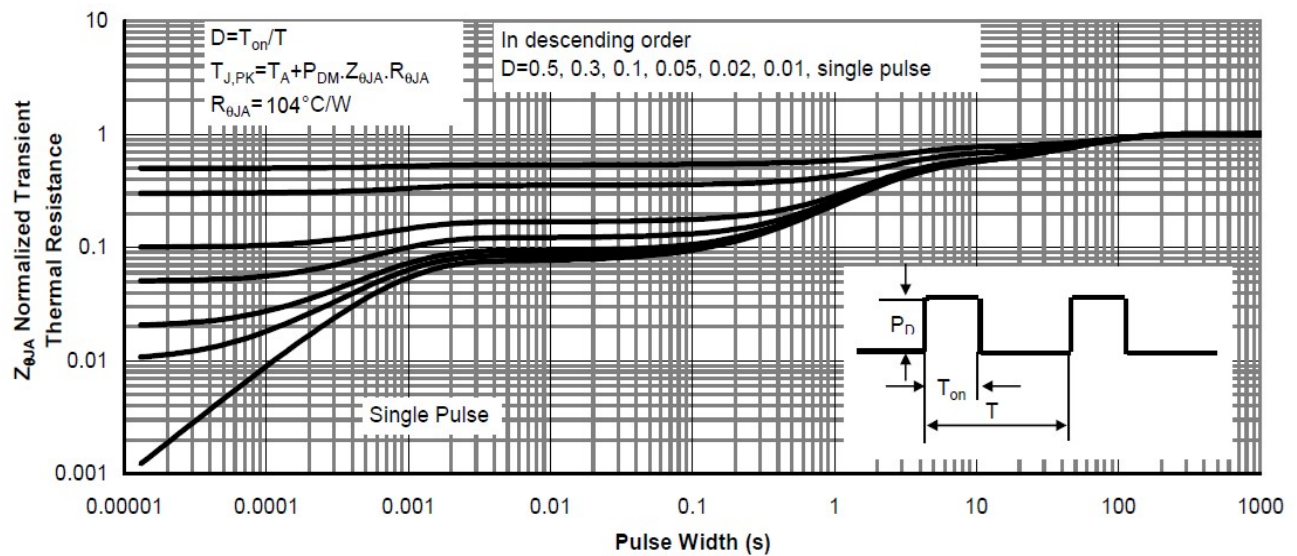
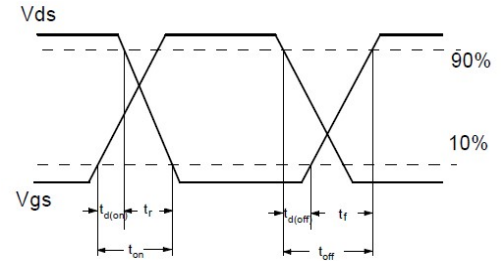
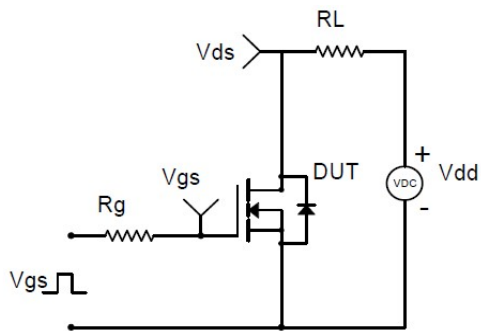
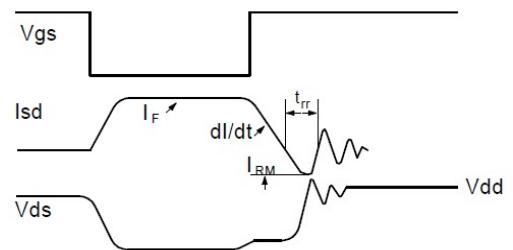
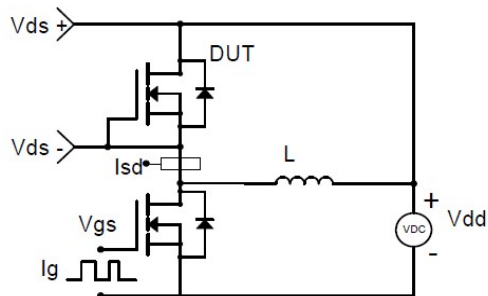
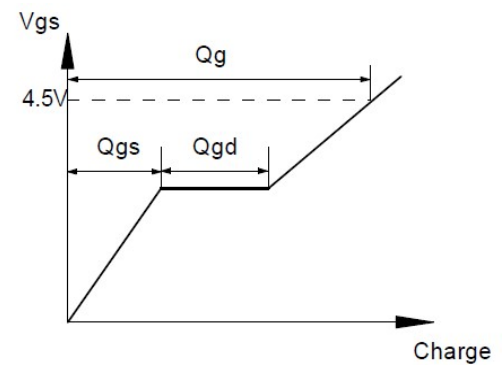
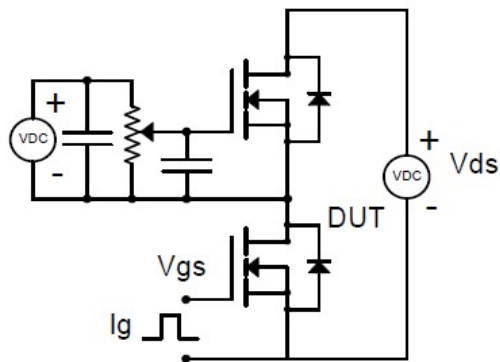
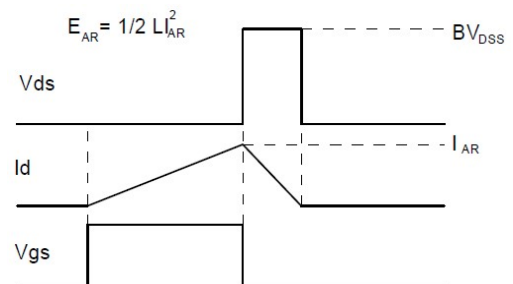
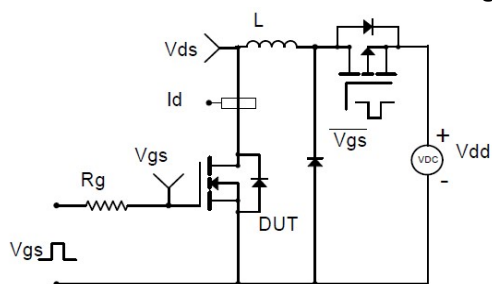
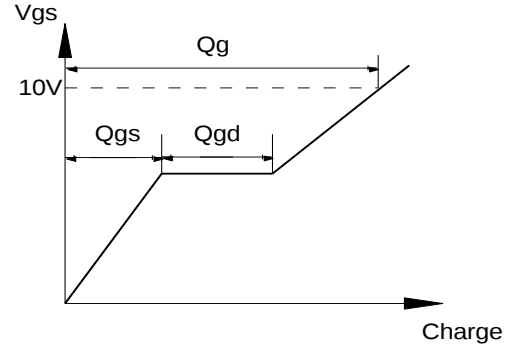
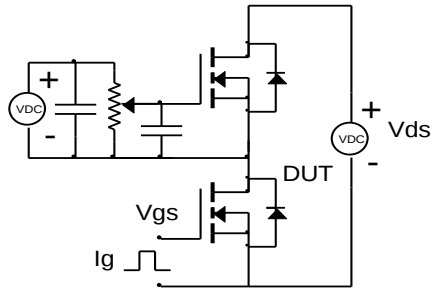


Figure9. Normalized Maximum Transient Thermal Impedance

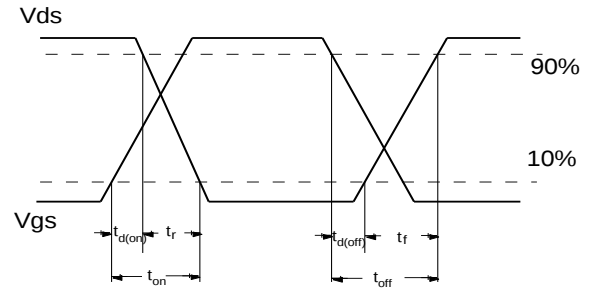
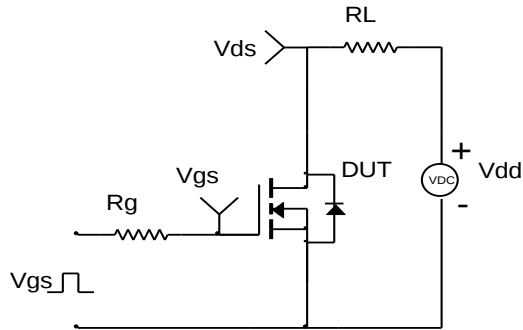

Resistive Switching Test Circuit & Waveforms

Diode Recovery Test Circuit & Waveforms

Gate Charge Test Circuit & Waveform

Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

P-Channel Test Circuit and Waveform

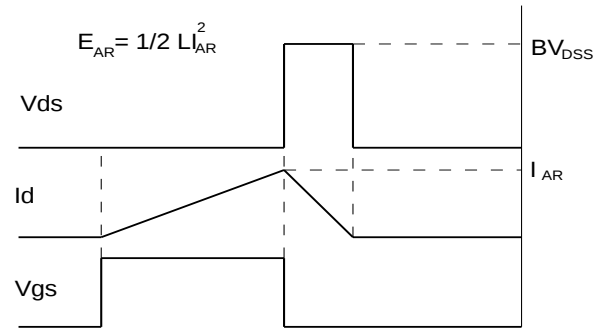
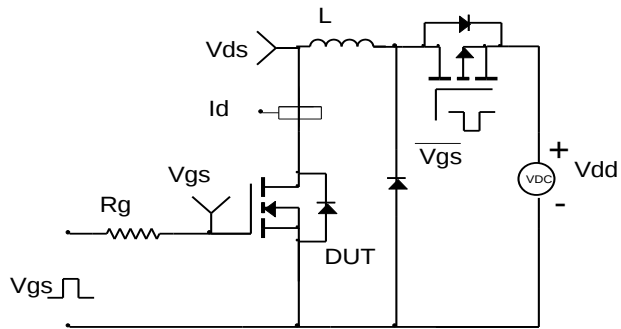
Gate Charge Test Circuit & Waveform



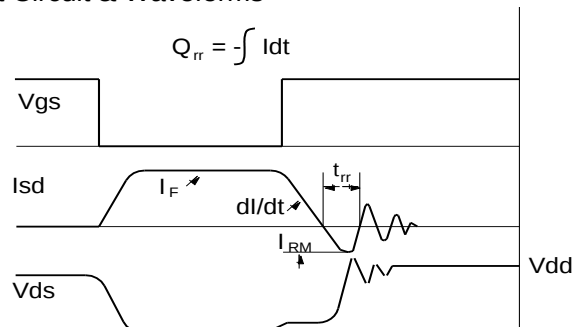
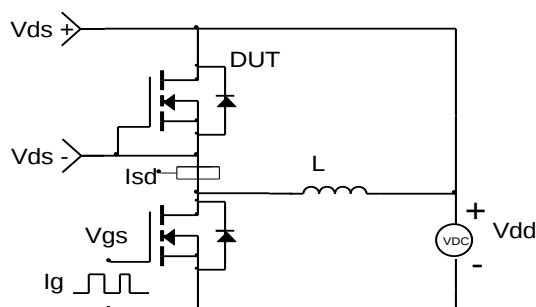
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Electrical Characteristics Diagrams

Fig.1 Power Dissipation Derating Curve

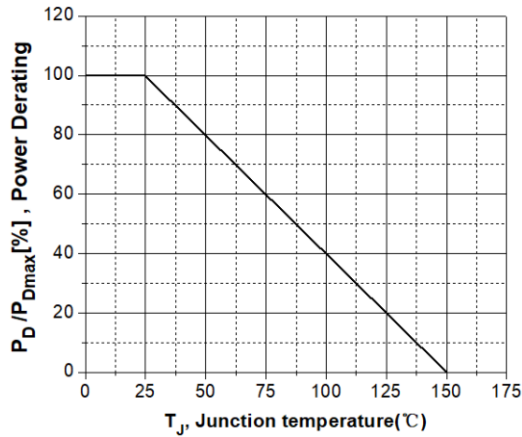


Fig.2 Avalanche Energy Derating Curve vs. Junction Temperature

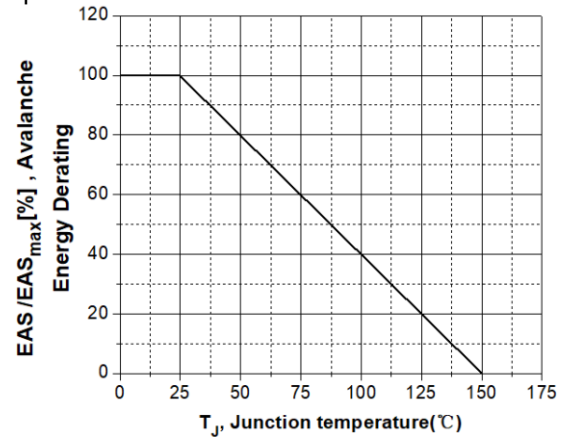


Fig.3 Typical Output Characteristics

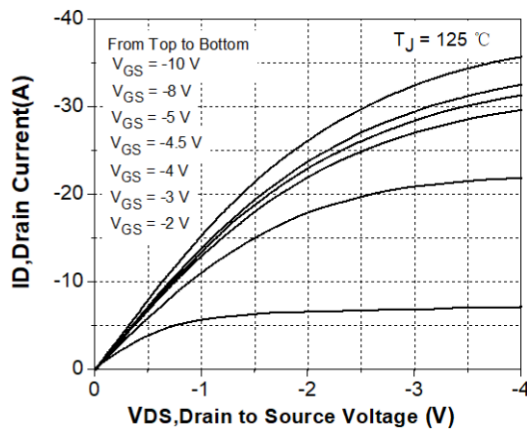


Fig. 4 Transconductance vs. Drain Current

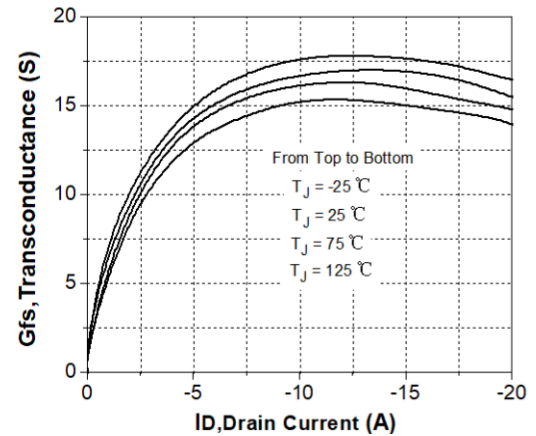


Fig.5 Typical Transfer Characteristics

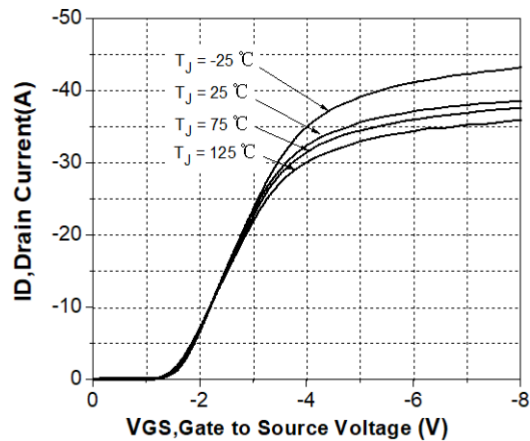


Fig. 6 Static Drain - Source On - State Resistance vs. Drain Current

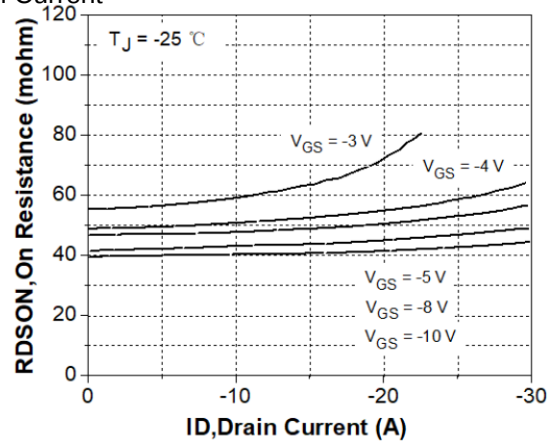


Fig.7 Static Drain - Source On - State Resistance vs. Drain Current

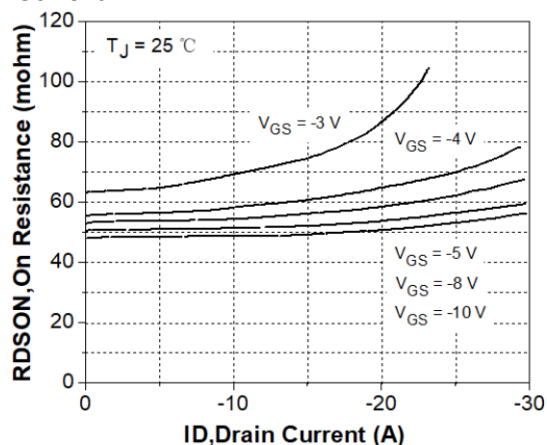


Fig.8 Static Drain - Source On - State Resistance vs. Drain Current

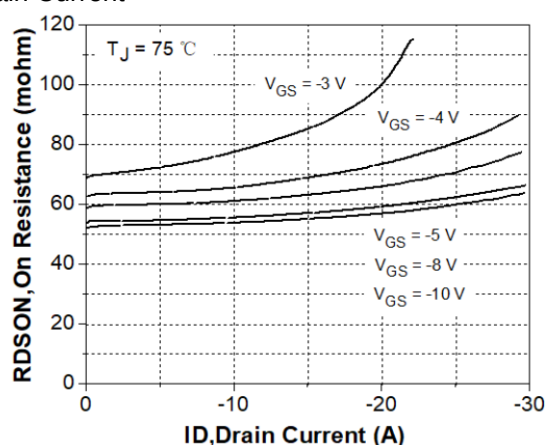


Fig.9 Static Drain - Source On - State Resistance vs. Drain Current

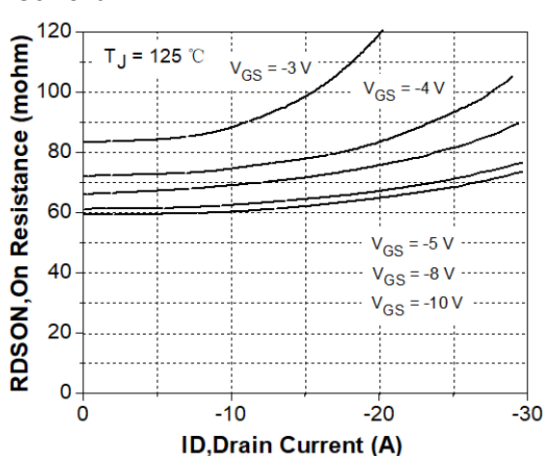


Figure.10 Gate Charge Characteristics

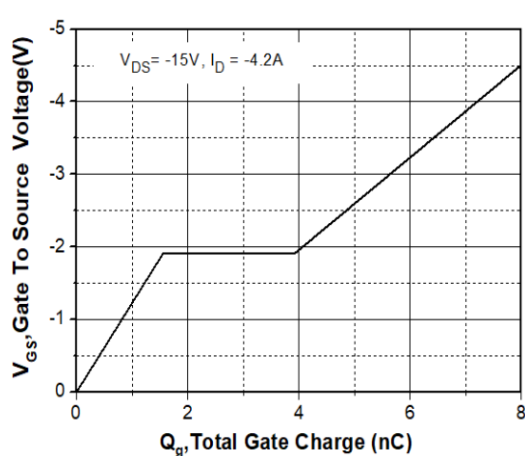


Figure.11 Breakdown Voltage vs. Junction Temperature

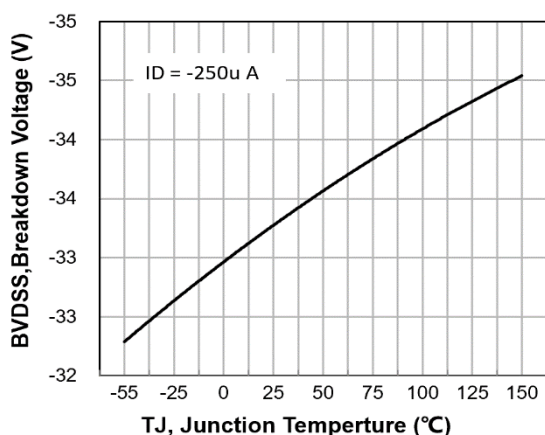


Figure.12 Gate Threshold Voltage vs. Junction Temperature

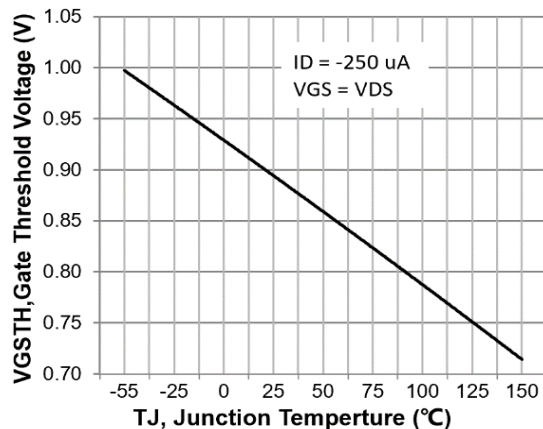


Fig.13 Safe Operating Area

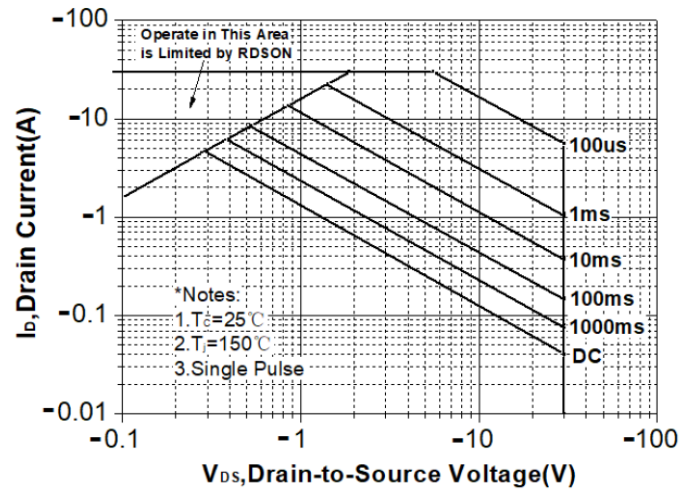
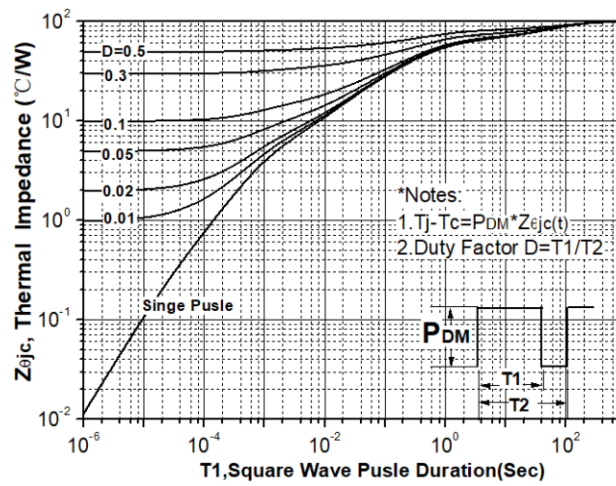
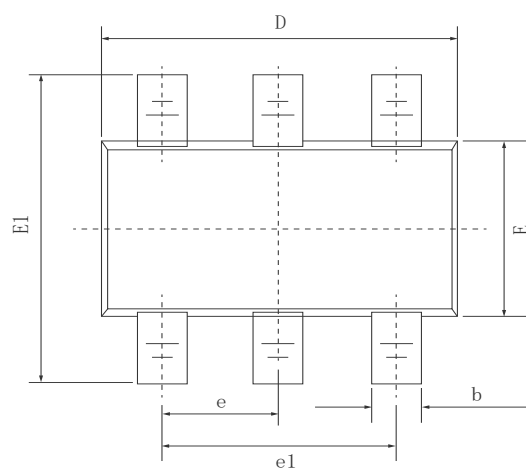


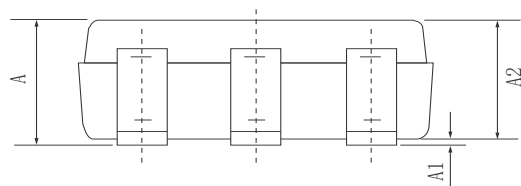
Fig. 14 Transient Thermal Response Curve



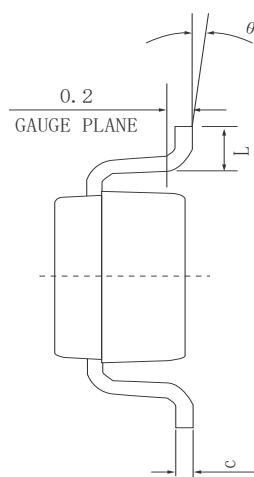
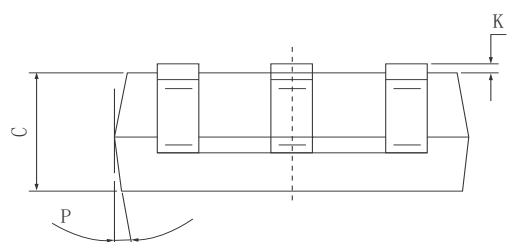
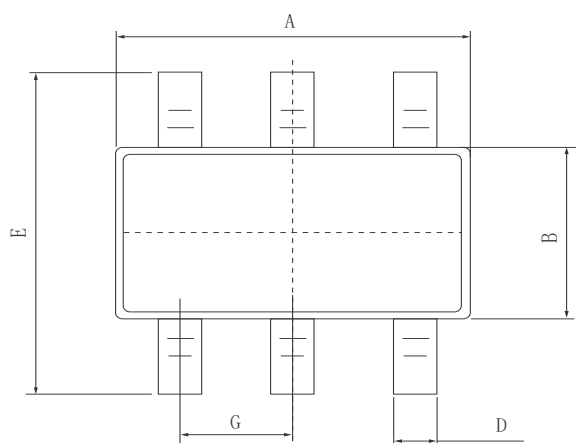
Dimensions (SOT23-6L)



TOP VIEW



SIDE VIEW



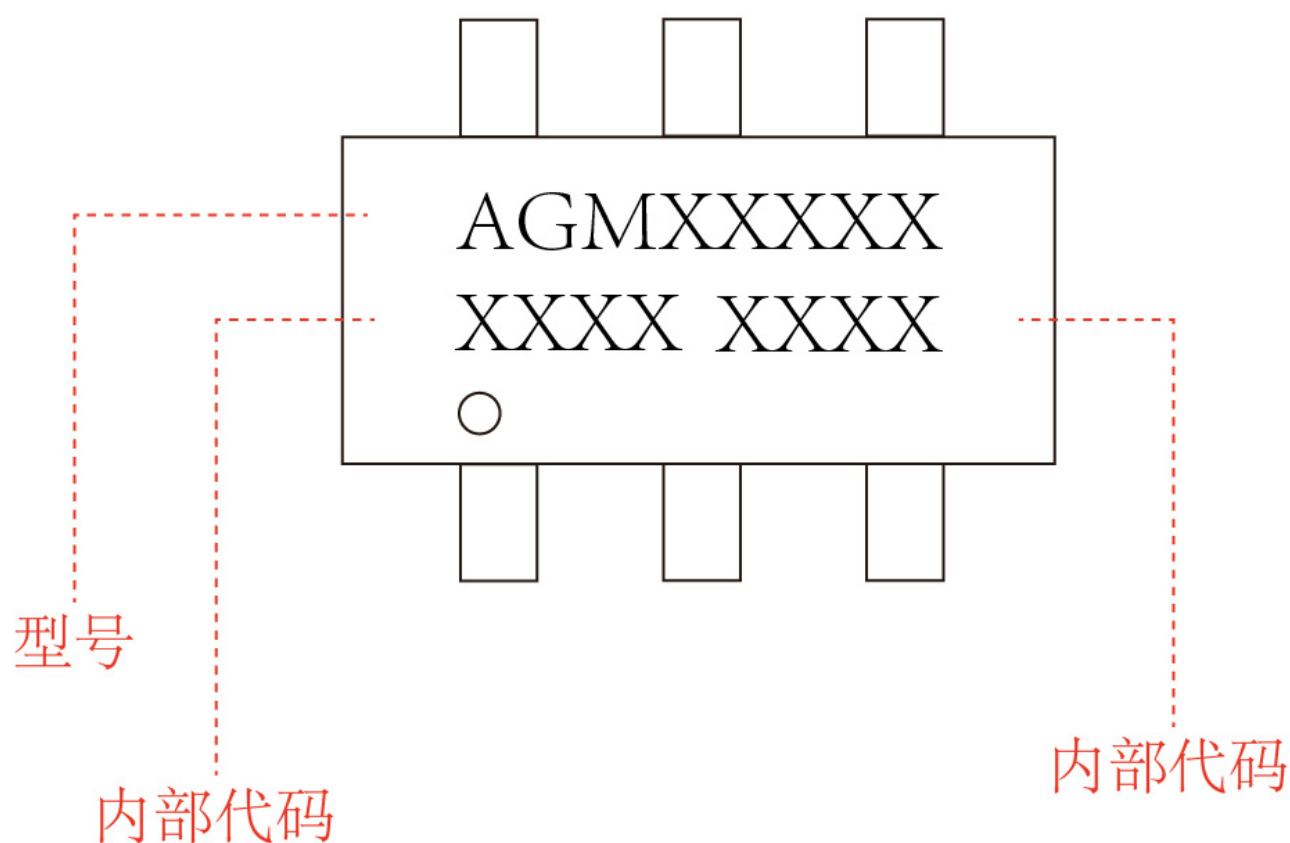
SIDE VIEW

SYMBOL	MIN	NOM	MAX
A	—	—	1.20
A1	0.00	0.05	0.10
A2	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.10	0.125	0.15
e1	1.80	1.90	2.00
D	2.80	2.90	3.00
E	1.50	1.60	1.70
E1	2.60	2.80	3.00
L	0.30	0.45	0.60
θ	0°	4°	8°
e	0.95BSC		

DIM	MILLIMETERS
A	2.82~3.02
B	1.60±0.10
C	1.10±0.05
D	0.40±0.10
E	2.65~2.95
G	0.95typ
K	0.00~0.10
M	0.20MIN
P	9±2°

SOT23-6L

Marking Instructions:



Disclaimer:

The information provided in this document is believed to be accurate and reliable. However, Shenzhen Core Control Source Electronics Technology Co., Ltd. does not assume any responsibility for the following consequences. Do not consider the use of such information or use beyond its scope.

The information mentioned in this document may be changed at any time without notice.

The products and information provided in this document do not infringe patents. Shenzhen Core Control Source Electronics Technology Co., Ltd. assumes no responsibility for any infringement of any other rights of third parties. The result of using such products and information.

This document is the third version issued on March 20th, 2024. This document replaces all previously provided information.

 It is a registered trademark of Shenzhen Core Control Source Electronics Technology Co., Ltd.

Copyright © 2017 Shenzhen Core Control Source Electronics Technology Co., Ltd. all rights reserved.