

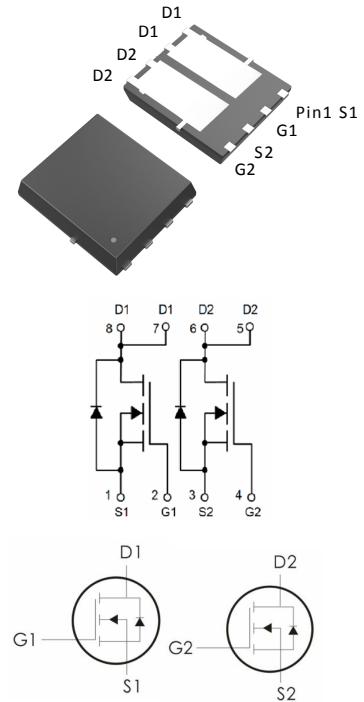
Description:

This Dual N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=20V, I_D=75A, R_{DS(on)} < 4m\Omega @ V_{GS}=4.5V$ (Typ: $2.8m\Omega$)
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.
- 6) MSL3



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
NB004DNAG	B004DNA	DFN5*6-8D	5000 pcs/Reel

Absolute Maximum Ratings: ($T_C=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Continuous Drain Current ¹	75	A
	Continuous Drain Current- $T_C=100^{\circ}C$ ¹	52.5	
I_{DM}	Pulsed Drain Current ²	300	
P_D	Power Dissipation	42	W
E_{AS}	Single pulse avalanche energy ³	133	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	$^{\circ}C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	3	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62	$^{\circ}C/W$

Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourctce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	20	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =20V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 12V, V _{DS} =0A	---	---	± 100	nA
On Characteristics (Note3)						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	0.5	0.75	1	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =4.5V, I _D =10A	---	2.8	4	m Ω
		V _{GS} =2.5V, I _D =5A	---	4	5.5	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, f=500KHz	---	3300	---	pF
C _{oss}	Output Capacitance		---	420	--	
C _{rss}	Reverse Transfer Capacitance		---	393	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =15V, I _D =15A, R _G =2.5 Ω ,V _{GS} =4.5V	---	16	---	ns
t _r	Rise Time		---	33	---	ns
t _{d(off)}	Turn-Off Delay Time		---	86	---	ns
t _f	Fall Time		---	136	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =10V, I _D =50A	---	38	---	nC
Q _{gs}	Gate-Source Charge		---	6.8	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	21	---	nC
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =50A	---	0.8	1.2	V
I _S	Continuous Drain Current	V _D =V _G =0V	---	---	62.5	A
I _{SM}	Pulsed Drain Current		---	---	250	A
T _{rr}	Reverse Recovery Time	I _F =15A, T _J =25 °C	---	50	---	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/us	---	0.04	---	nC

Notes:

1. Computed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. EAS condition : $T_J=25^{\circ}C, V_{DD}=10V, V_G=10V, L=0.5mH$
4. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_C=25^{\circ}C$ unless otherwise noted)

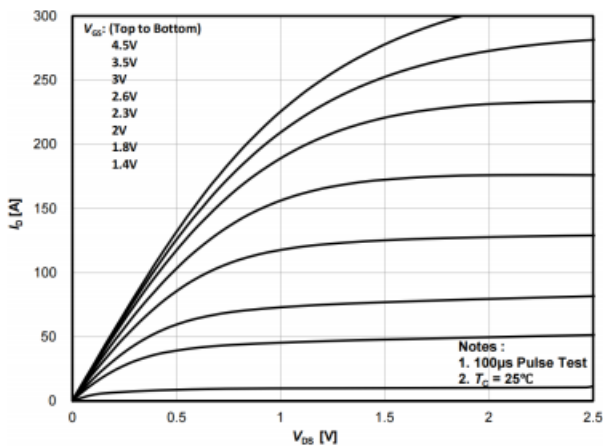


Figure 1. On-Region Characteristics

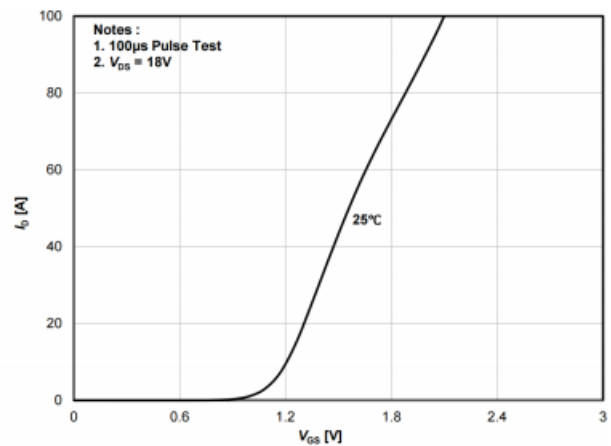


Figure 2. Transfer Characteristics

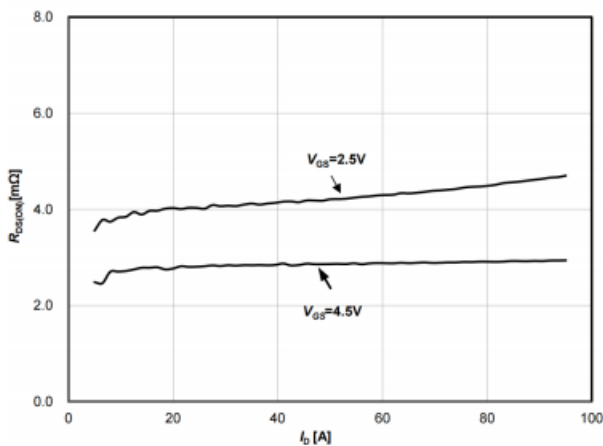


Figure 3. On Resistance vs. Drain Current

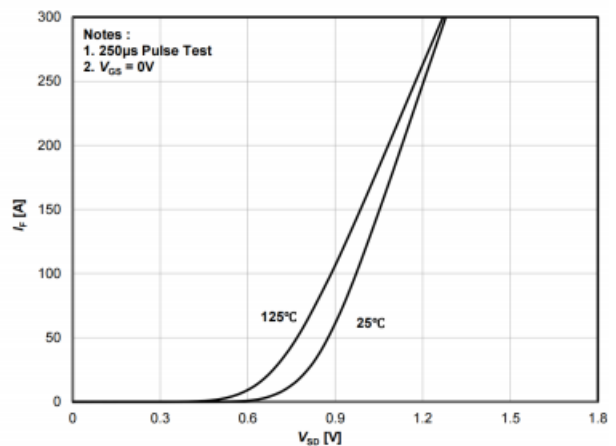


Figure 4. Diode Forward Voltage vs. Current

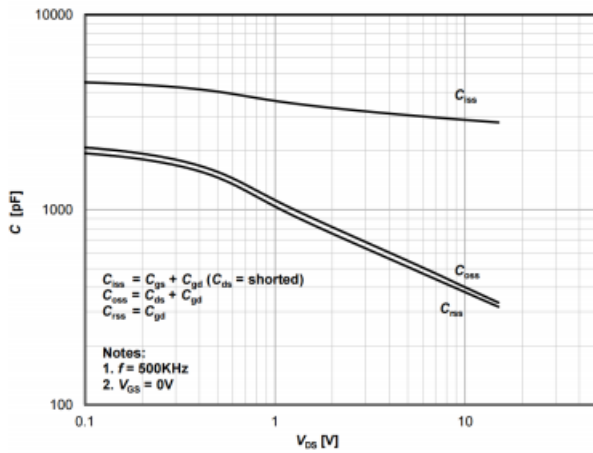


Figure 5. Capacitance Characteristics

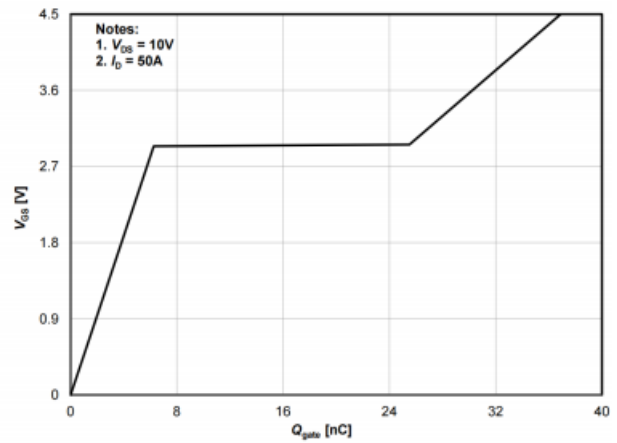


Figure 6. Gate Charge Characteristics

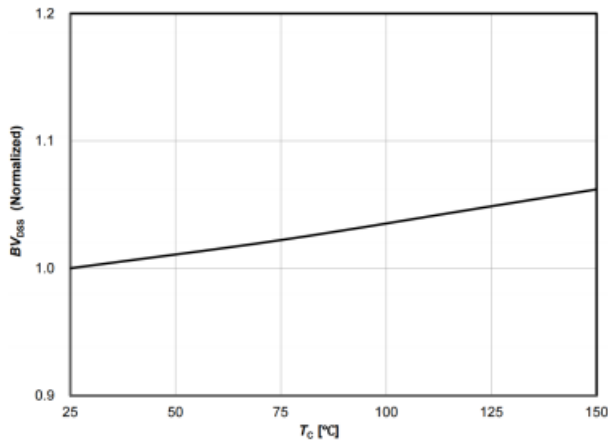


Figure 7. Normalized BV_{DS} vs. Temperature

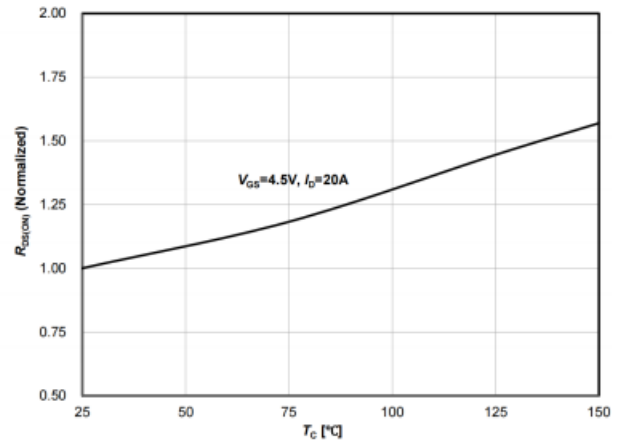


Figure 8. Normalized On-Resistance Variation vs. Temperature

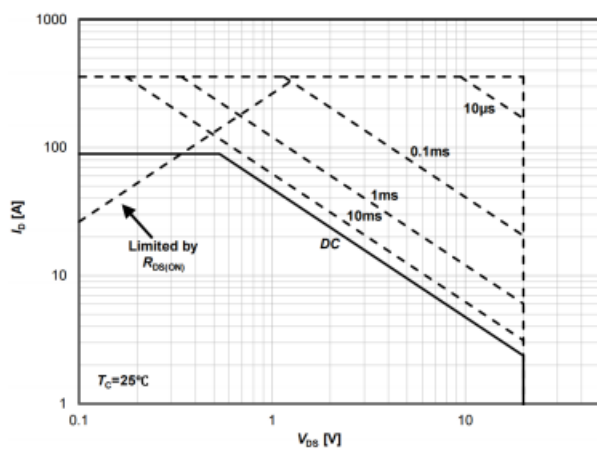


Figure 9. Safe Operating Area ³⁾

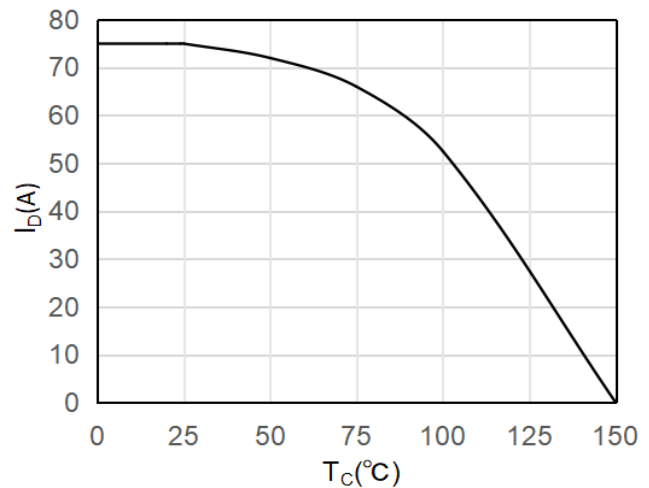


Figure 10. Drain Current vs. Temperature

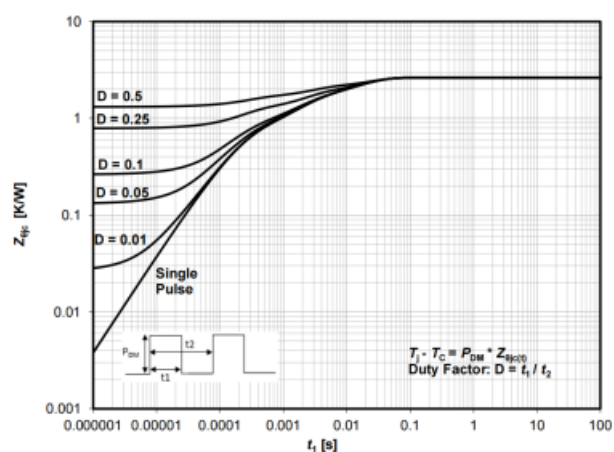
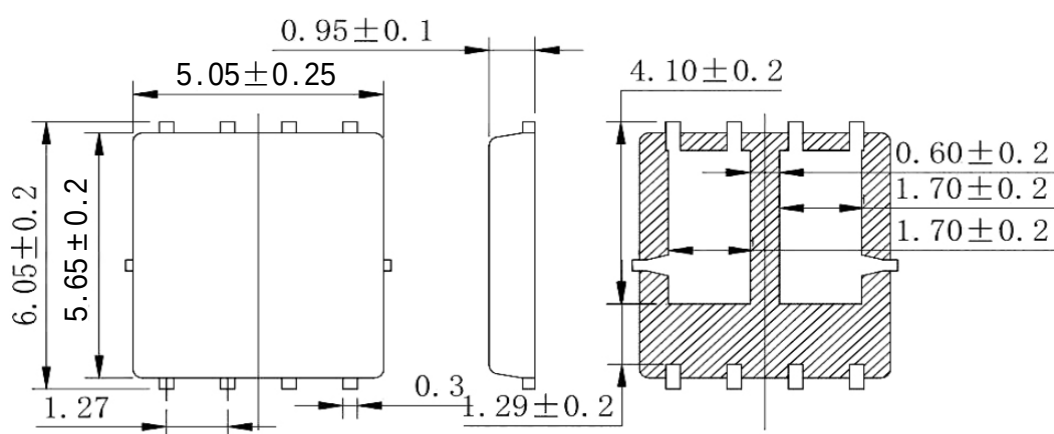
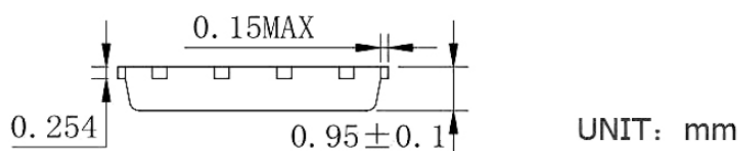


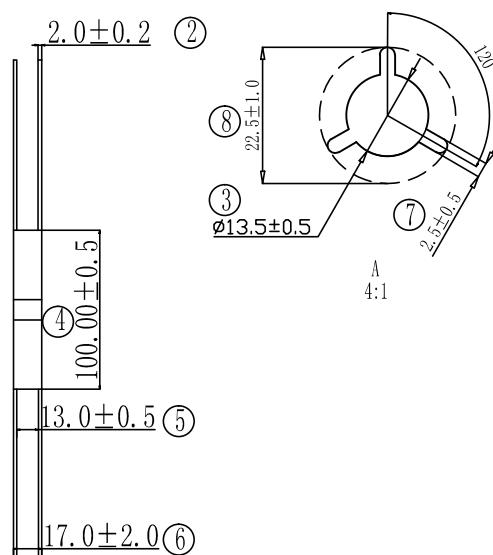
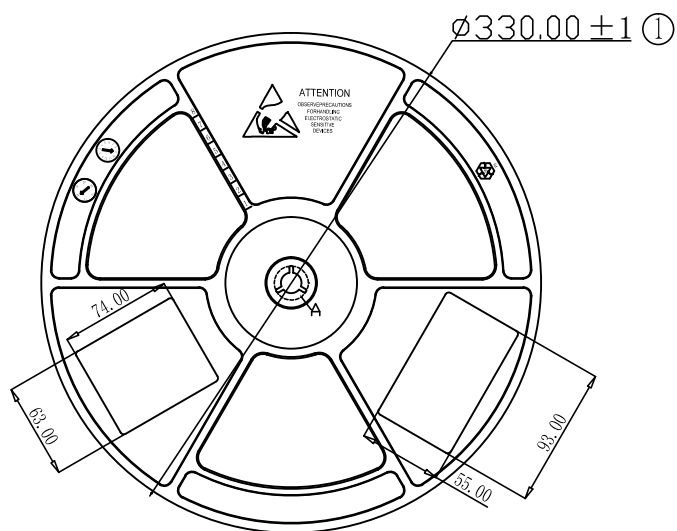
Figure 11. Transient Thermal Impedance

DFN5×6-8D Package Information:

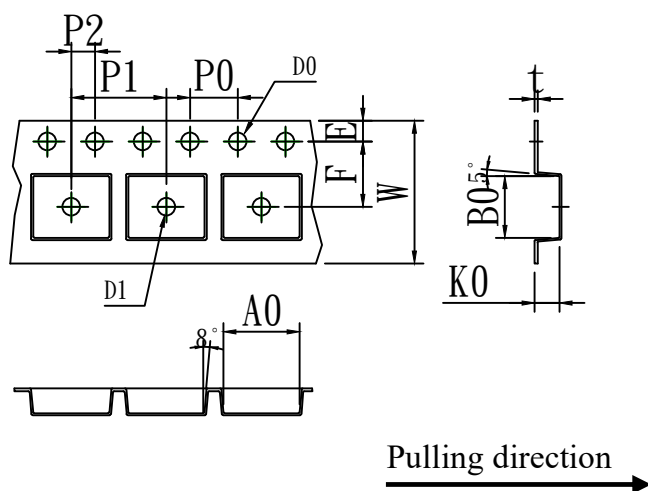


Tape & Reel Information

Dimensions in mm



Symbol	A0	B0	K0	D0	D1	P0	P1	10*P0
Spec	6.15±0.10	5.40±0.10	1.30±0.10	1.55±0.10	1.55±0.10	4.00±0.10	8.00±0.10	40.00±0.10
Symbol	W	E	F	P2	t			
Spec	12.00±0.10	1.75±0.10	5.50±0.10	2.00±0.10	0.20±0.05			



Marking Information:

①. Doingter LOGO

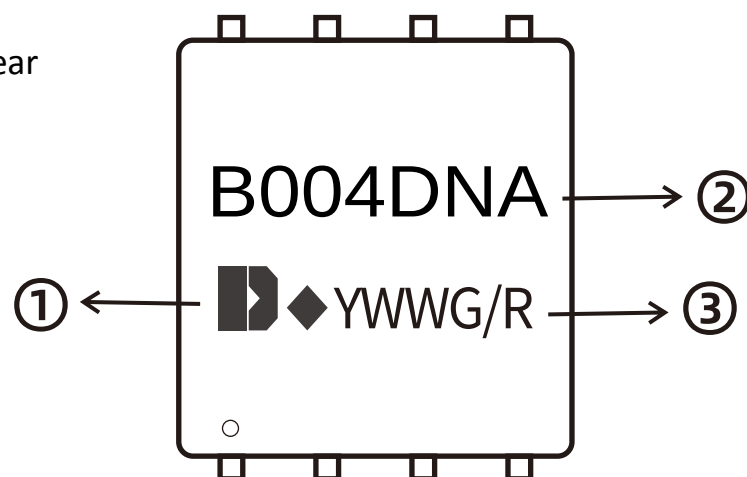
②. Part NO.

③. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2026-03-12	Release of final version

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