

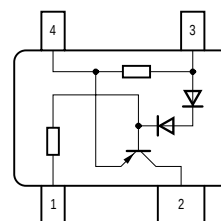
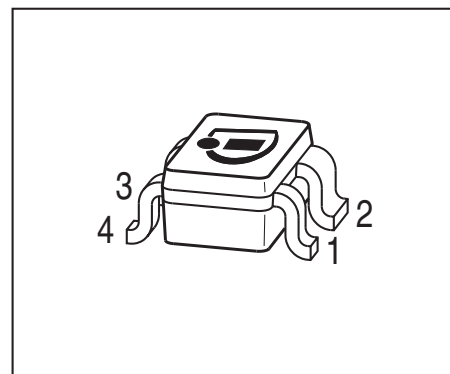
Active Bias Controller

Characteristics

- Supplies stable bias current even at low battery voltage and extreme ambient temperature variation
- Low voltage drop of 0.7V

Application notes

- Stabilizing bias current of NPN transistors and FET's from less than 0.2mA up to more than 200mA
- Ideal supplement for Sieget and other transistors
- also usable as current source up to 5mA


EHA07188

- Pb-free (RoHS compliant) package¹⁾
- Qualified according AEC Q101



Type	Marking	Pin Configuration				Package
BCR400W	W4s	1=GND/ E _{NPN}	2=Contr/ B _{NPN}	3V _S	4=Rext/ C _{NPN}	SOT343

(E_{NPN}, B_{NPN}, C_{NPN} are electrodes of a stabilized NPN transistor)

Maximum Ratings

Parameter	Symbol	Value	Unit
Source voltage	V _S	18	V
Control current	I _{Contr.}	10	mA
Control voltage	V _{Contr.}	16	V
Reverse voltage between all terminals	V _R	0.5	
Total power dissipation, T _S = 117 °C	P _{tot}	330	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-65 ... 150	

Thermal Resistance

Junction - soldering point ²⁾	R _{thJS}	≤ 100	K/W
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¹Pb-containing package may be available upon special request

²For calculation of R_{thJA} please refer to Application Note Thermal Resistance

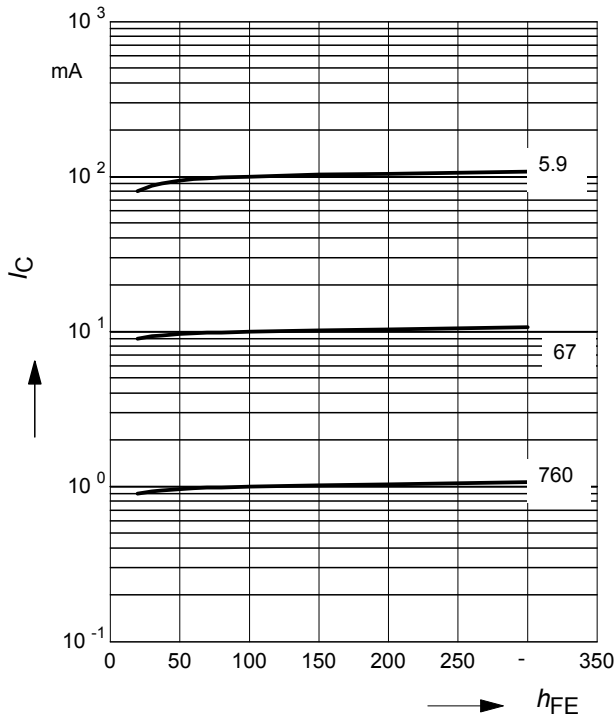
Electrical Characteristics at $T_A=25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Additional current consumption $V_S = 3\text{ V}$	I_0	-	20	40	μA
Lowest stabilizing current $V_S = 3\text{ V}$	I_{min}	-	0.1	-	mA
DC Characteristics with stabilized NPN-Transistors					
Lowest sufficient battery voltage $I_B\text{ (NPN)} < 0.5\text{mA}$	$V_{S\text{min}}$	-	1.6	-	V
Voltage drop ($V_S - V_{CE}$) $I_C = 25\text{ mA}$	V_{drop}	-	0.65	-	
Change of I_C versus h_{FE} $h_{FE} = 50$	$\Delta I_C/I_C$	-	0.08	-	$\Delta h_{FE} / h_{FE}$
Change of I_C versus V_S $V_S = 3\text{ V}$	$\Delta I_C/I_C$	-	0.15	-	$\Delta V_S/V_S$
Change of I_C versus T_A	$\Delta I_C/I_C$	-	0.2	-	%/K

Collector current $I_C = f(h_{FE})$

I_C and h_{FE} refer to stabilized NPN Transistor

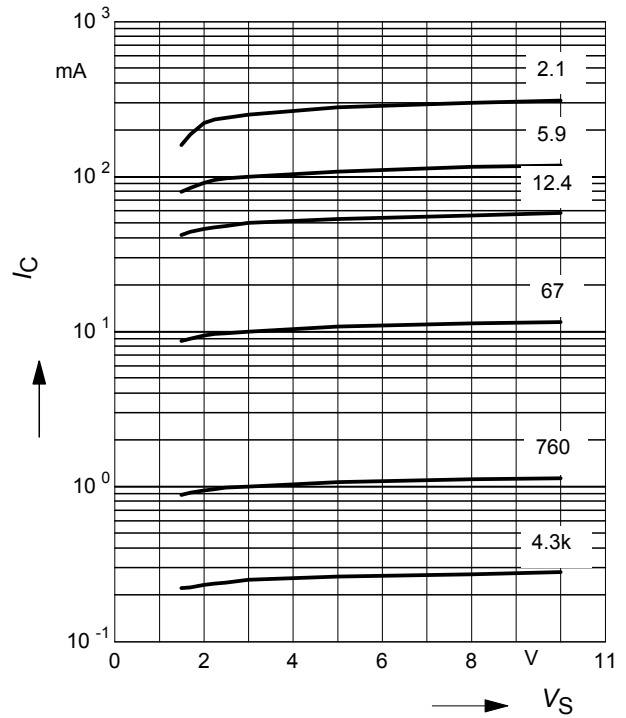
Parameter $R_{ext.} (\Omega)$



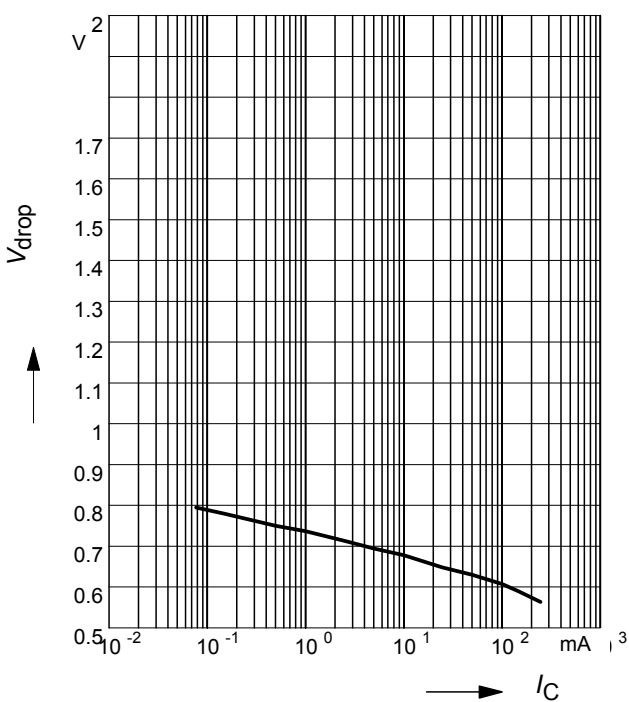
Collector Current $I_C = f(V_S)$

of stabilized NPN Transistor

Parameter $R_{ext.} (\Omega)$

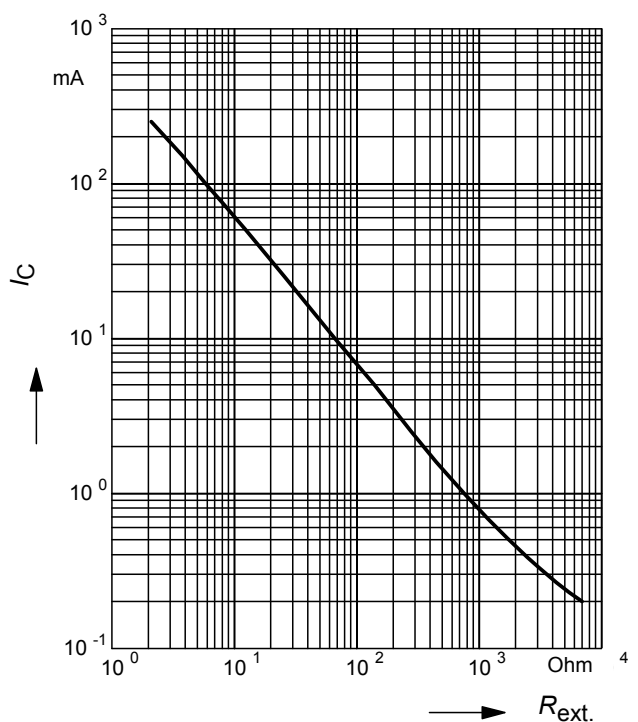


Voltage drop $V_{drop} = f(I_C)$



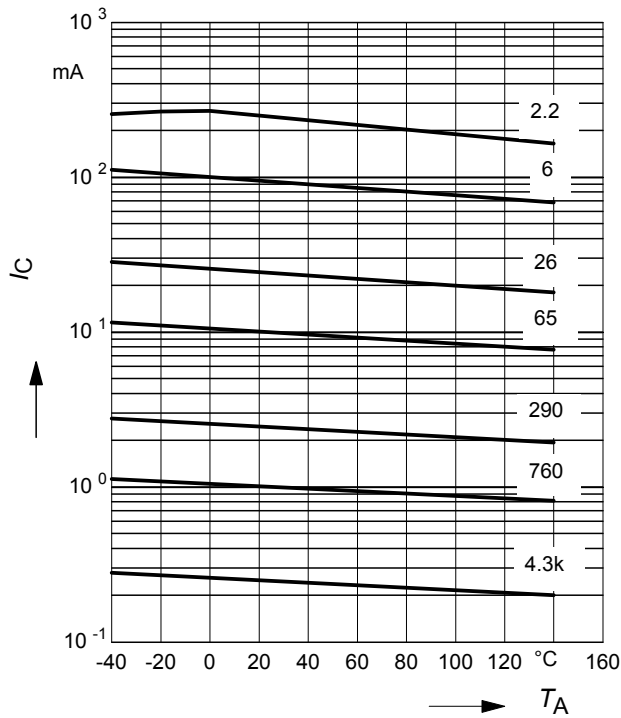
Collector current $I_C = f(R_{ext.})$

of stabilized NPN Transistor

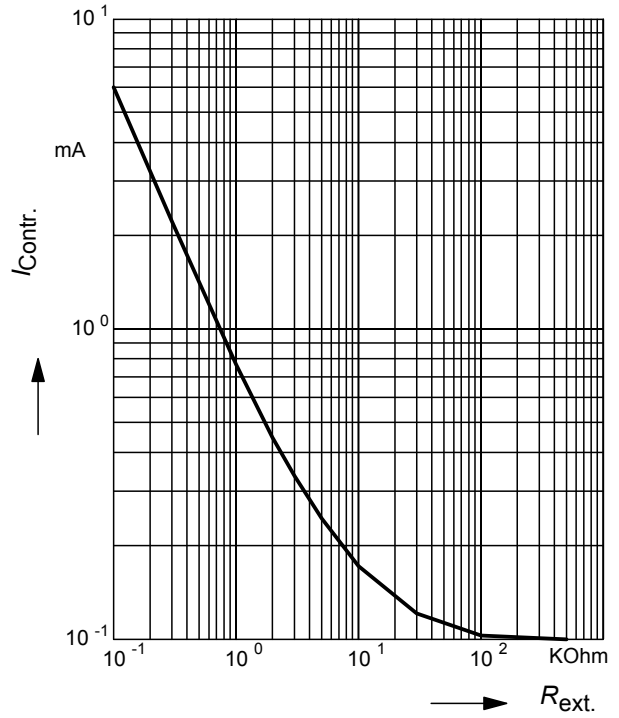


Collector current $T_A = f(I_C)$
of stabilized NPN Transistor

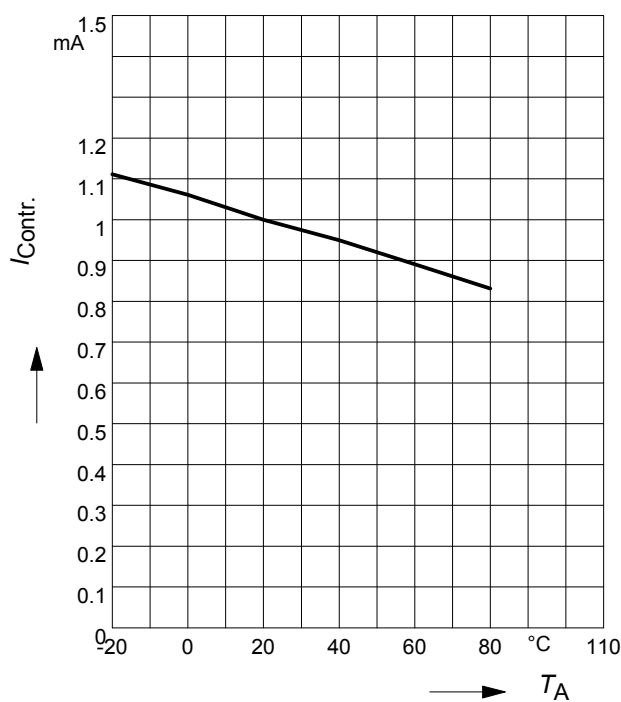
Parameter: $R_{ext.} (\Omega)$



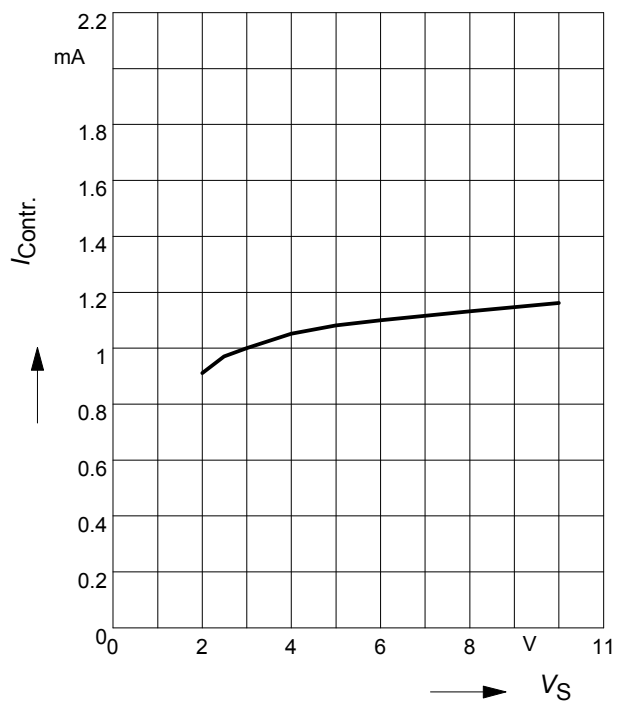
Control current $I = f(R_{ext.})$
in current source application



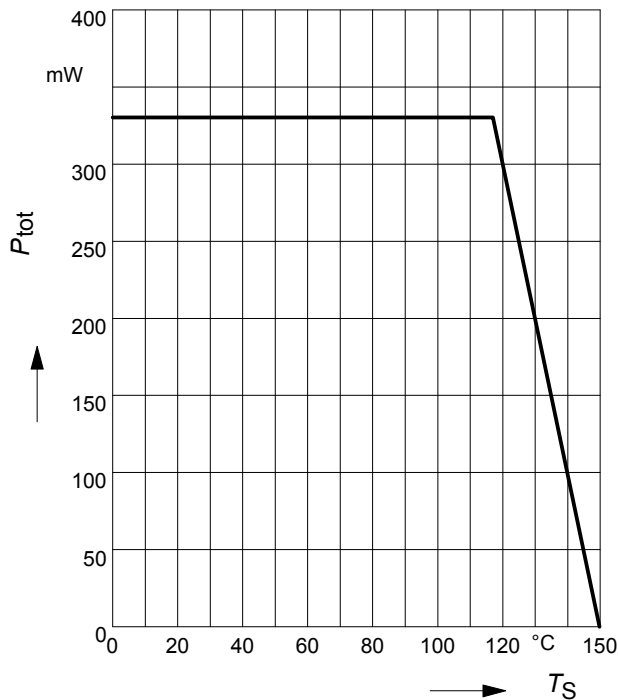
Control current $I = f(T_A)$
in current source application



Control current $I = f(V_S)$
in current source application

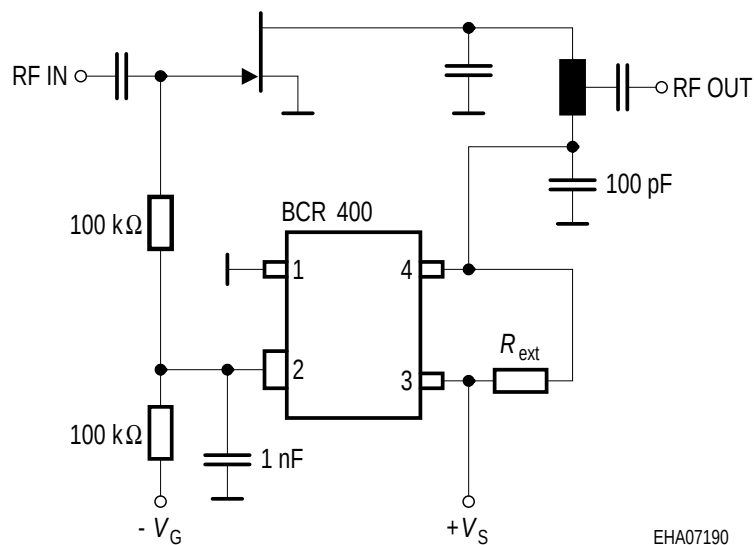


Total power dissipation $P_{\text{tot}} = f(T_S)$

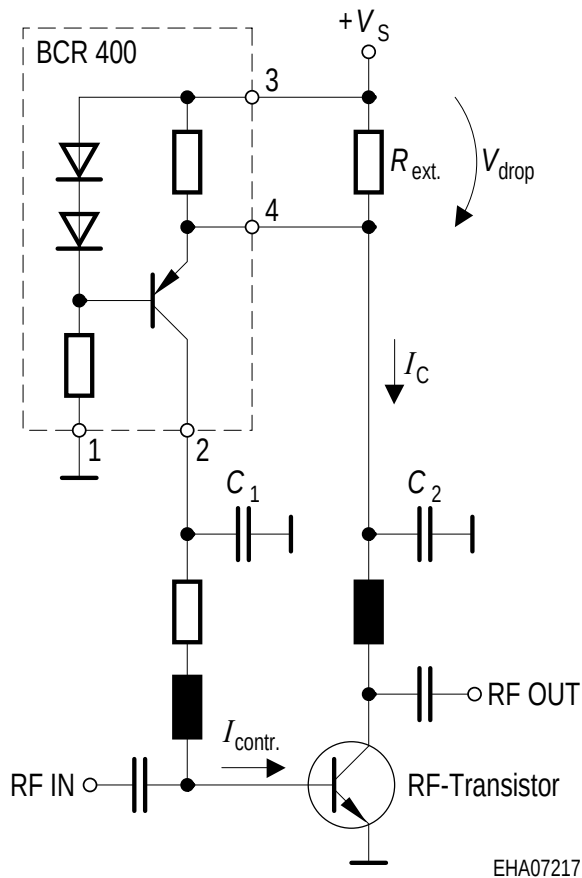


Note that up to $T_S=115^\circ\text{C}$ it is not possible to exceed P_{tot} respecting the maximum ratings of V_S and I_{Contr} . The collector or drain current (respectively) of the stabilized RF transistor does not affect BCR 400 directly, as it provides just the base current.

Typical application for GaAs FET with active bias controller



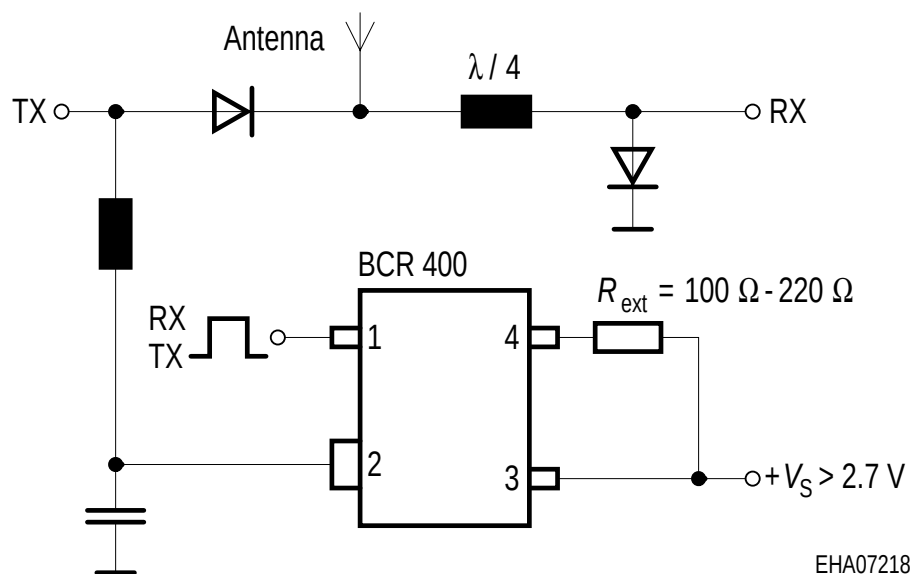
RF transistor controlled by BCR400



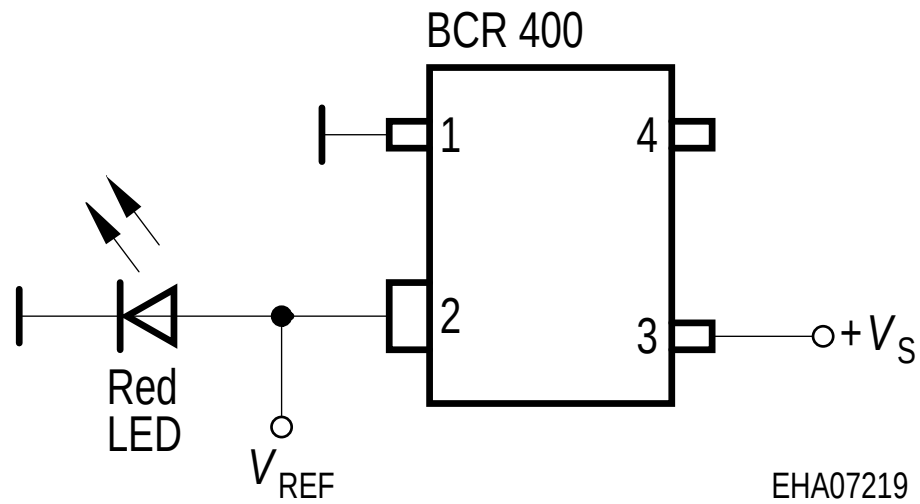
Be aware that BCR400 stabilized bias current of transistors in an active control loop

In order to avoid loop oscillation (hunting), time constants must be chosen adequately, i.e. $C_1 \geq 10 \times C_2$

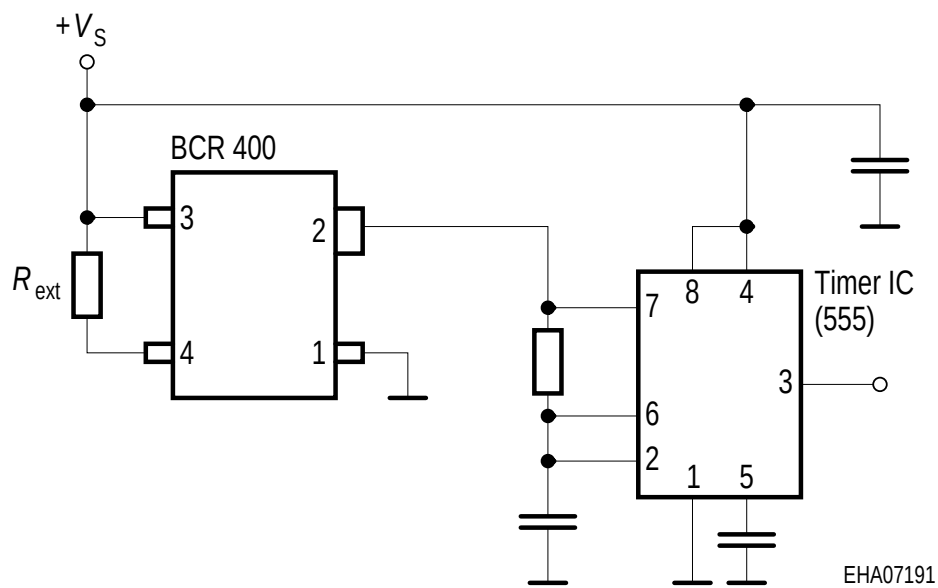
RX/TX antenna switch, compatible to control logic and working at wide battery voltage range



Low voltage reference



Precision timer with BCR400 providing constant charge current



The technical drawing illustrates the 240-100-01 component from three perspectives: isometric, top, and side views.

Isometric View: Shows the component with four pins labeled 1, 2, 3, and 4.

Top View:

- Overall width: 2 ± 0.2
- Distance from left edge to pin 1: $0.3^{+0.1}_{-0.05}$
- Distance between pins 1 and 2: 1.3
- Distance from pin 2 to right edge: $0.6^{+0.1}_{-0.05}$
- Pin diameter: $\Phi 0.1 \text{ (M)}$
- Pin length: $4x$
- Distance from centerline to pin 3: 0.15

Side View:

- Overall height: 0.9 ± 0.1
- Top surface flatness: 0.1 MAX.
- Top surface circular runout: 0.1
- Distance from top surface to pin 1: 2.1 ± 0.1
- Distance from pin 1 to pin 2: 0.1 MIN.
- Distance from pin 2 to bottom surface: $0.15^{+0.1}_{-0.05}$
- Bottom surface circular runout: 0.2 (M) A
- Distance from centerline to pin 3: 1.25 ± 0.1

Technical drawing of a mechanical part with dimensions: 0.6, 0.8, 1.6, 1.15, and 0.9.

Pin 1

Manufacturer

2005, June
Date code (YM)

BGA420
Type code

[illegible]

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