



钜地半导体
Tudi Semiconductor

Product Specification

TUDI-LM139/239/339

Low-power quad voltage comparators

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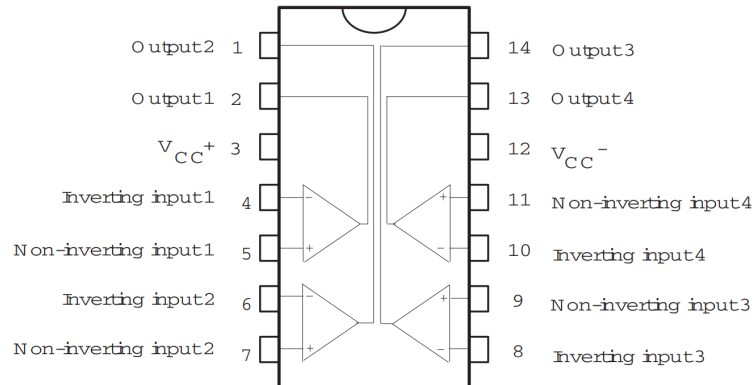
**semiconductor device
manufacturer**

- Design
- research and development
- production
- and sales



Features

- Wide single supply voltage range or dual supplies for all devices: +2 to +36 V or ± 1 V to ± 18 V
- Very low supply current (1.1 mA) independent of supply voltage
- Low input bias current: 25 nA typ
- Low input offset current: ± 5 nA typ
- Low input offset voltage: ± 1 mV typ
- Input common-mode voltage range includes ground
- Low output saturation voltage: 250 mV typ; (ISINK = 4 mA)
- Differential input voltage range equal to the supply voltage
- TTL, DTL, ECL, MOS, CMOS compatible outputs

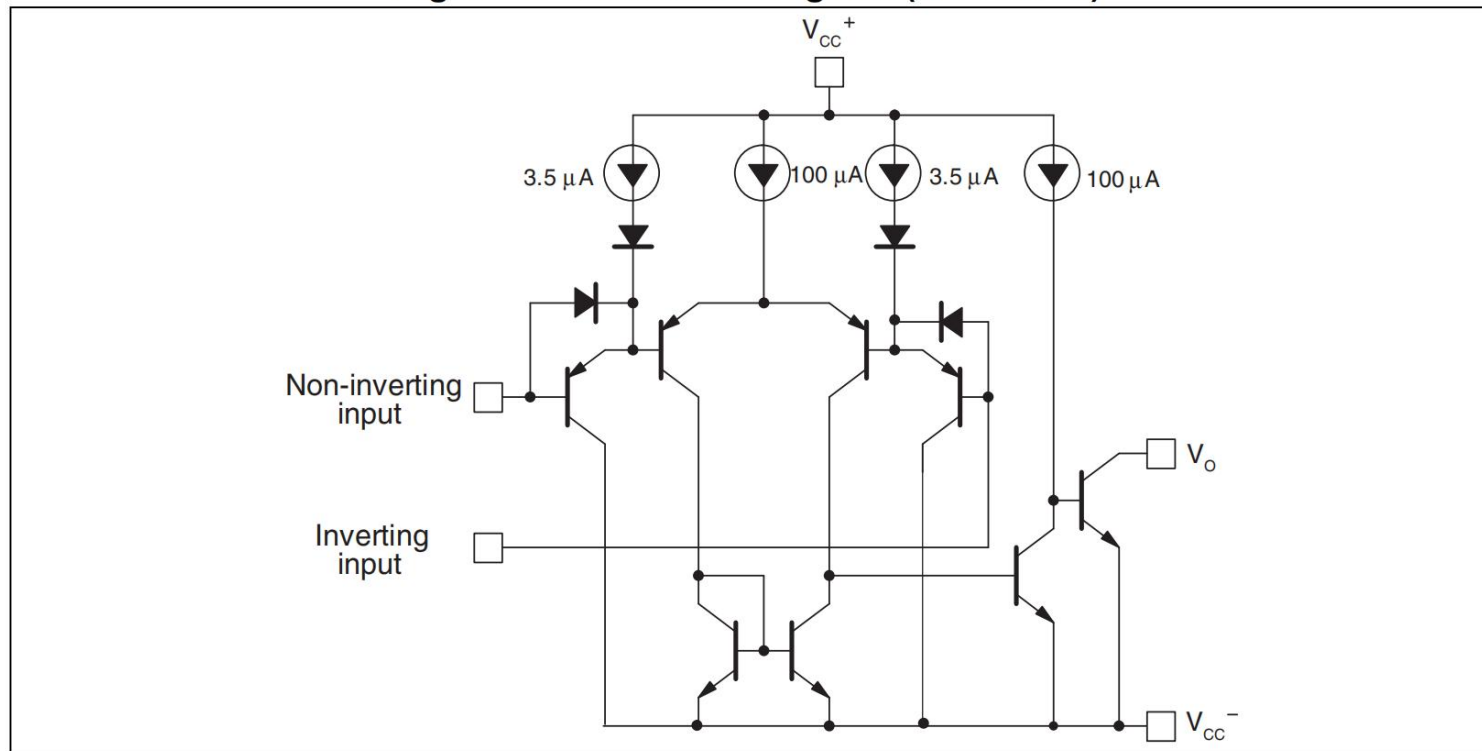


Description

This family of devices consists of four independent precision-voltage comparators with an offset voltage specification as low as 2 mV maximum for LM339A, LM239A, and LM139A. Each comparator has been designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible. These comparators also have a unique characteristic in that the input common mode voltage range includes ground even though operated from a single power supply voltage.



Figure 2. Schematic diagram (1/4 LM139)





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Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage	± 18 or 36	V
V_{ID}	Differential input voltage	± 36	V
V_{IN}	Input voltage	-0.3 to +36	V
	Output short-circuit to ground ⁽¹⁾	Infinite	
R_{thja}	Thermal resistance junction to ambient ⁽²⁾ SO14 TSSOP14 QFN16 3x3	105 100 45	°C/W
R_{thjc}	Thermal resistance junction to case ⁽²⁾ SO14 TSSOP14 QFN16 3x3	31 32 14	°C/W
T_{stg}	Storage temperature range	-65 to +150	°C
T_j	Junction temperature	+150	°C
T_{LEAD}	Lead temperature (soldering 10 seconds)	260	°C
ESD	Human body model (HBM) ⁽³⁾	500	V
	Machine model (MM) ⁽⁴⁾	100	
	Charged device model (CDM) ⁽⁵⁾	1500	

1. Short-circuits from the output to V_{CC}^+ can cause excessive heating and eventual destruction. The maximum output current is approximately 20 mA independent of the magnitude of V_{CC}^+ .
2. Short-circuits can cause excessive heating. These values are typical.
3. Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 k Ω resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
4. Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.
5. Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.



Table 2. Operating conditions ($T_{amb} = 25\text{ }^{\circ}\text{C}$)

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage	2 to 32 ± 1 to ± 16	V
V_{ICM}	Common mode input voltage range	0 to ($V_{CC}^{+} - 1.5$)	V
T_{oper}	Operating free-air temperature range – LM139, LM139A – LM239, LM239A – LM339, LM339A	-55, +125 -40, +105 0, +70	$^{\circ}\text{C}$



3 Electrical characteristics

Table 3. Electrical characteristics at $V_{CC}^+ = +5\text{ V}$, $V_{CC}^- = \text{GND}$, $T_{\text{amb}} = +25\text{ }^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	LM139A - LM239A LM339A			LM139 - LM239 LM339			Unit
		Min.	Typ.	Max.	Min	Typ.	Max.	
V_{IO}	Input offset voltage ⁽¹⁾ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		1	2 4		1	5 9	mV
I_{IO}	Input offset current $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		3	25 100		5	50 150	nA
I_{IB}	Input bias current (I^+ or I^-) ⁽²⁾ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		25	100 300		25	250 400	nA
A_{VD}	Large signal voltage gain $V_{CC} = 15\text{ V}$, $R_L = 15\text{ k}\Omega$, $V_O = 1\text{ V to } 11\text{ V}$	50	200		50	200		V/mV
I_{CC}	Supply current (all comparators) $V_{CC} = +5\text{ V}$, no load $V_{CC} = +30\text{ V}$, no load		1.1 1.3	2 2.5		1.1 1.3	2 2.5	mA
V_{ICM}	Input common mode voltage range ⁽³⁾ $V_{CC} = 30\text{ V}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$	0 0		$V_{CC}^+ - 1.5$ $V_{CC}^+ - 2$	0 0		$V_{CC}^+ - 1.5$ $V_{CC}^+ - 2$	V
V_{ID}	Differential input voltage ⁽⁴⁾			V_{CC}^+			V_{CC}^+	V
V_{OL}	Low level output voltage $V_{ID} = -1\text{ V}$, $I_{SINK} = 4\text{ mA}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		250	400 700		250	400 700	mV
I_{OH}	High level output current $V_{CC} = V_O = 30\text{ V}$, $V_{ID} = 1\text{ V}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$		0.1	1		0.1	1	nA μA
I_{SINK}	Output sink current $V_{ID} = 1\text{ V}$, $V_O = 1.5\text{ V}$	6	16		6	16		mA
t_{re}	Response time ⁽⁵⁾ $R_L = 5.1\text{ k}\Omega$ connected to V_{CC}^+		1.3			1.3		μs



**Table 3. Electrical characteristics at $V_{CC}^+ = +5\text{ V}$, $V_{CC}^- = \text{GND}$, $T_{\text{amb}} = +25\text{ }^\circ\text{C}$
(unless otherwise specified) (continued)**

Symbol	Parameter	LM139A - LM239A LM339A			LM139 - LM239 LM339			Unit
		Min.	Typ.	Max.	Min	Typ.	Max.	
t _{rel}	Large signal response time $R_L = 5.1\text{ k}\Omega$ connected to V_{CC}^+ , $e_i = \text{TTL}$, $V_{(\text{ref})} = +1.4\text{ V}$		300			300		ns

1. At output switch point, $V_o \approx 1.4\text{ V}$, V_{CC}^+ from 5 V to 30 V, and over the full common-mode range (0 V to $V_{CC}^+ - 1.5\text{ V}$).
2. The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output, so no loading charge exists on the reference of input lines.
3. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is $V_{CC}^+ - 1.5\text{ V}$, but either or both inputs can go to +30 V without damage.
4. Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than -0.3 V (or 0.3 V below the negative power supply, if used).
5. The response time specified is for a 100 mV input step with 5 mV overdrive. For larger overdrive signals, 300 ns can be obtained.



5 Typical applications

Figure 8. Basic comparator

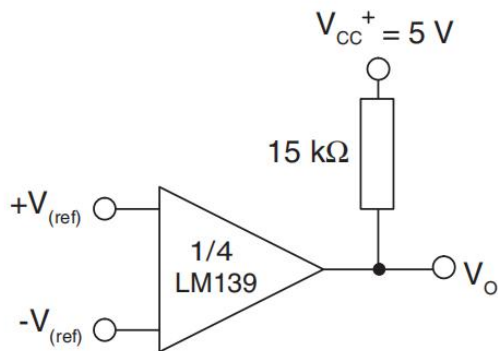


Figure 9. Driving CMOS

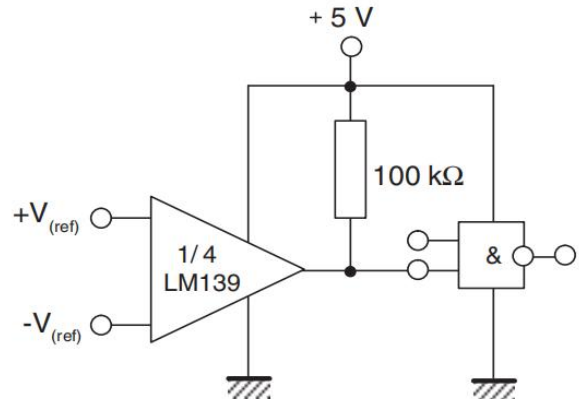


Figure 10. Driving TTL

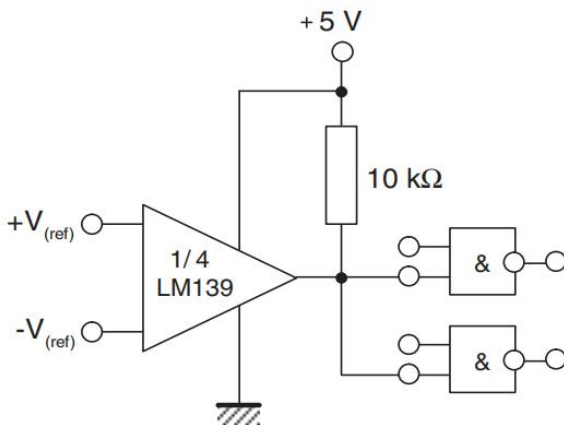


Figure 11. Low frequency op amp

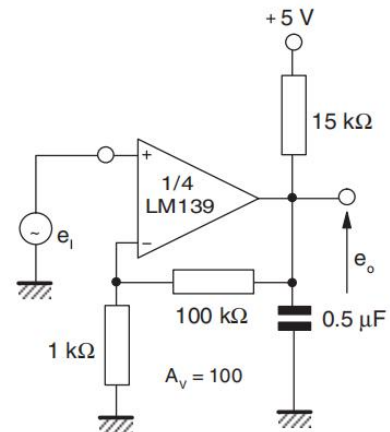


Figure 12. Low frequency op amp

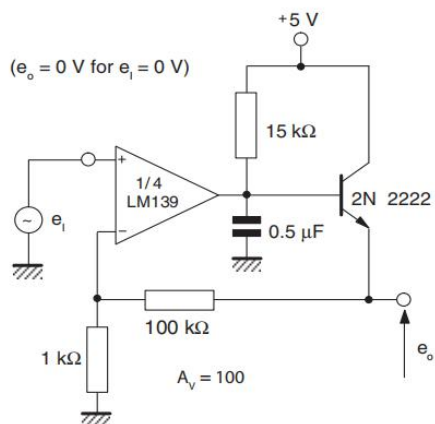


Figure 13. Transducer amplifier

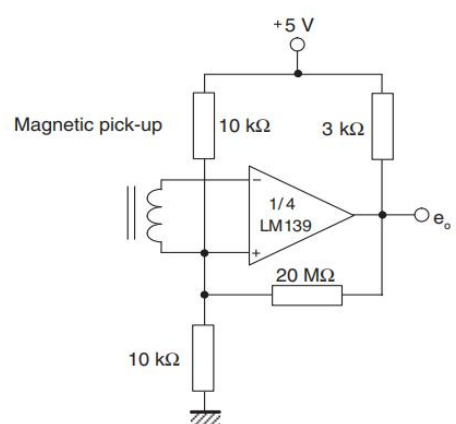




Figure 14. Time delay generator

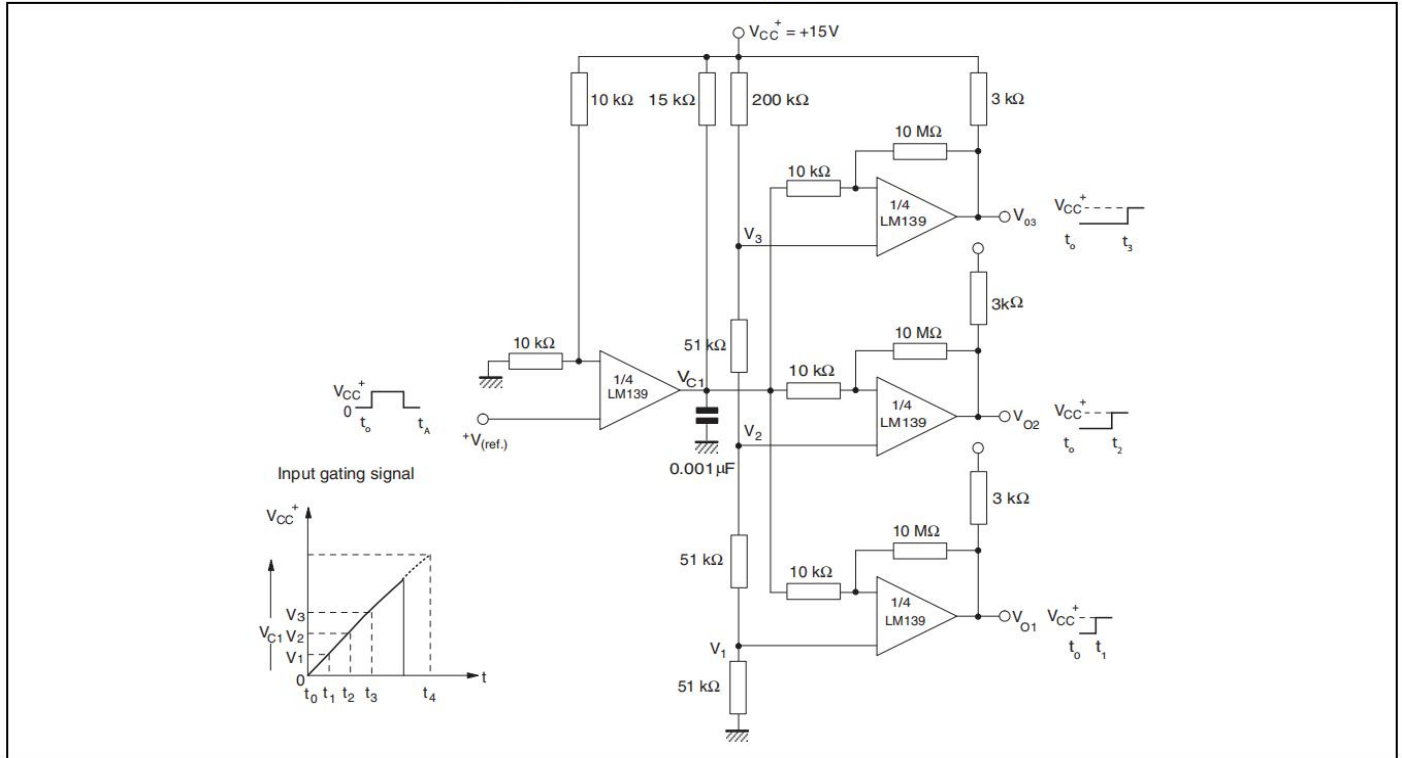


Figure 15. Low frequency op amp with offset adjust

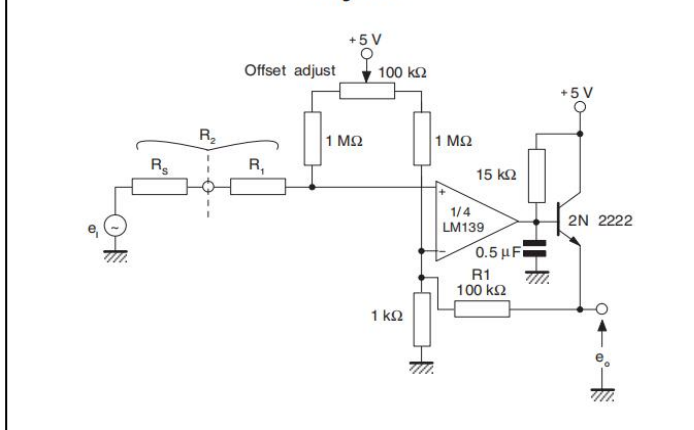


Figure 16. Zero crossing detector (single power supply)

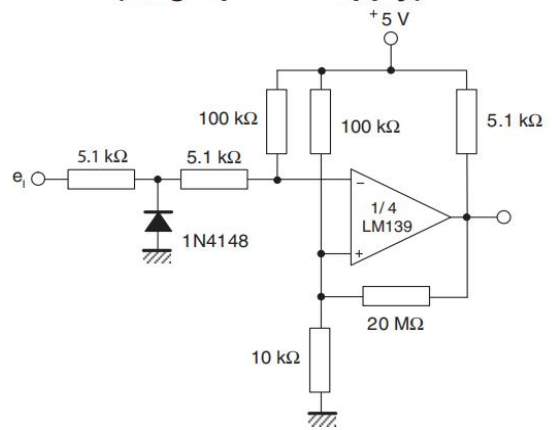


Figure 17. Two-decade high-frequency VCO

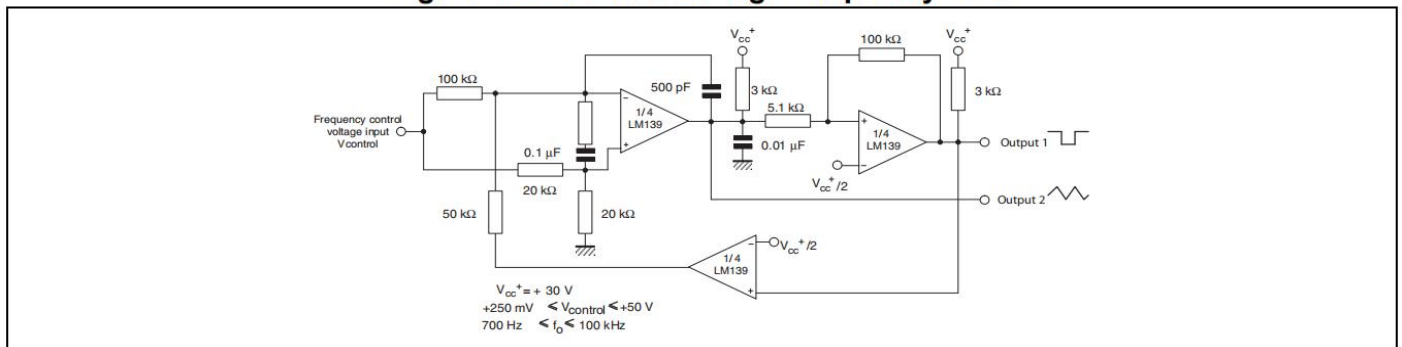




Figure 18. Limit comparator

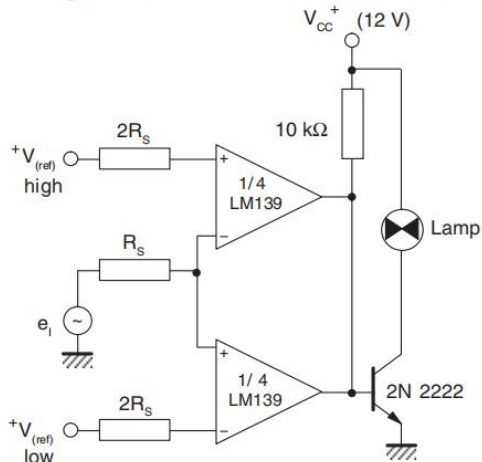


Figure 19. Crystal-controlled oscillator

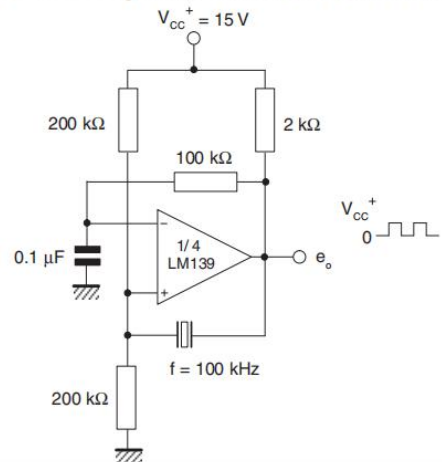


Figure 20. Zero crossing detector

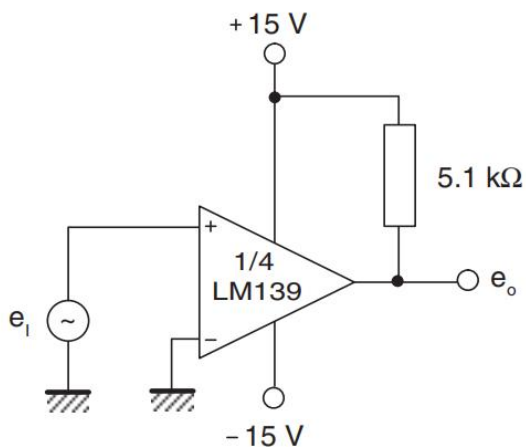
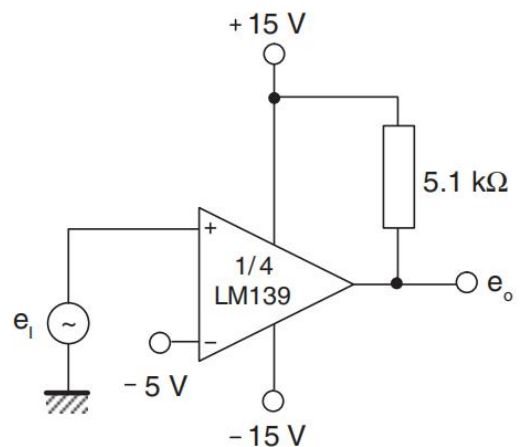


Figure 21. Comparator with a negative reference

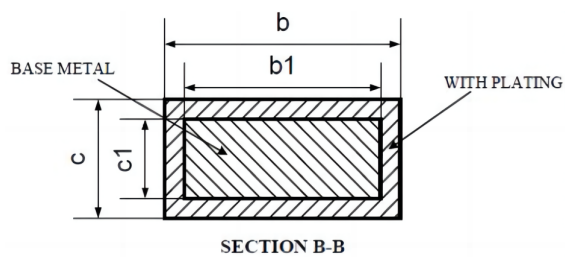
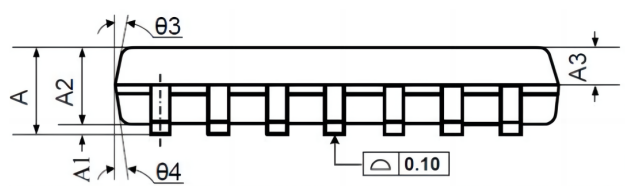
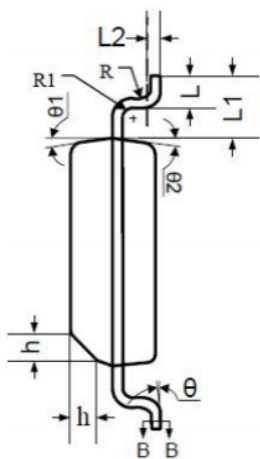
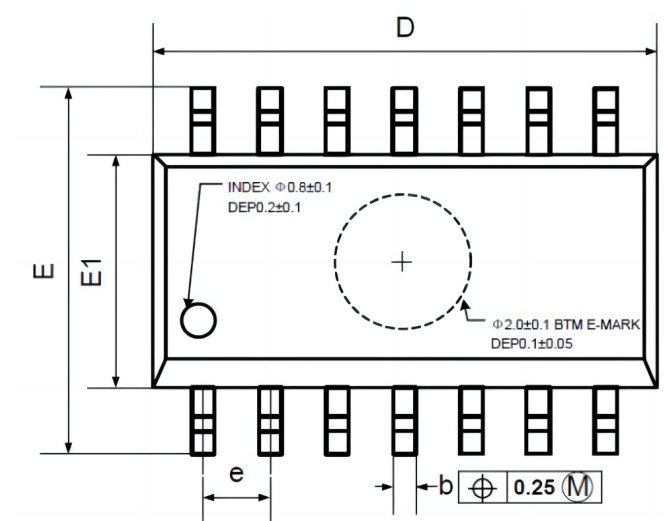




Order information

Order Number	Package	Package Quantity	Marking On The park
LM139ADT-TUDI	SOP14	Tape,Reel,2500	LM139ADT
LM239ADT-TUDI	SOP14	Tape,Reel,2500	LM239ADT
LM239APT-TUDI	TSSOP14	Tape,Reel,2500	LM239APT

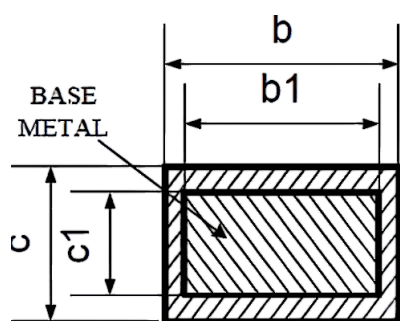
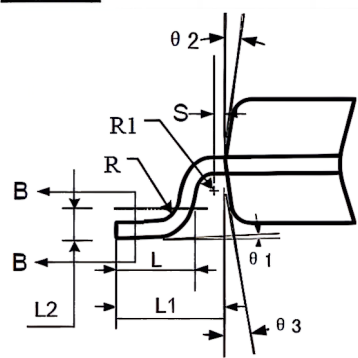
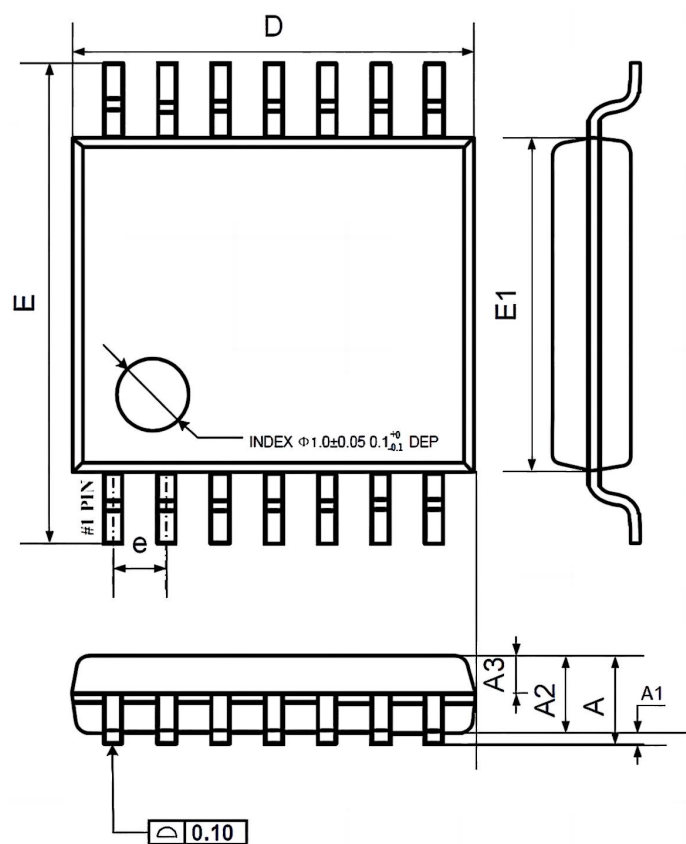
Package SOP14



Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	1.35	1.60	1.75
A1	0.10	0.15	0.25
A2	1.25	1.45	1.65
A3	0.55	0.65	0.75
b	0.36		0.49
b1	0.35	0.40	0.45
C	0.16		0.25
c1	0.15	0.20	0.25
D	8.53	8.63	8.73
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27 BSC		
L	0.45	0.60	0.80
L1	1.04 REF		
L2	0.25 BSC		
R	0.07		
R1	0.07		
h	0.30	0.40	0.50
θ	0°		8°
θ1	6°	8°	10°
θ2	6°	8°	10°
θ3	5°	7°	9°
θ4	5°	7°	9°



Package TSSOP14



Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	—	—	1.20
A1	0.05	—	0.15
A2	0.90	1.00	1.05
A3	0.34	0.44	0.54
b	0.20	—	0.28
b1	0.20	0.22	0.24
C	0.10	—	0.19
c1	0.10	0.13	0.15
D	4.86	4.96	5.06
E	6.20	6.40	6.60
E1	4.30	4.40	4.50
e	0.65 BSC		
L	0.45	0.60	0.75
L1	1.00 REF		
L2	0.25 BSC		
R	0.09	—	—
R1	0.09	—	—
S	0.20	—	—
\theta 1	0°	—	8°
\theta 2	10°	12°	14°
\theta 3	10°	12°	14°



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