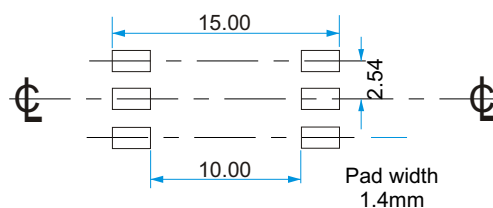
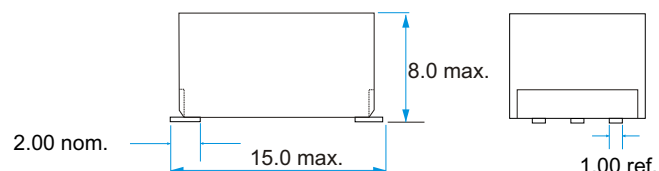
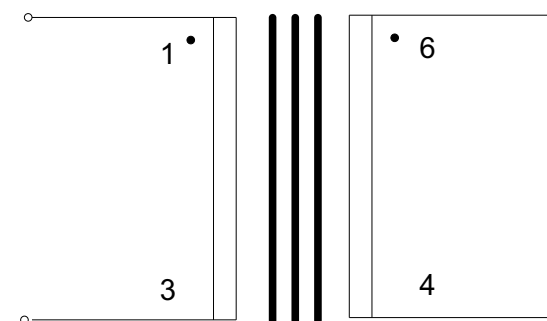


Part number
YYWW = date code



Suggested PCB layout



Electrical schematic

Electrical specification:

Ratio: 1 to 1

DC resistance (sum of windings): 200 - 245

Designed for matching 600

Parallel inductance, pins 1 - 3: see page 3

Leakage inductance: (10mVrms, 1kHz) pins 6-4 S/C: 4.9mH max.

Return loss: (ref. 430 ohms) see graph on page 2

Insertion loss: (ref. 600 ohms, 2kHz): 2dB max.

(ref. 430 ohms, 2kHz): 4dB max.

Frequency response: 200 - 4kHz: +/- 0.2dB

Longitudinal balance: 200Hz - 4kHz: 80dB min.

Turns ratio (@ 6kHz, 0.1Vrms), pins 1 - 3 to 6 - 4: 1.00 +/-1%

Distortion: see page 3

Saturation: 10Vrms max. @ 250VRMS, 50/60Hz

Hi-pot, primary to secondary: 5500VDC, 1mA for 2 seconds

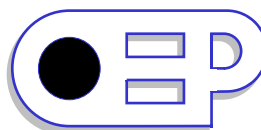
Operating temperature range: -20 to +85°C

Storage temperature range: -40 to +125°C

Certified to EN60950-1: 2001

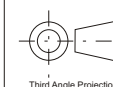
RoHS process compliant.

Note: Do not pass DC current through windings.



Unit 5, Oxonian Park, Langford Locks,
Kidlington, Oxfordshire. OX5 1FP
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Website: www.oep.co.uk

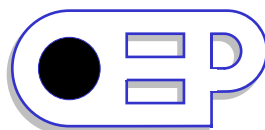
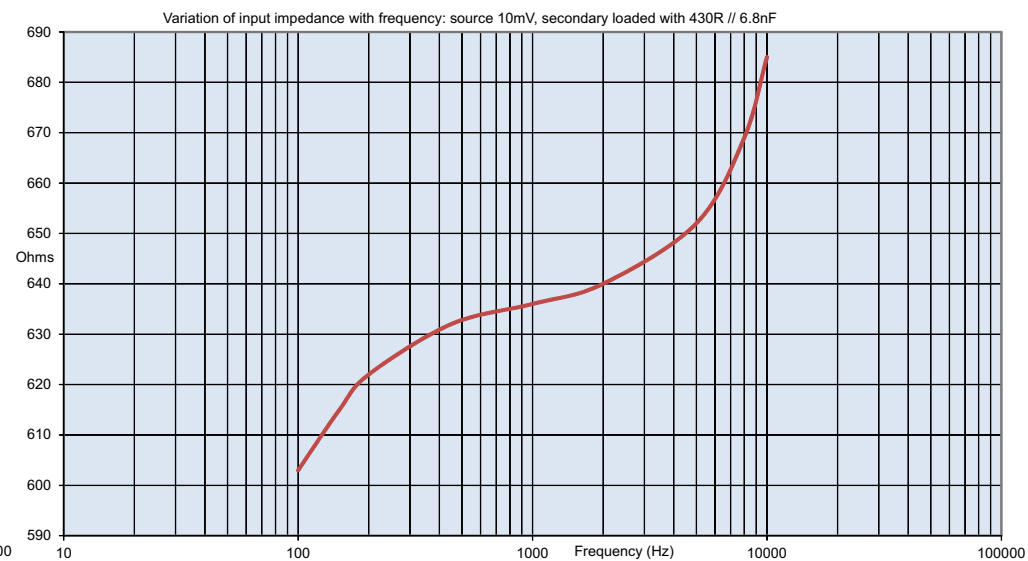
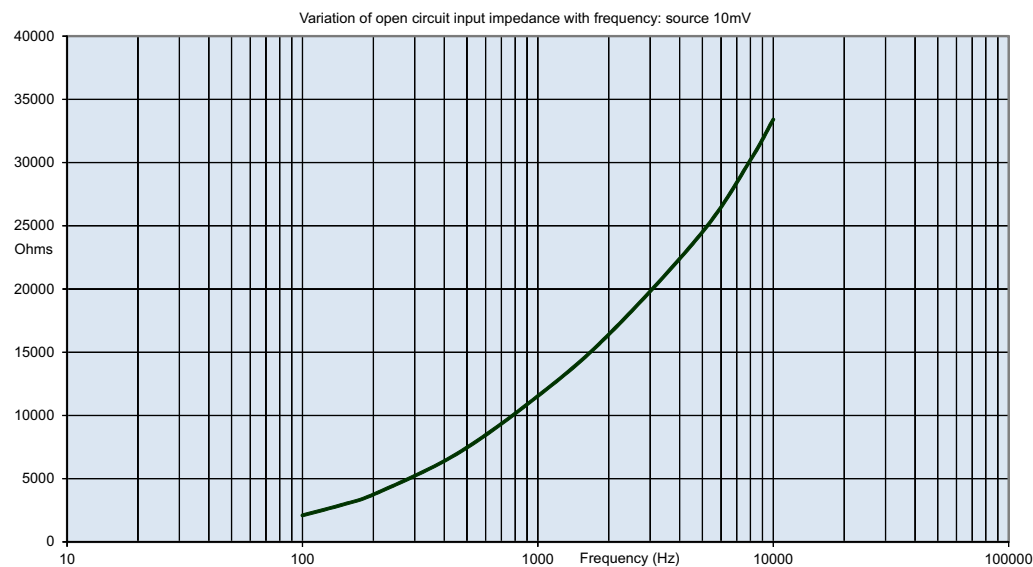
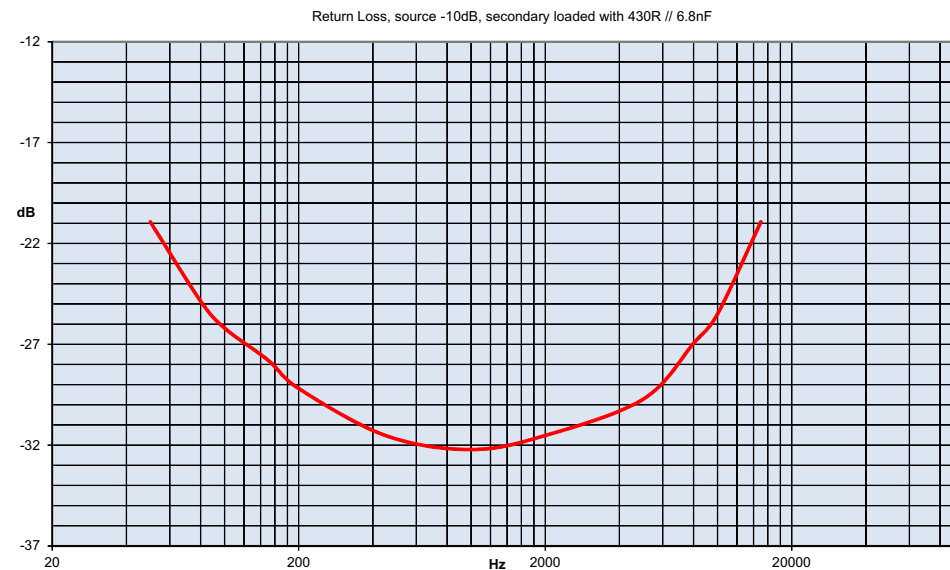
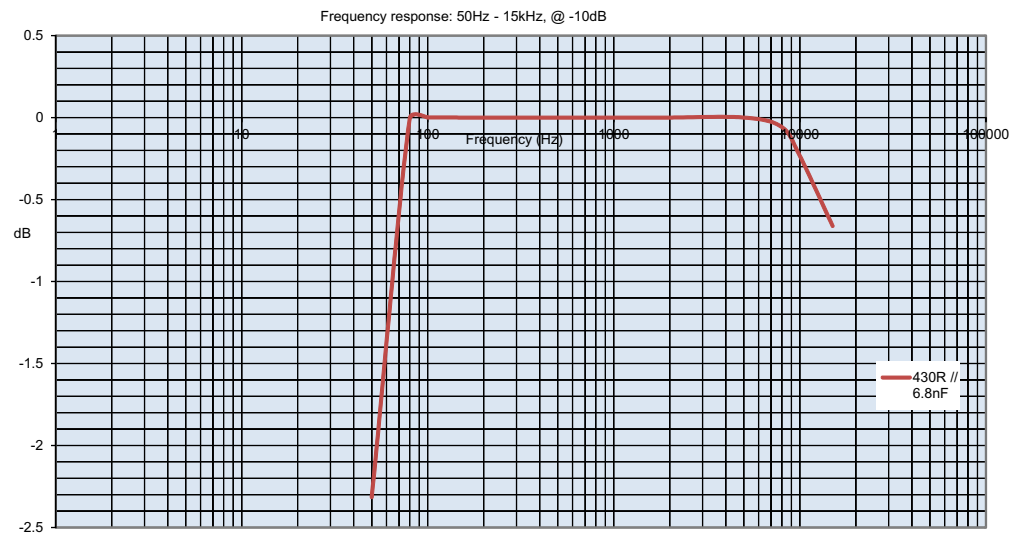
DESCRIPTION
Specification for OEP8000
page 1 of 3



Scale: nts

All dimensions in mm unless stated otherwise

ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
1	29/04/04	CS		OEP8000
11	12/02/18	CS		
12	05/06/19	CS		
13	02/12/19	CS		

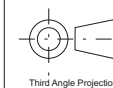


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DESCRIPTION

Specification for OEP8000

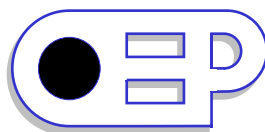
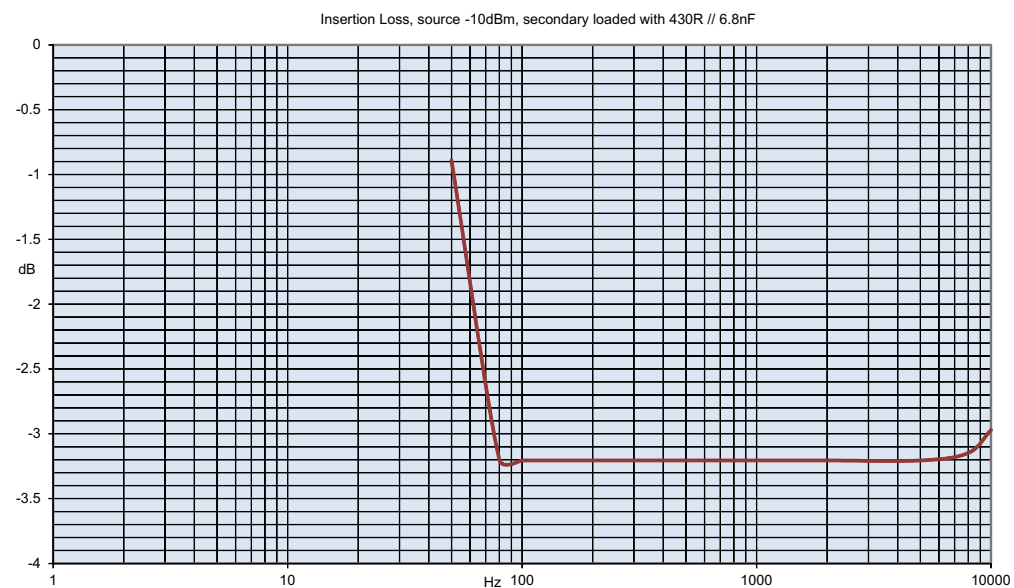
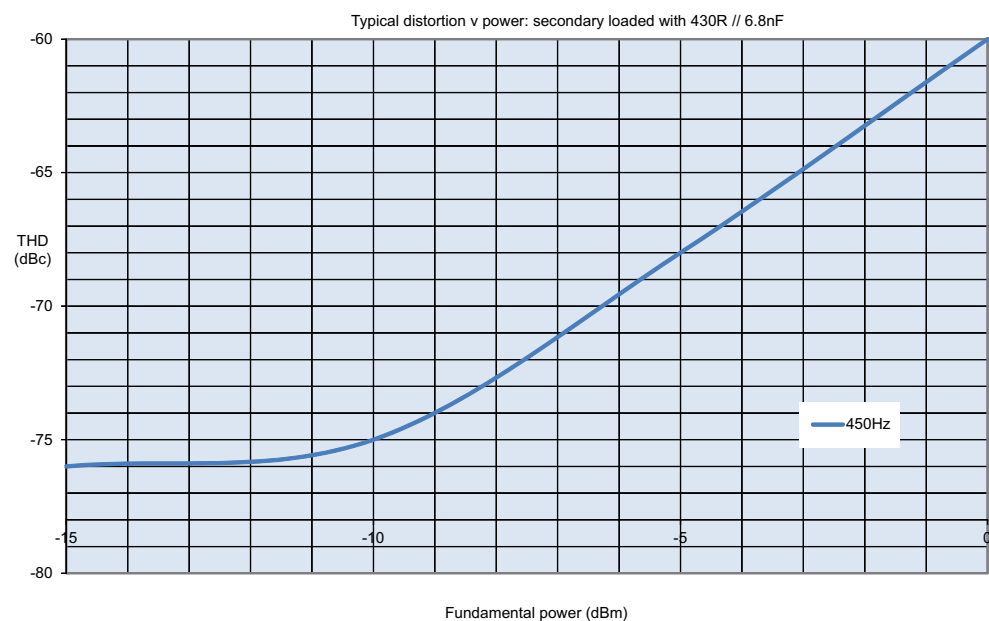
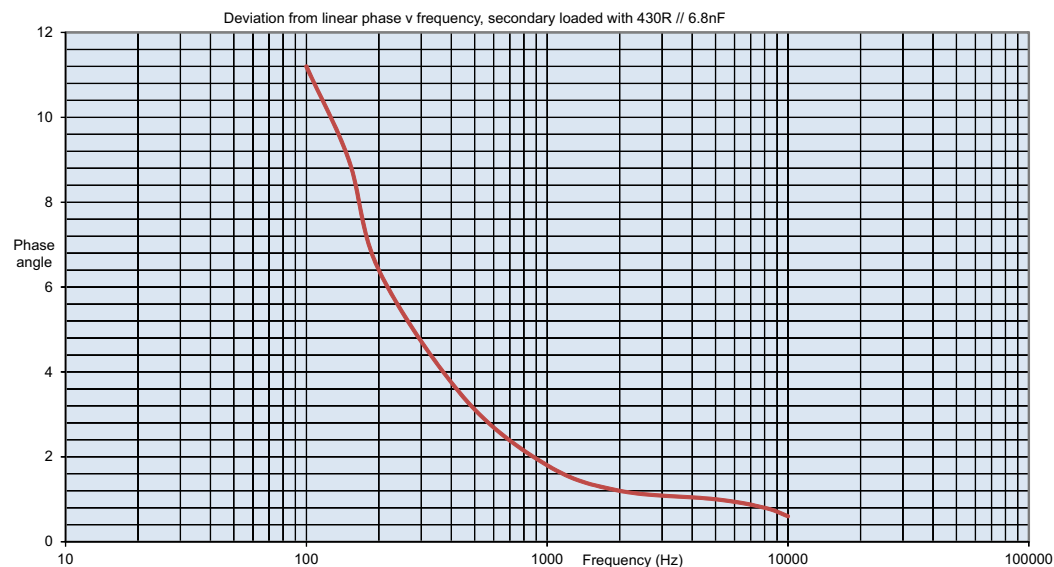
page 2 of 3



Scale: nts

All dimensions in mm unless stated otherwise

ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
1	29/04/04	CS		OEP8000
11	12/02/18	CS		
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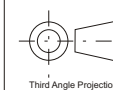
DESCRIPTION

Specification for OEP8000

page 3 of 3

Scale: nts

All dimensions in mm unless stated otherwise



ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
1	29/04/04	CS		OEP8000
11	12/02/18	CS		
12	05/06/19	CS		
13	02/12/19	CS		