



GRF2040

Broadband Linear Gain Block with Bypass 0.05 to 5 GHz

FEATURES

- Flexible Bias
- Internally Matched to 50 Ω
- Compact 1.5 x 1.5 mm DFN-6 Package
- Process: GaAs pHEMT

Gain Mode Reference: 5 V / 90 mA / 2 GHz

- Gain: 10.2 dB
- OP1dB: 20.9 dBm
- OIP3: 35.3 dBm
- Evaluation Board Noise Figure: 3.4 dB

Bypass Mode Reference: 5 V / 0.5 mA / 2 GHz

- Gain: -1.6 dB
- OP1dB: 25.2 dBm
- OIP3: 43.4 dBm

APPLICATIONS

- Systems Requiring Flat Gain
- IF Amplifier
- Linear Driver Amplifier
- Small Cells and Cellular Repeaters

DESCRIPTION

The GRF2040 is a broadband gain block with exceptional gain flatness for small cell, wireless infrastructure and other high-performance applications. It exhibits outstanding broadband noise figure (NF) and linearity from 400 to 4000 MHz with a single match and can be optimized for applications from 50 MHz to 5 GHz.

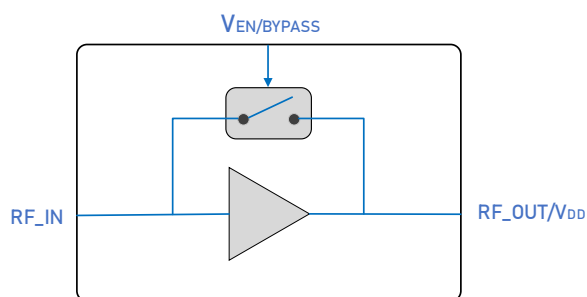
The device can be operated from a supply voltage of 2.7 to 8 volts with a selectable I_{DDQ} range from 15 to 90 mA for optimal efficiency and linearity.

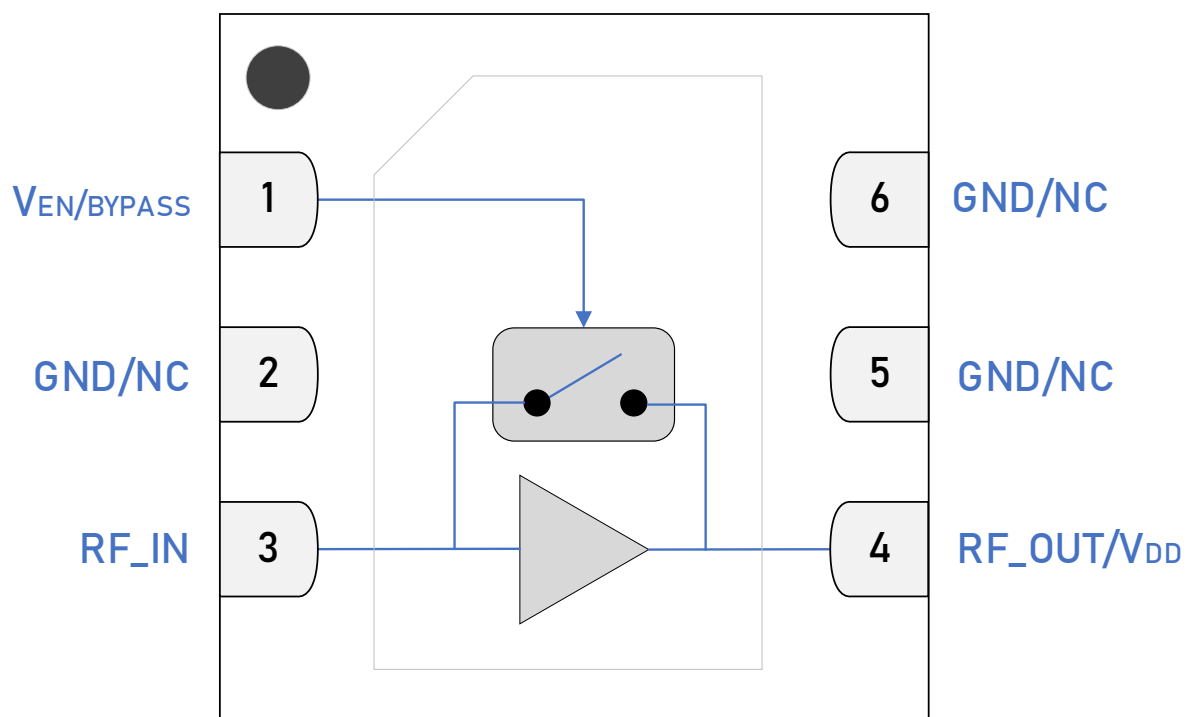
The GRF2040 is internally matched to 50 Ω at the input and output ports requiring only external DC blocks and a bias choke on the output.

Please consult with the GRF applications engineering team for custom tuning/evaluation board data and device S-parameters.

Additional tunes can be found on the GRF2040 "Custom Tunes" product page: [GRF2040 Custom Tunes](#)

BLOCK DIAGRAM





DFN-6 1.5 x 1.5 mm Pin Out (Top View)

Pin Assignments

Pin	Name	Description	Note
1	V _{ENABLE/BYPASS}	V _{EN/BYPASS} Voltage Input	V _{EN/BYPASS} and series resistor sets I _{DDQ} . V _{EN/BYPASS} ≤ 0.2 volts disables the device. On-die pull-down resistor will turn the device off if this node is allowed to float. See Note.
2, 5, 6	GND/NC	Ground or No Connect	No internal connection to die. These pins can be left unconnected or connected to ground (highly recommended). Use a via as close to the pin as possible if grounded.
3	RF_IN	RF Input	Internally matched to 50 Ω. An external DC blocking capacitor must be used.
4	RF_OUT/V _{DD}	RF Output	Internally matched to 50 Ω. V _{DD} must be applied through an RF choke to this pin.
PKG BASE	GND	Ground	Provides DC and RF ground for device, as well as thermal heat sink. Recommend multiple 8-mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

Note: Pin 1 voltage should not exceed 3.0 volts due to excessive resulting I_{ENABLE}.

Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V_{DD}	0	9	V
RF Input Power: Load VSWR < 2:1, $V_{DD} \leq 8$ V.	$P_{IN\ MAX}$		27	dBm
Operating Temperature (package base).	$T_{PKG\ BASE}$	-40	105	°C
Maximum Channel Temperature (MTTF > 10 ⁶ hours).	T_{MAX}		170	°C
Maximum Dissipated Power	$P_{DISS\ MAX}$		1	W

Electrostatic Discharge

Human Body Model	HBM	200		V
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Storage

Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	--



Caution! ESD Sensitive Device.

Exceeding Absolute Maximum Rating conditions may cause permanent damage.

Note: For additional information, please refer to [Package Manufacturing Information | Guerrilla RF \(guerrilla-rf.com\)](#)



All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging requiring no exemptions. Additional information for this topic can be found at this link - [Environmental and Restricted Substance Statement Library](#)

Recommended Operating Conditions

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	V _{DD}	2.7	5	9	V	
RF Frequency Range	F _{RF}	0.05	2	5	GHz	Typical application schematic with external matching components (notes 1 & 2).
Operating Temperature (package base)	T _{PKG BASE}	-40		105	°C	
RF_IN Port Impedance	Z _{RF_IN}		50		Ω	Single-ended.
RF_OUT Port Impedance	Z _{RF_OUT}		50		Ω	Single-ended.

Note 1: Operation outside of this range is supported by using different custom tunes. Examples of other optimized tunes can be found here: [GRF2040 Custom Tunes](#)

Note 2: Contact the Guerrilla RF applications team for guidance on optimizing the tuning of the device for alternative bands.

Nominal Operating Parameters – General

The following conditions apply unless noted otherwise: typical application schematic, $V_{DD} = 5\text{ V}$, $F_{TEST} = 2\text{ GHz}$, $50\ \Omega$ system impedance, $T_{PKG\ BASE} = 25\text{ }^{\circ}\text{C}$. Evaluation board losses are included within the specifications.

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Current	I_{DD}		90		mA	$V_{DD} = 5\text{ V}$, $V_{EN/BYPASS} = 5\text{ V}$.
Enable Current	I_{ENABLE}		5		mA	$V_{DD} = 5\text{ V}$, $V_{EN/BYPASS} = 5\text{ V}$.
Switching Rise Time	T_{RISE}		80		ns	Bypass mode to Gain mode (note 3).
Switching Fall Time	T_{FALL}		30		ns	Gain mode to Bypass mode (note 4).

Thermal Data

Thermal Resistance (Infrared Scan)	Θ_{JC}		75		$^{\circ}\text{C/W}$	On standard evaluation board (note 5).
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Note 3: Switching Time: 50% of $V_{EN/BYPASS}$ to 90% of P_{out} .

Note 4: Switching Time: 50% of $V_{EN/BYPASS}$ to 10% of P_{out} .

Note 5: MTTF > 10^6 hours for $T_j \leq 170\text{ }^{\circ}\text{C}$.

Nominal Operating Parameters – RF

The following conditions apply unless noted otherwise: typical application schematic, $V_{DD} = 5\text{ V}$, $F_{TEST} = 2\text{ GHz}$, $50\ \Omega$ system impedance, $T_{PKG\ BASE} = 25\ ^\circ\text{C}$. Evaluation board losses are included within the specifications.

Gain Mode

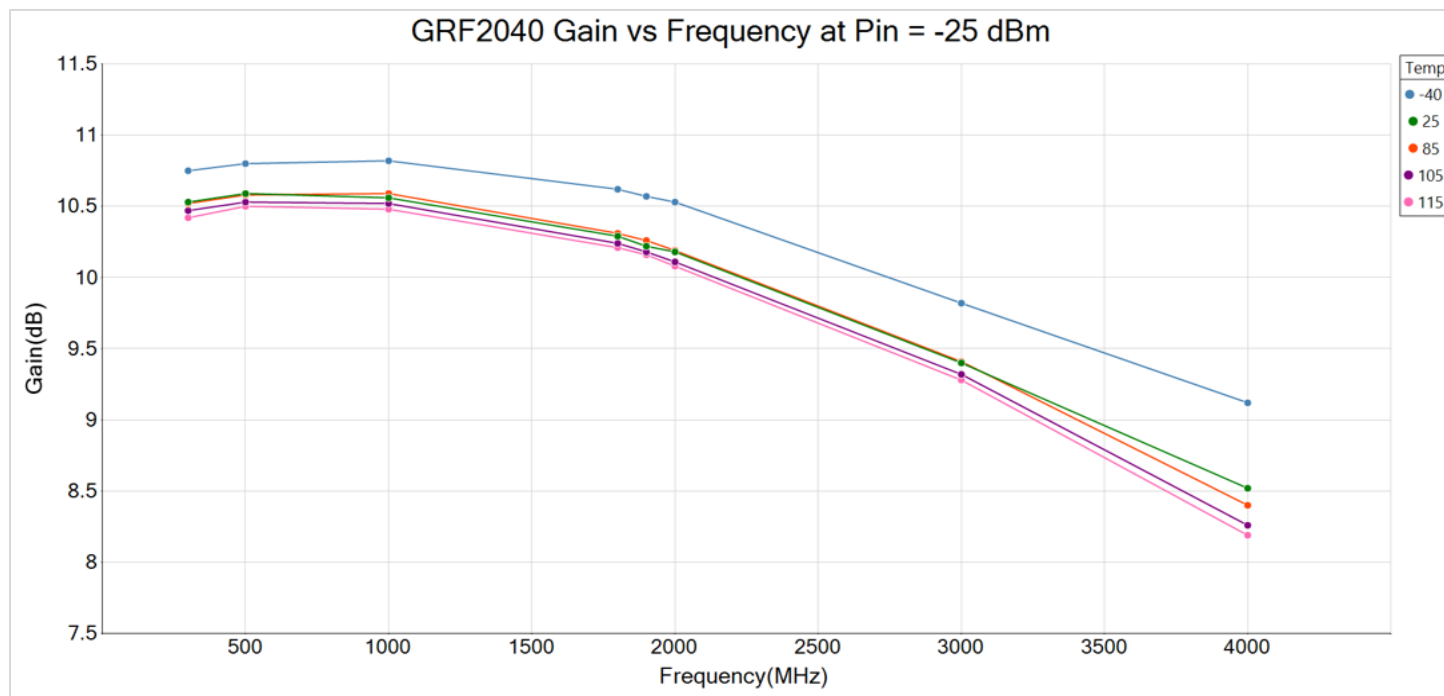
Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Current	I_{DD}		90		mA	$V_{DD} = 5\text{ V}$, $V_{EN/BYPASS} = 5\text{ V}$.
Gain	S_{21}		10.2		dB	$V_{DD} = 5\text{ V}$, $V_{EN/BYPASS} = 5\text{ V}$.
Output 1 dB Compression Power	OP1dB		20.9		dBm	
Output 3rd Order Intercept Point	OIP3		35.3		dBm	2 dBm P_{OUT} per tone at 2 MHz spacing (1999 and 2001 MHz).
Noise Figure	NF		3.4		dB	On standard evaluation board.

Bypass Mode

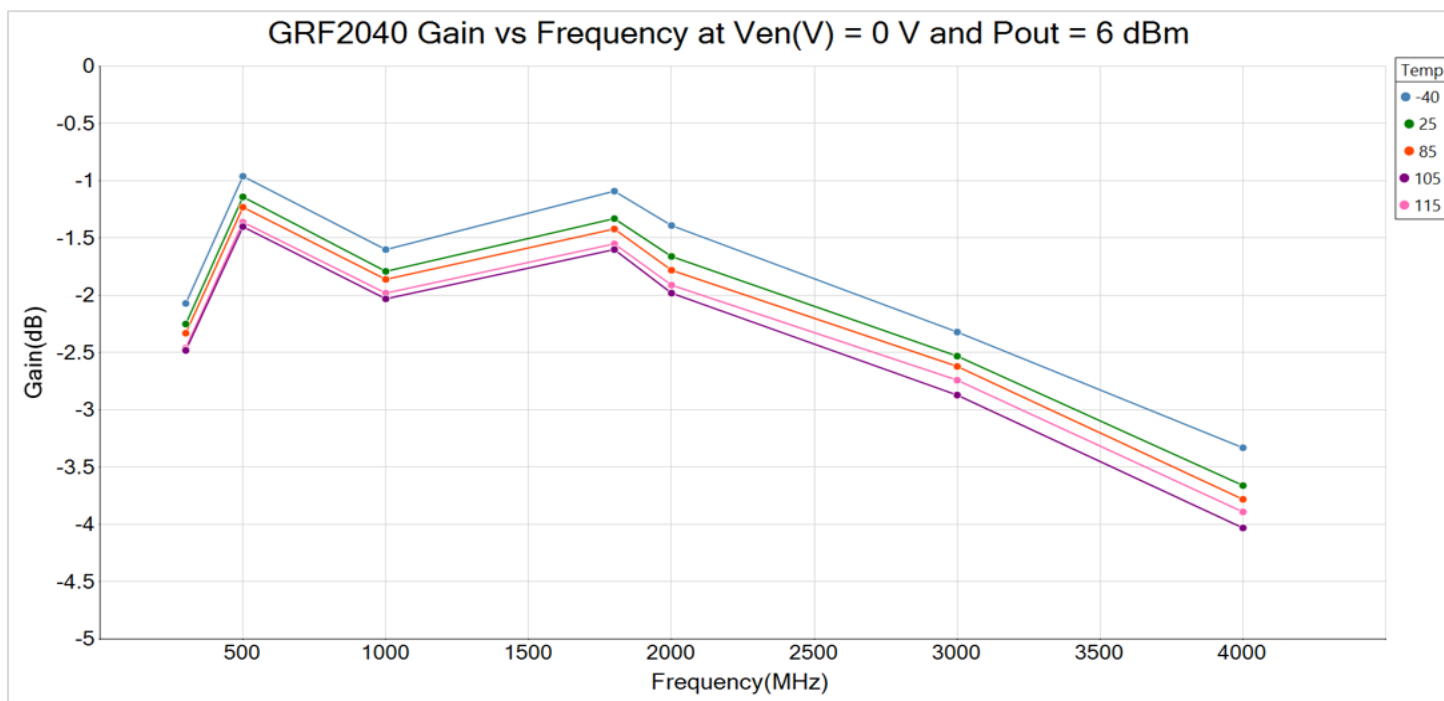
Supply Current	I_{DD}		0.5		mA	$V_{DD} = 5\text{ V}$, $V_{EN/BYPASS} = 5\text{ V}$.
Gain	S_{21}		-1.6		dB	$V_{DD} = 5\text{ V}$, $V_{EN/BYPASS} = 5\text{ V}$.
Output 1 dB Compression Power	OP1dB		25.2		dBm	
Output 3rd Order Intercept Point	OIP3		43.4		dBm	2 dBm P_{OUT} per tone at 2 MHz spacing (1999 and 2001 MHz).

GRF2040 Typical Operating Curves: 0.4 to 4 GHz Tune

Gain Mode

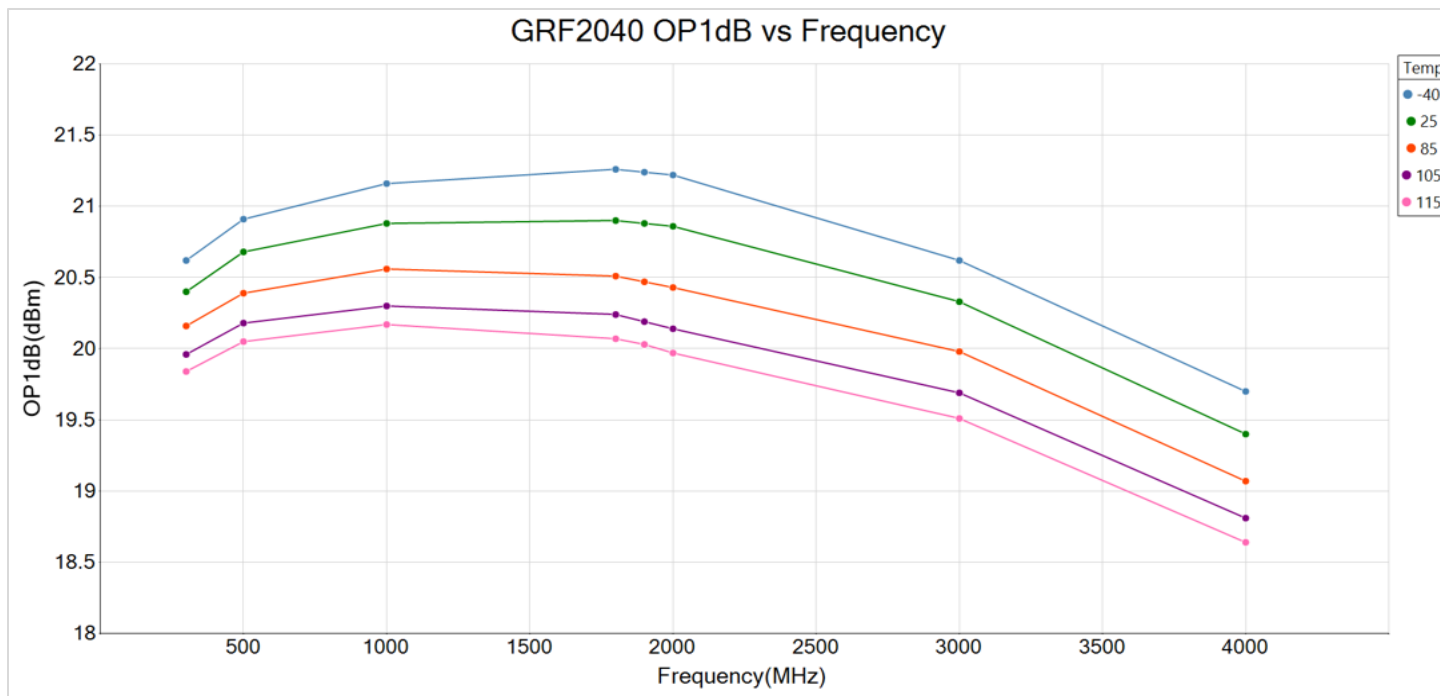


Bypass Mode

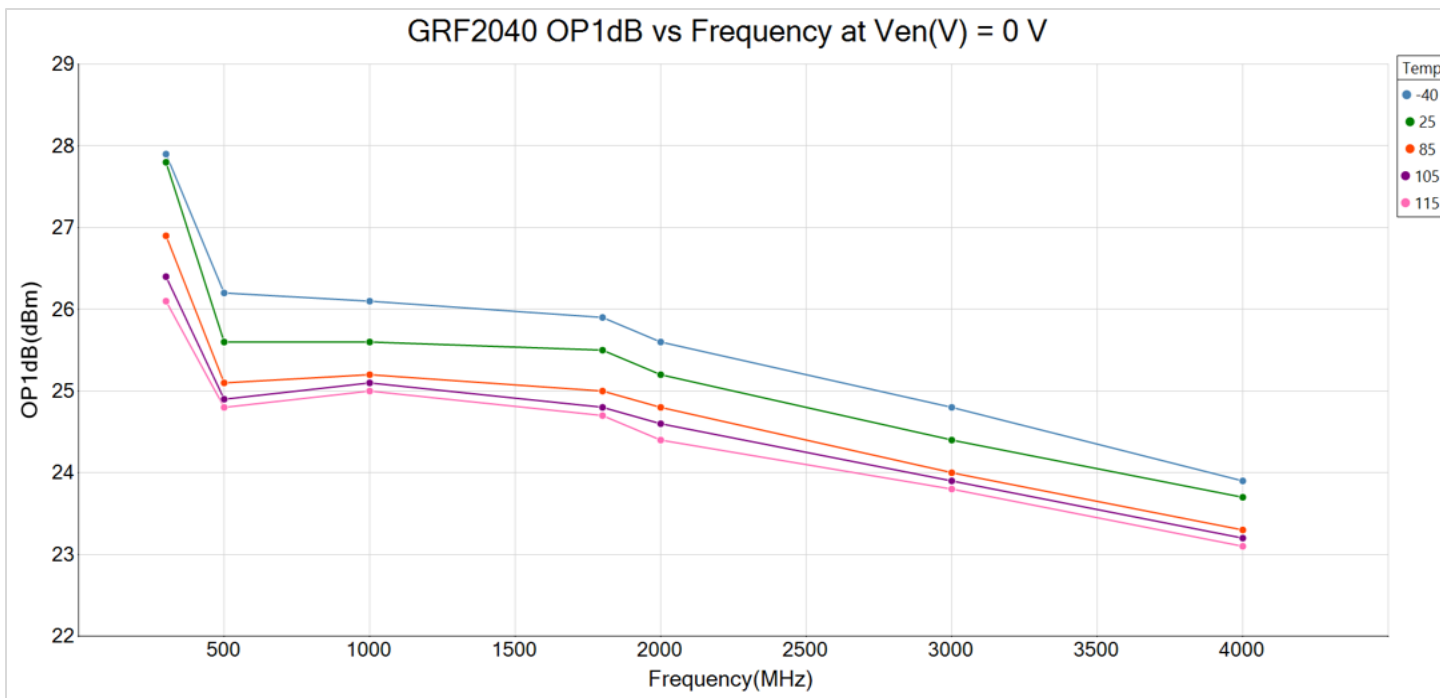


GRF2040 Typical Operating Curves: 0.4 to 4 GHz Tune

Gain Mode

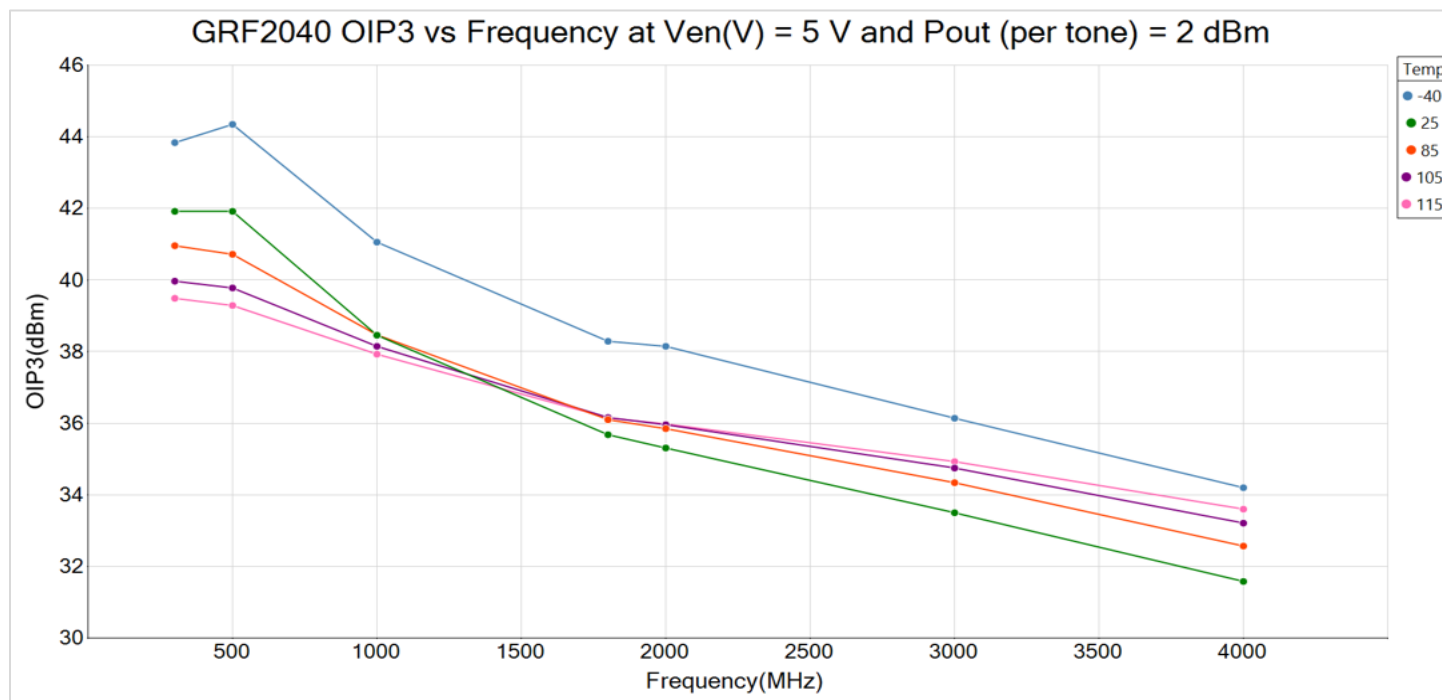


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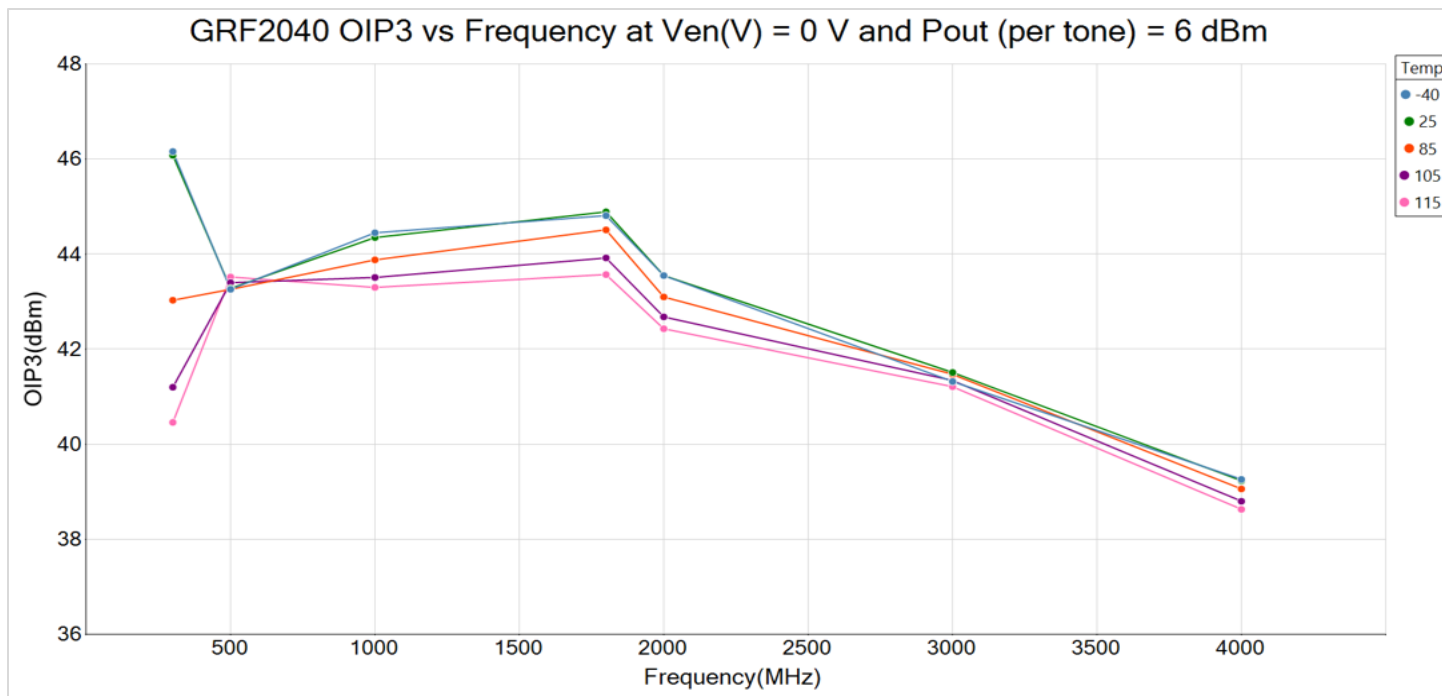


GRF2040 Typical Operating Curves: 0.4 to 4 GHz Tune

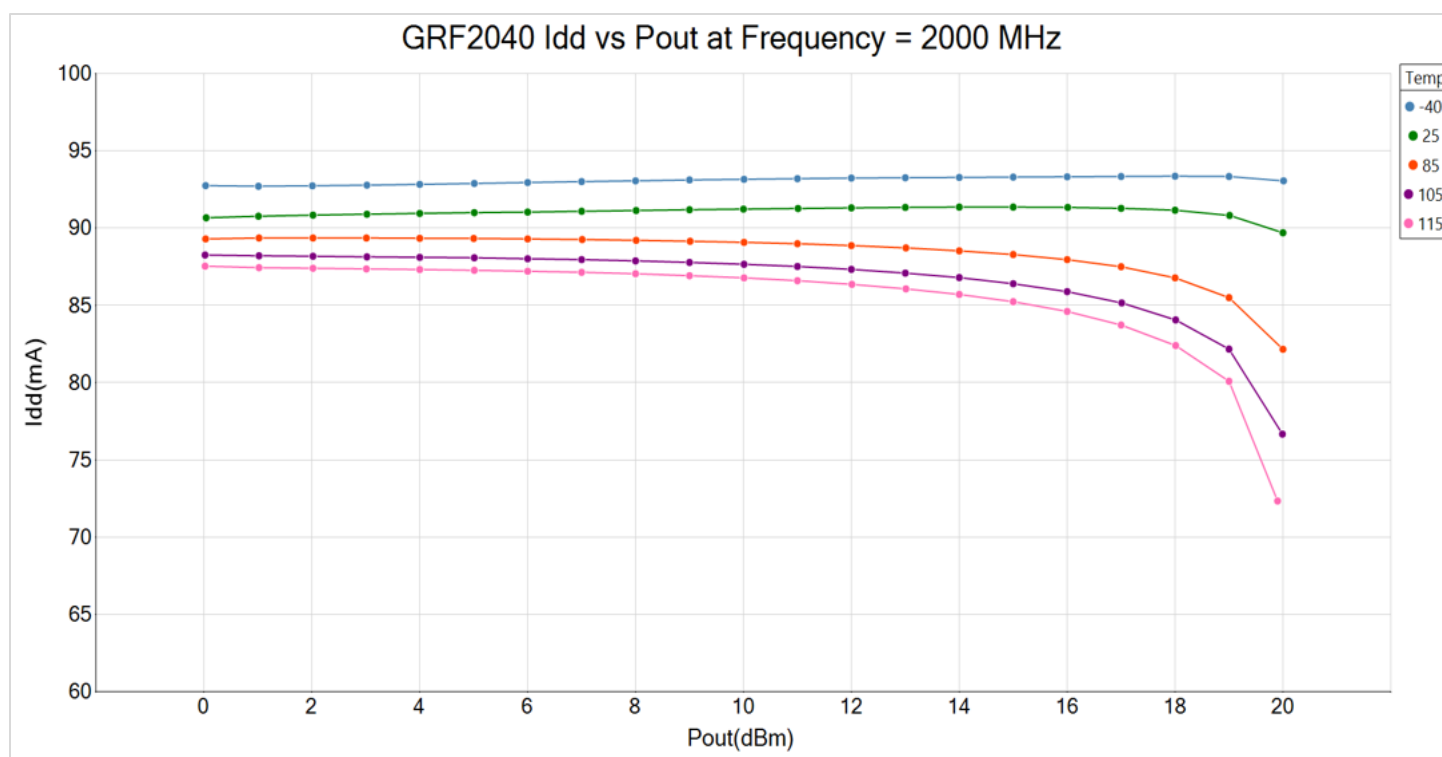
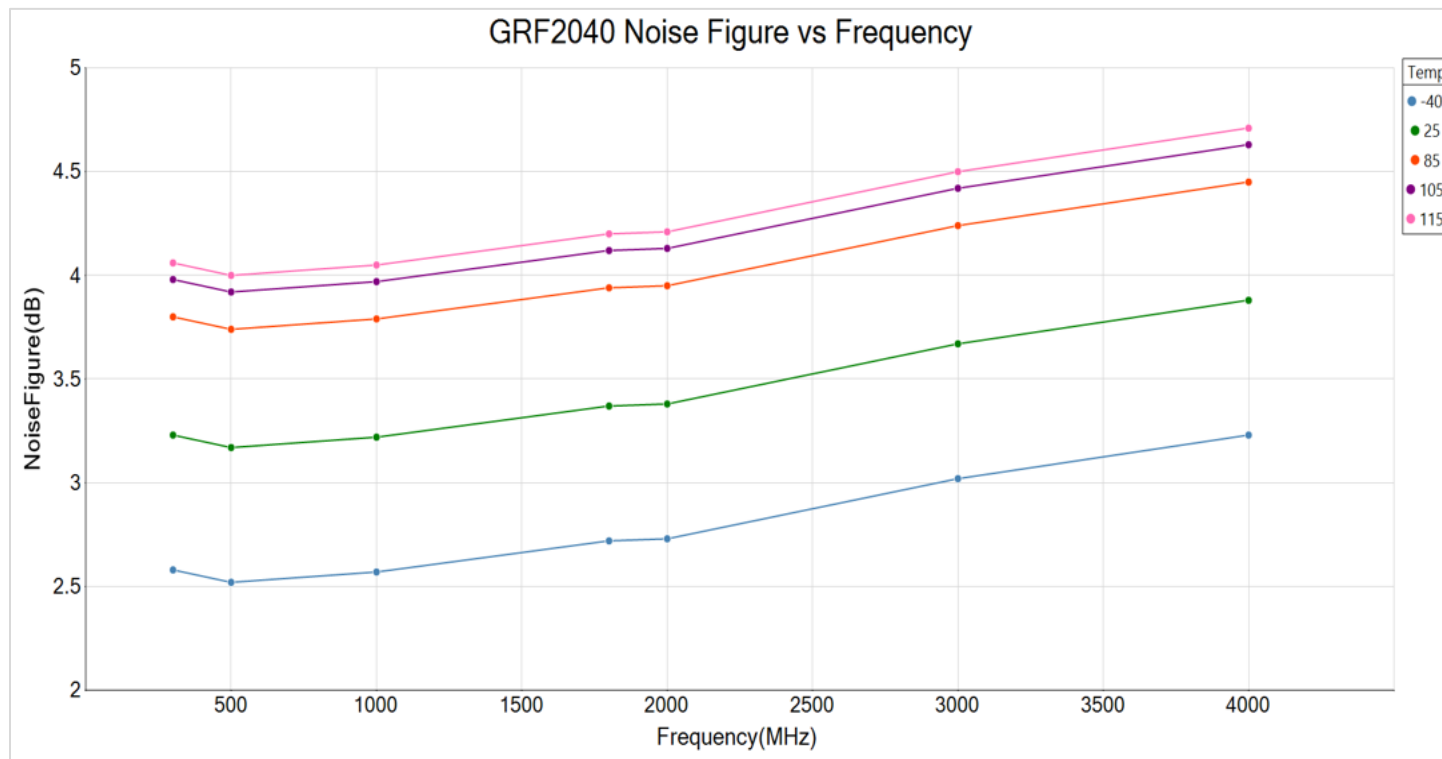
Gain Mode



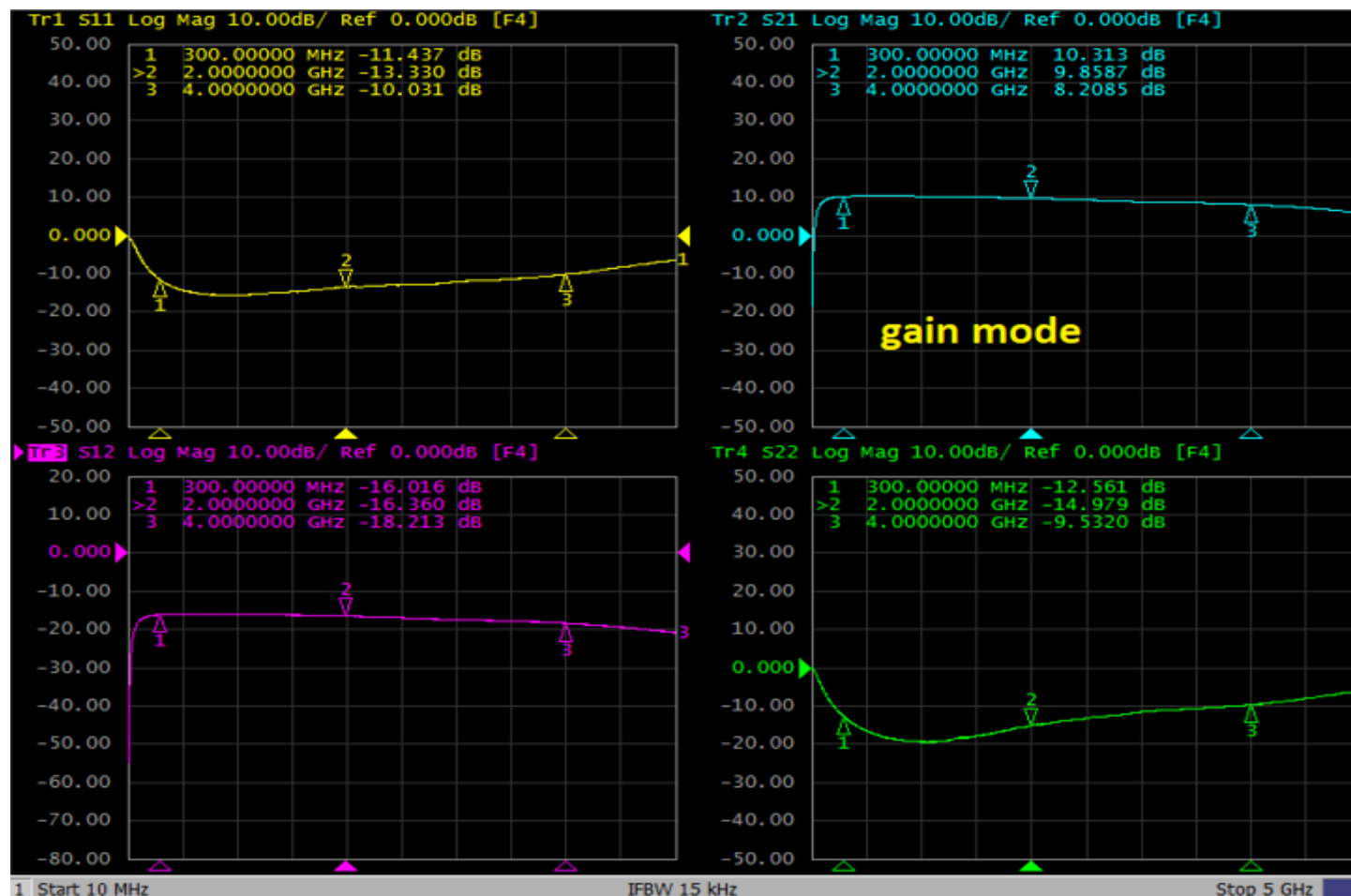
Bypass Mode



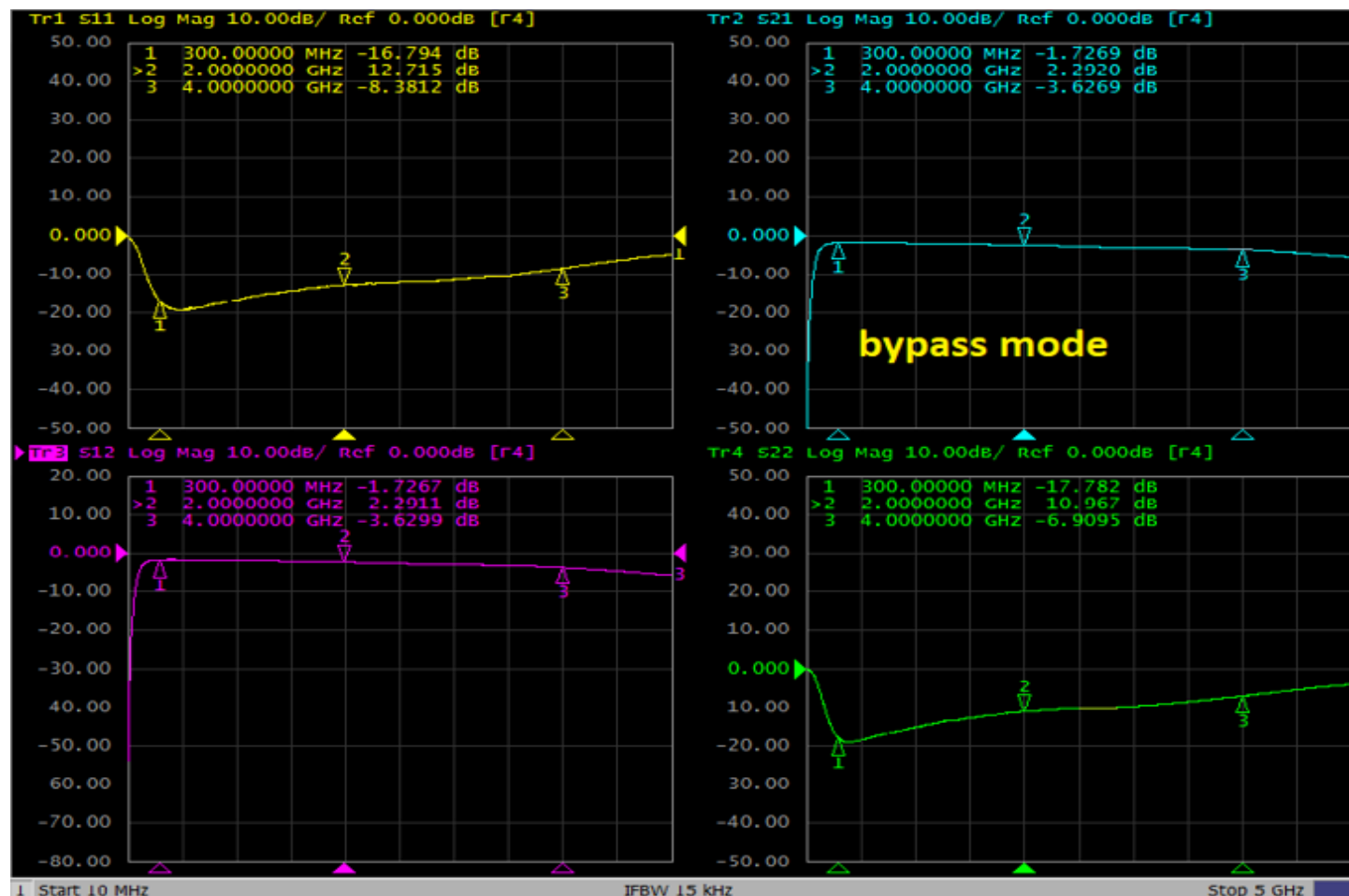
GRF2040 Typical Operating Curves: 0.4 to 4 GHz Tune



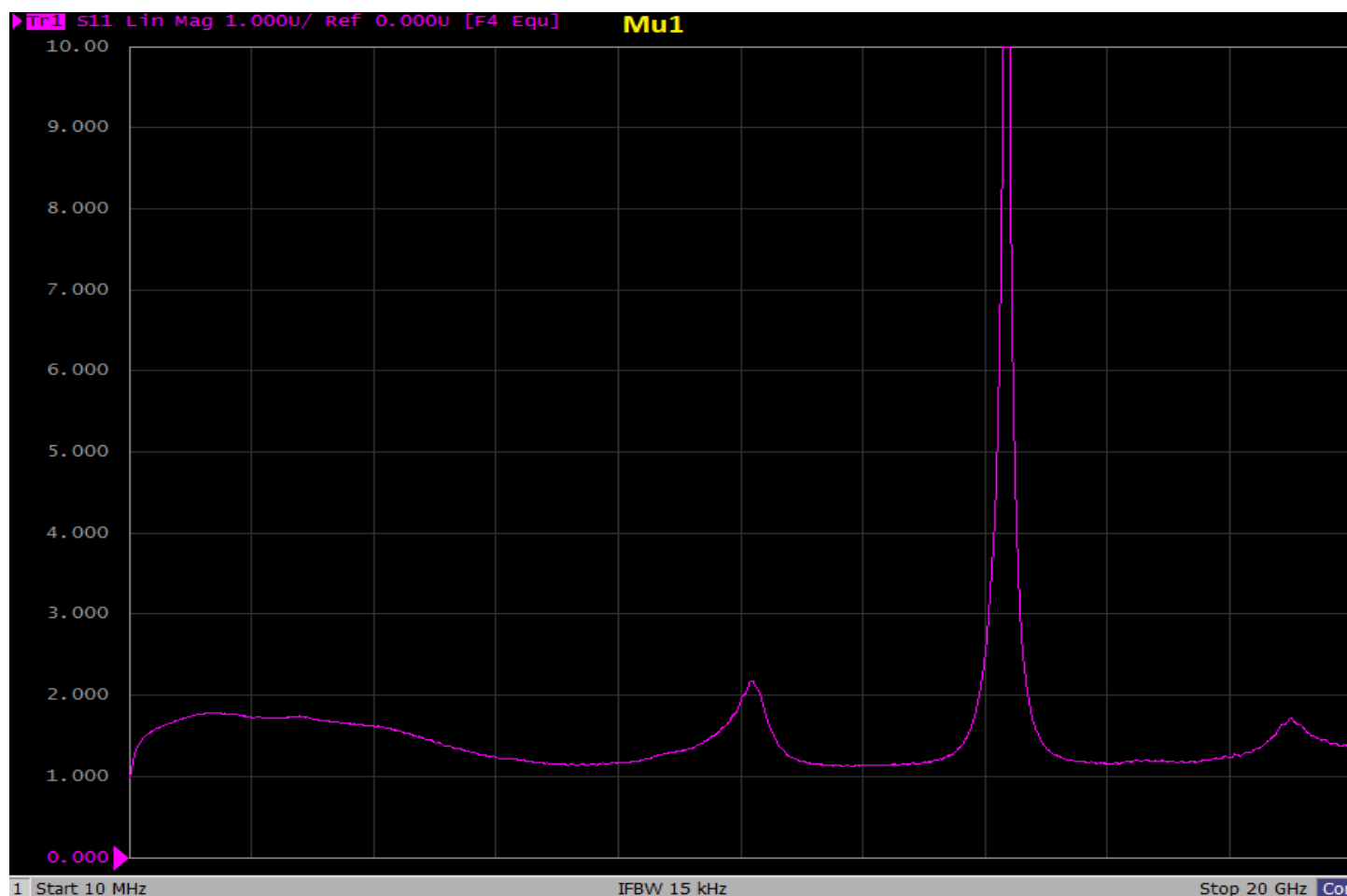
GRF2040 Typical Operating Curves: S-Parameters (300 MHz to 4 GHz)



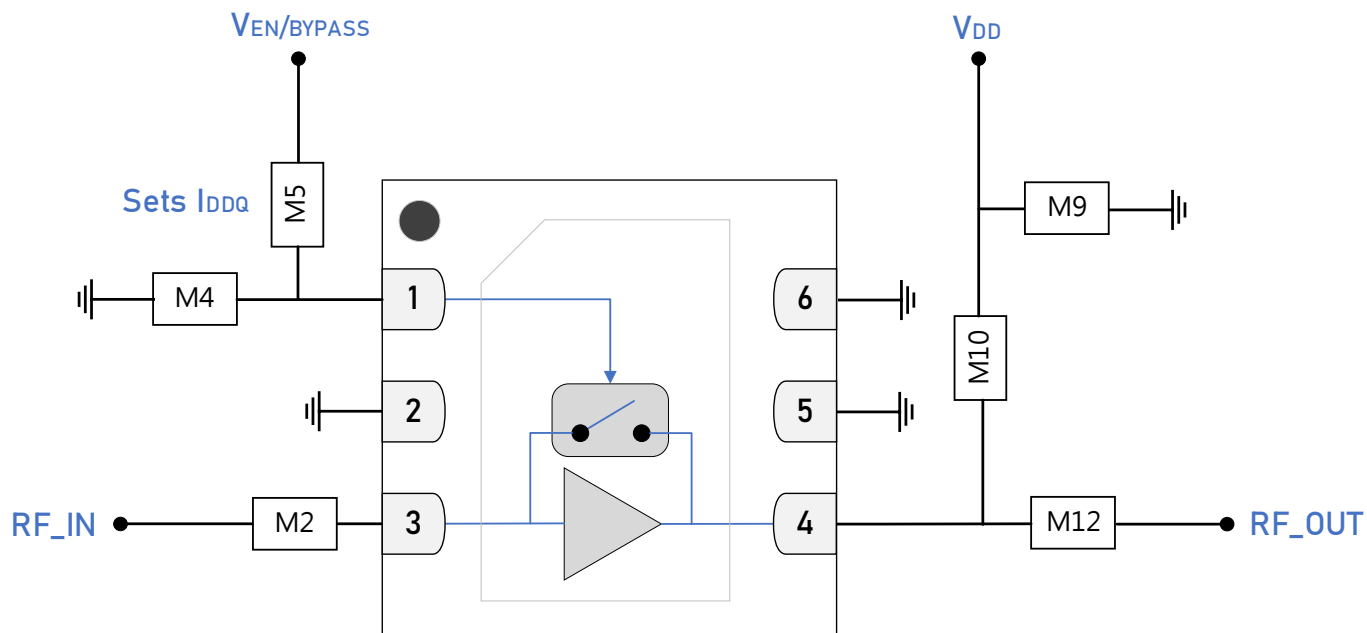
GRF2040 Typical Operating Curves: S-Parameters (300 MHz to 4 GHz)



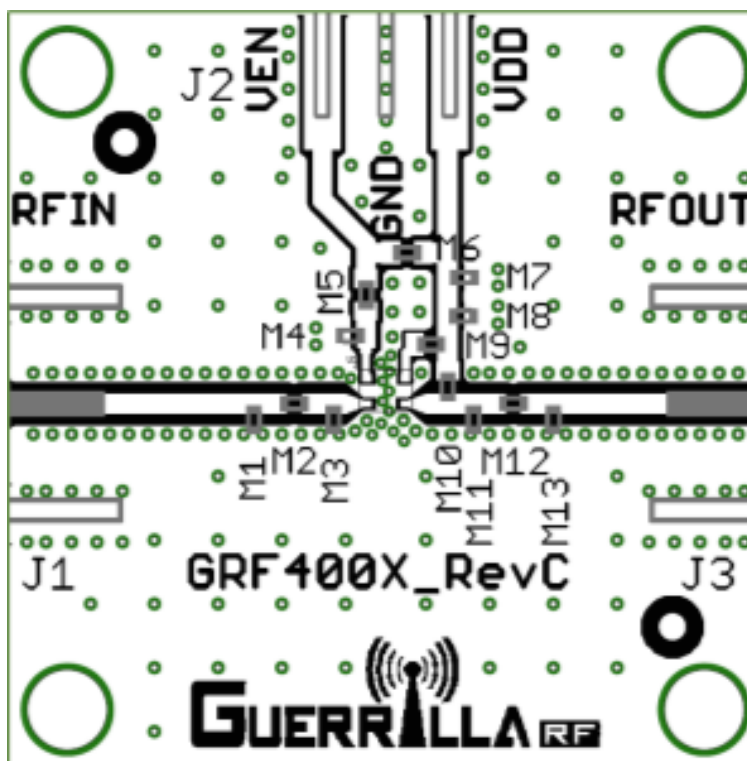
GRF2040 Typical Operating Curves: Stability Mu Factor (10 MHz to 20 GHz)



Note: Mu factor ≥ 1.0 implies unconditional stability.



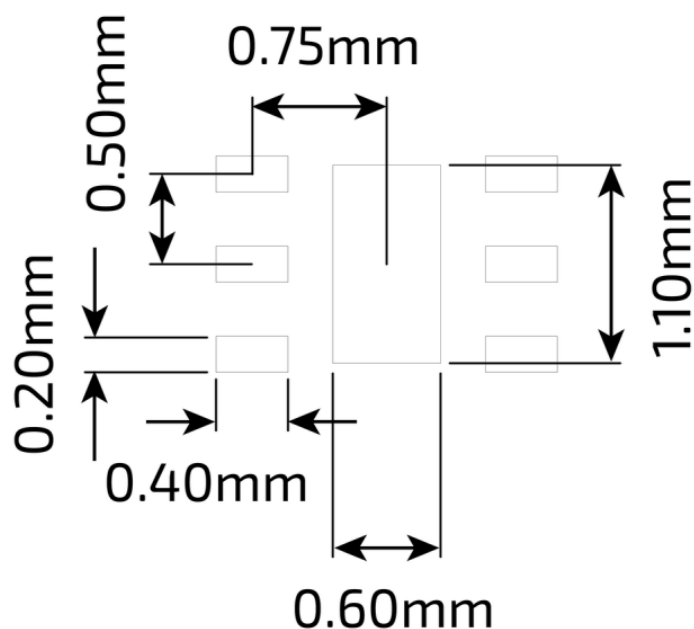
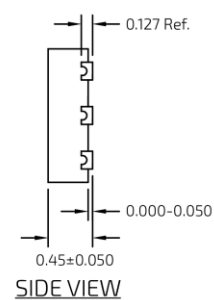
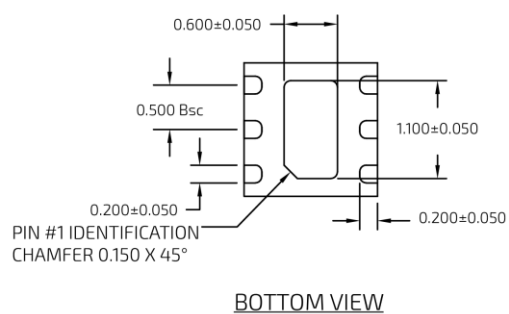
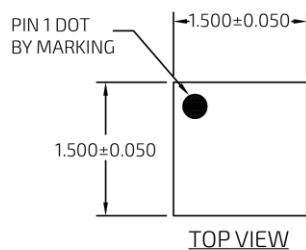
GRF2040 Standard Evaluation Board Schematic



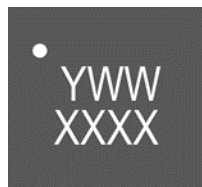
GRF2040 Evaluation Board Assembly Diagram

GRF2040 Evaluation Board Assembly Diagram Reference: 0.4 to 4 GHz Tune

Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M2	Capacitor	Murata	GRM	100 pF	0402	ok
M4	Capacitor	Murata	GRM	100 pF	0402	ok
M5 (sets I _{DDQ})	Resistor	Various	5%	500 Ω	0402	ok
M9	Capacitor	Murata	GRM	0.1 μF	0402	ok
M10	Inductor	Murata	LQG	47 nH	0402	ok
M12	Capacitor	Murata	GRM	100 pF	0402	ok
Evaluation Board	GRF400X_RevC					


DFN-6 1.5 x 1.5 mm Suggested PCB Footprint (Top View)

DFN-6 1.5 x 1.5 mm DFN-6 Package Dimensions

Package Marking Diagram



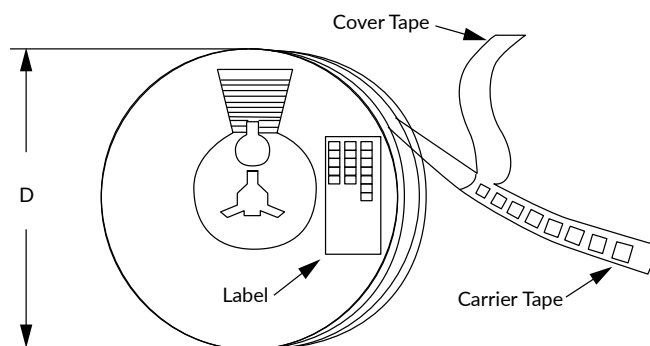
- Line 1: "Y" = YEAR (single digit). "WW" = WORK WEEK the device was assembled.
- Line 2: "XXXX" = Device PART NUMBER.

Tape and Reel Information

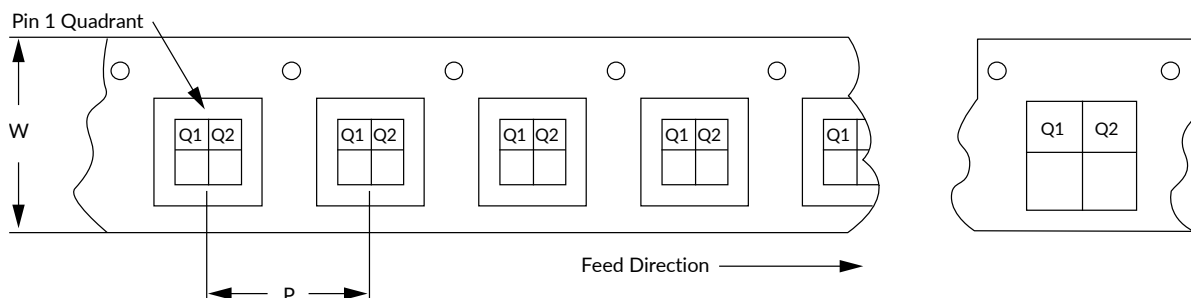
Guerrilla RF's tape and reel specification complies with Electronic Industries Alliance (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). See the following page for the Tape and Reel Specification and Device Package Information table, which includes units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag and the outside surface of the box.

For the Tape and Reel Reference Table, please refer to: [Package Manufacturing Information | Guerrilla RF \(guerrilla-rf.com\)](#)



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information

Revision History

Revision Date	Description of Change
October 12, 2023	Preliminary Data Sheet.
May 31, 2024	Updated HBM specification.
September 9, 2024	Release Ø Data Sheet. Updated RF specifications and plots. Changed HBM from 125 to 200 volts.
November 20, 2025	Upgraded Data Sheet to new format only. No change to device or device specifications.



Data Sheet Classifications

Data Sheet Status	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on limited evaluation board measurements taken within the Guerrilla RF Applications Lab. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with <i>production-released</i> material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using <i>modelled estimates</i> that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material <i>derived from multiple lots which have been fabricated over an extended period of time</i> . MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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