

ULN2003

DATASHEET

Specification Revision History:

Version	Date	Description
V1.0	2019/02	New
V1.1	2021/11	Modify Ordering Information
V1.2	2025/02	Modify Ordering Information
V1.3	2025/03	Add application precautions and overall typesetting.

General Description

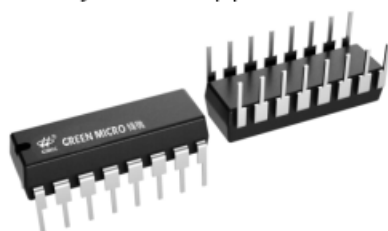
The ULN2003 is high-voltage high-current Darlington transistor arrays each containing seven open collector common emitter pairs. Each pair is rated at 500mA. Suppression diodes are included for inductive load driving, the inputs and outputs are pinned in opposition to simplify board layout.

These devices are capable of driving a wide range of loads including solenoids, relays, DC motors, LED displays, filament lamps, thermal print-heads and high-power buffers.

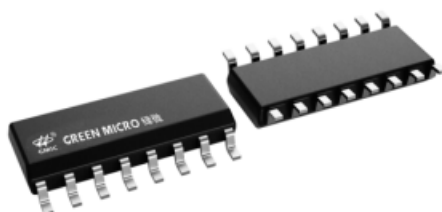
The ULN2003 is available in both a small outline 16-pin package (DIP16, SOP16 and SSOP16)

Features

- 500-mA-Rated Collector Current (single output)
- High-Voltage Outputs: 50V
- Output Clamp Diodes
- Inputs Compatible With Various Types of Logic
- Relay-Driver Applications



DIP-16



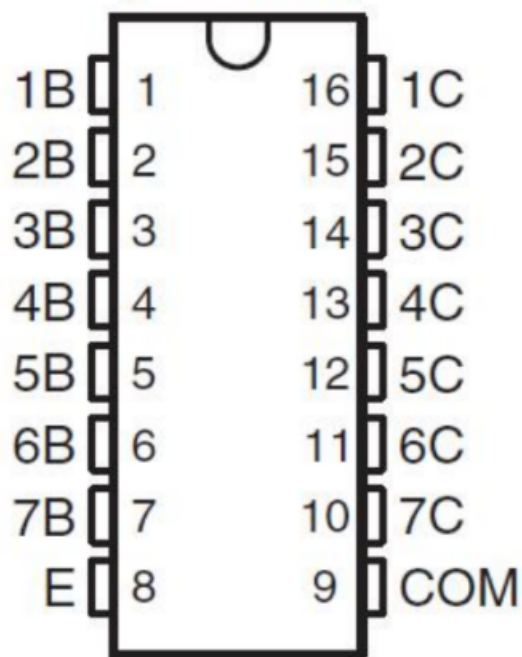
SOP-16

Ordering Information

Product Model	Package Type	Marking	Packing	Packing Qty
ULN2003AN225	DIP-16	ULN2003 D225	TUBE	1000PCS/BOX
ULN2003AES	SOP-16	ULN2003 S212	REEL	2500PCS/REEL
ULN2003APG	DIP-16	ULN2003 B25	TUBE	1000PCS/BOX
ULN2003A	SOP-16	ULN2003 G212	REEL	2500PCS/REEL

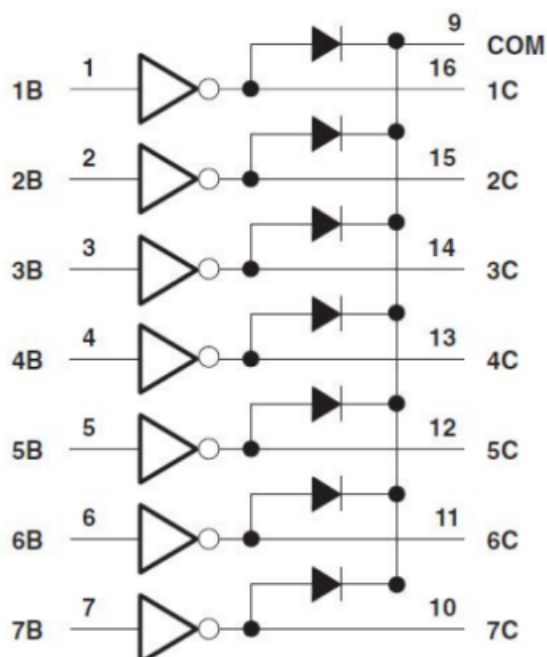
Pin Assignments

(TOP VIEW)



Connection Diagram

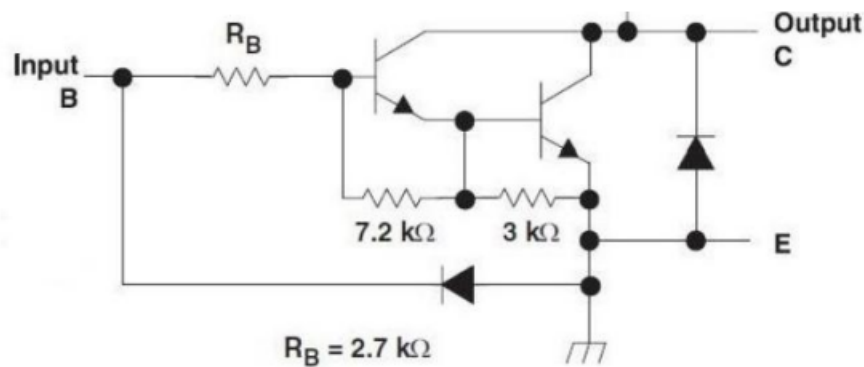
LOGIC DIAGRAM



Pin Descriptions

Pin Number	Pin Name	Function
1	1B	Input pair1
2	2B	Input pair1
3	3B	Input pair1
4	4B	Input pair1
5	5B	Input pair1
6	6B	Input pair1
7	7B	Input pair1
8	E	Common Emitter(ground)
9	COM	Common Clamp Diodes
10	7C	Output pair7
11	6C	Output pair6
12	5C	Output pair5
13	4C	Output pair4
14	3C	Output pair3
15	2C	Output pair2
16	1C	Output pair1

Functional Block Diagram



Note: All resistor values shown are nominal.

The collector-emitter diode is a parasitic structure and should not be used to conduct current. If the collector(s) go below ground an external Schottky diode should be added to clamp negative undershoots.

Absolute Maximum Ratings(1)

At 25°C free-air temperature(unless otherwise noted)

Symbol	Parameter		Min	Max	Unit
V_{CC}	Collector to emitter voltage			50	V
V_R	Clamp diode reverse voltage(2)			50	V
V_I	Input voltage(2)			30	V
I_{CP}	Peak collector current	See typical characteristics		500	mA
I_{OK}	Output clamp current			500	mA
I_{TE}	Total emitter-terminal current			-2.5	A
T_A	Operating free-air temperature range	ULN2003	-20	70	°C
θ_{JA}	Thermal Resistance Junction-to-Ambient(3)			63	°C/W
θ_{JC}	Thermal Resistance Junction-to-Case(4)			12	
T_J	Operating virtual junction temperature			150	°C
T_{STG}	Storage temperature range		-65	150	°C
ESD	Human Body Mode		-	3000	V

(1)Stresses beyond those listed under "absolute maximum ratings"may cause permanent damage to the device.

These are stress ratings only,and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions"is not implied.Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2)All voltage values are with respect to the emitter/substrate terminal E,unless otherwise noted.

(3)Maximum power dissipation is a function of $T_J(\max)$, θ_{JA} ,and T_A .The maximum allowable power dissipation at any allowable ambient temperature is $PD=(T_J(\max)-T_A)/\theta_{JA}$.Operating at the absolute maximum T_J of 150°C can affect reliability.

(4)Maximum power dissipation is a function of $T_J(\max)$, θ_{JC} ,and T_A .The maximum allowable power dissipation at any allowable ambient temperature is $PD=(T_J(\max)-T_A)/\theta_{JC}$.Operating at the absolute maximum T_J of 150°C can affect reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
VCC	Collector to Emitter voltage	-	50	V
TA	Operating Ambient Temperature	-40	+105	°C

Electrical Characteristics($T_A=+25^{\circ}\text{C}$, unless otherwise specified)

Parameter		Test Figure	Test Conditions		ULN2003			Unit
					MIN	TYP	MAX	
$V_{I(on)}$	On-state input voltage	Figure 6	$V_{CE}=2\text{V}$	$I_C=200\text{mA}$	--	--	2.4	V
				$I_C=250\text{mA}$	--	--	2.7	
				$I_C=300\text{mA}$	--	--	3	
$V_{CE(sat)}$	Collector-emitter saturation voltage	Figure 5	$I_I=250\mu\text{A}$,	$I_C=100\text{mA}$	--	0.9	1.1	V
			$I_I=350\mu\text{A}$,	$I_C=200\text{mA}$	--	1	1.3	
			$I_I=500\mu\text{A}$,	$I_C=350\text{mA}$	--	1.2	1.6	
I_{CEX}	Collector cutoff current	Figure 1	$V_{CE}=50\text{V}$,	$I_I=0$	--	--	50	μA
		Figure 2	$V_{CE}=50\text{V}$, $T_A=+105^{\circ}\text{C}$	$I_I=0$	--	--	100	
V_F	Clamp forward voltage	Figure 8	$I_F=350\text{mA}$		--	1.7	2	V
$I_{I(off)}$	Off-state input current	Figure 3	$V_{CE}=50\text{V}, I_C=500\mu\text{A}$		50	65	--	μA
I_I	Input current	Figure 4	$V_I=3.85\text{V}$		--	0.93	1.35	mA
			$V_I=5\text{V}$		--	--	--	
			$V_I=12\text{V}$		--	--	--	
I_R	Clamp reverse current	Figure 7	$V_R=50\text{V}$		--	--	50	μA
				$T_A=70^{\circ}\text{C}$	--	--	100	
C_i	Input capacitance		$V_I=0, f=1\text{MHz}$		--	15	25	pF

Switching Characteristics($T_A=+25^{\circ}\text{C}$, unless otherwise specified)

Parameter		Test Conditions	ULN2003			UNIT
			MIN	TYP	MAX	
T_{PLH}	Propagation delay time, low-to high-level output	See Figure 9		0.25	1	μs
T_{PHL}	Propagation delay time, high-to low-level output	See Figure 9		0.25	1	μs
V_{OH}	High-level output voltage after switching	$V_S=50\text{V}, I_O=300\text{mA}$, See Figure 9	VS-20			mV

Parameter Measurement Information

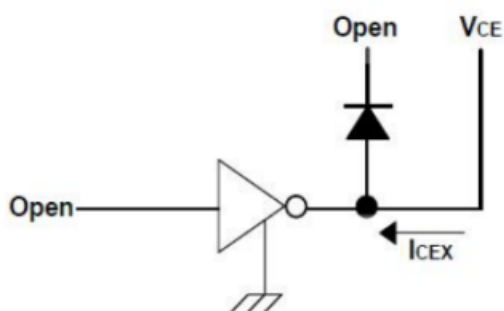


Fig.1 ICEX Test Circuit

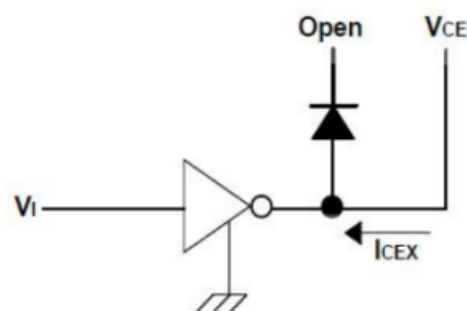


Fig.2 ICEX Test Circuit

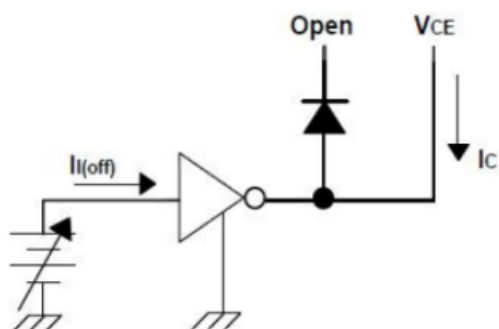


Fig.3 Ii(off) Test Circuit

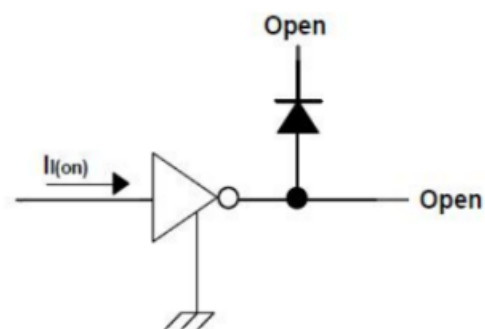


Fig.4 Ii Test Circuit

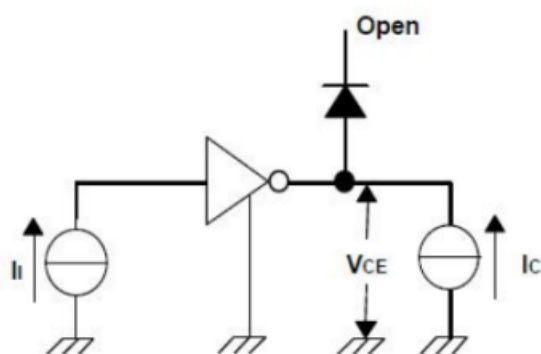


Fig. 5 hFE , VCE(sat) Test Circuit

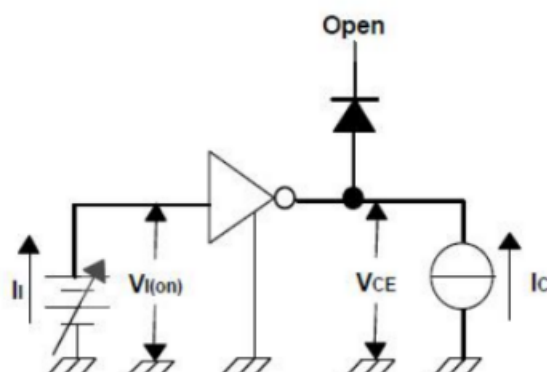


Fig. 6 Vi(on) Test Circuit

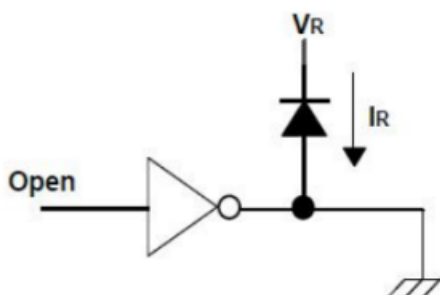


Fig. 7 IR Test Circuit

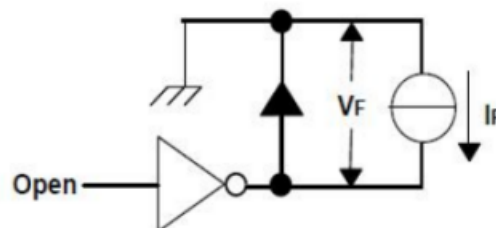


Fig. 8 VF Test Circuit

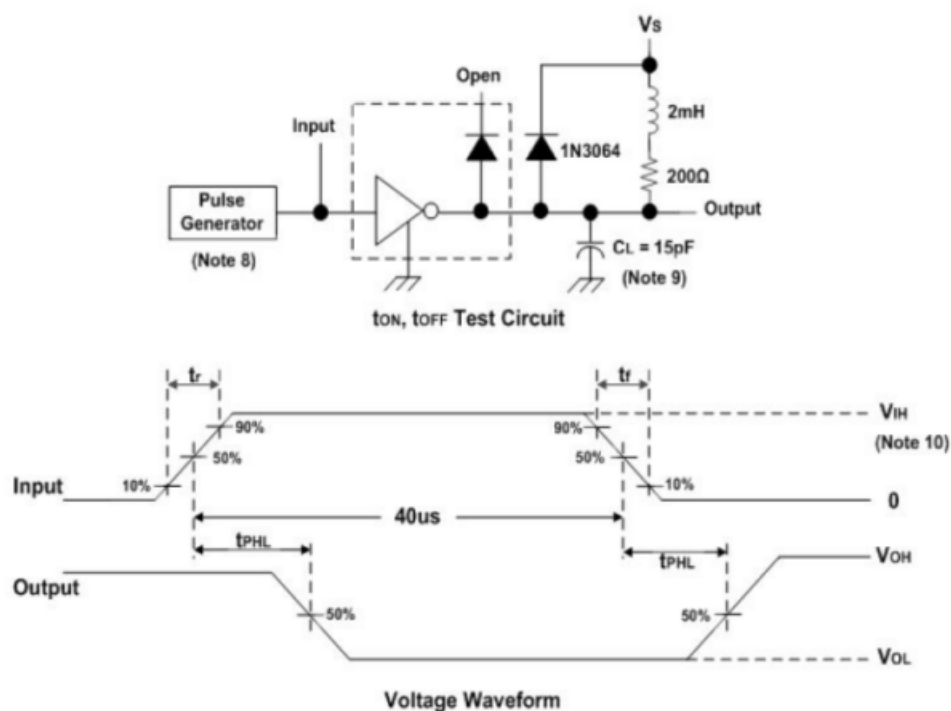


Fig.9 Latch-Up Test Circuit and Voltage Waveform

Notes: 8.The pulse generator has the following characteristics:

Pulse Width=12.5Hz,output impedance 50Ω , $t_r \leq 5ns$, $t_f \leq 10ns$.

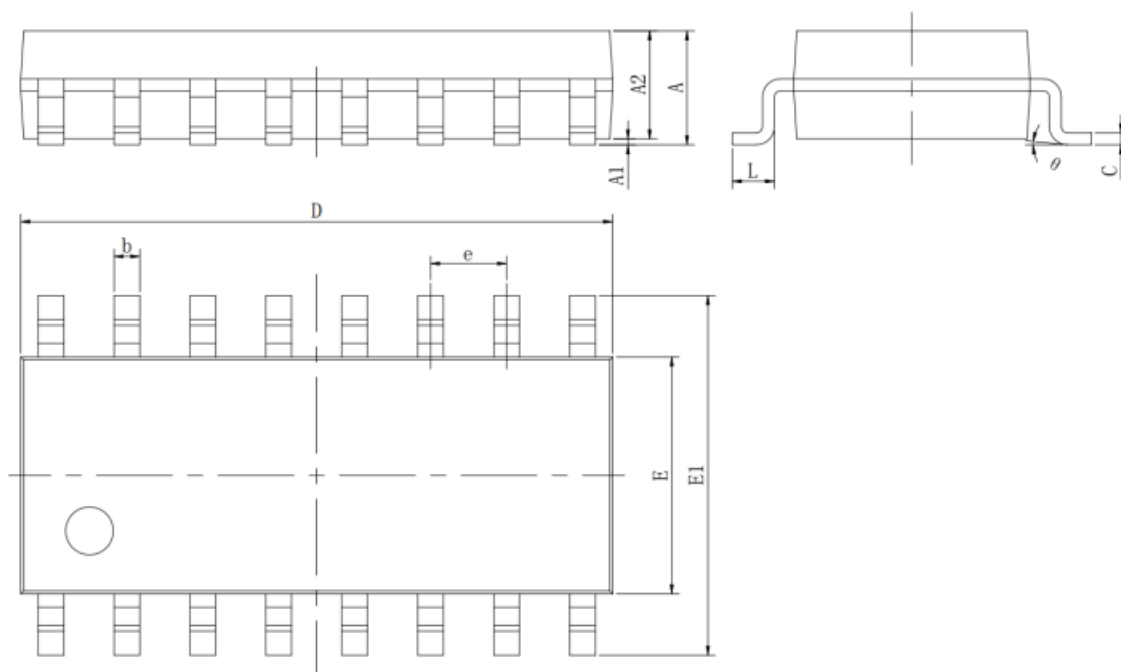
9. C_L includes probe and jig capacitance.

10. $V_{IH}=3V$

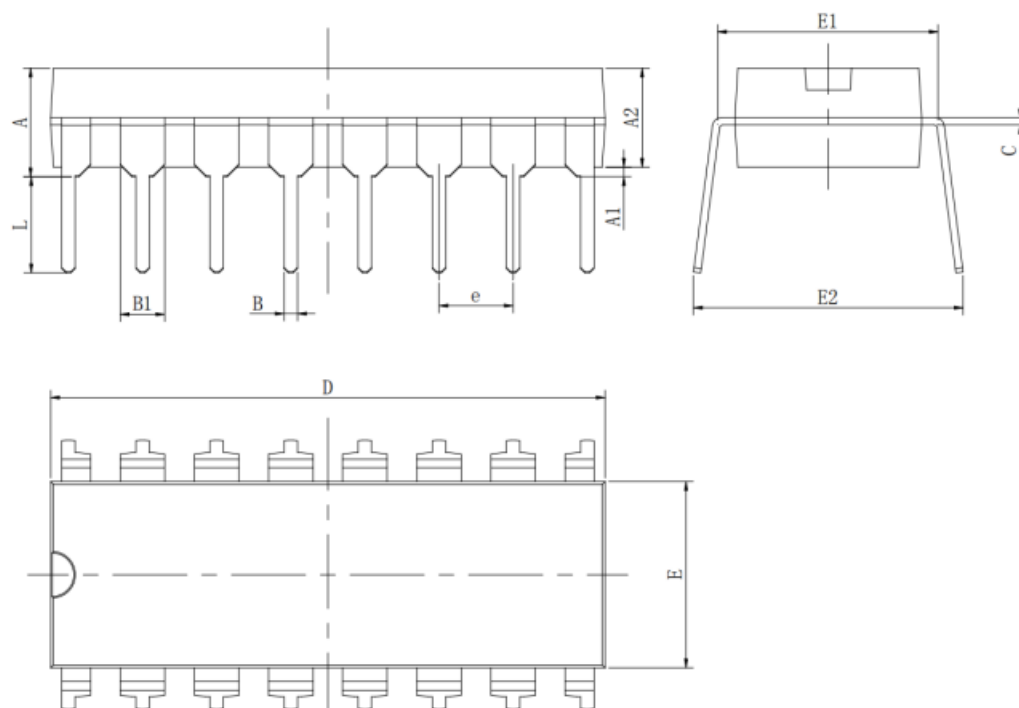
Outline Dimensions

SOP16

Unit:mm



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	9.800	10.200	0.386	0.402
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

DIP-16
Unit:mm


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	3.710	4.310	0.146	0.170
A1	0.510		0.020	
A2	3.200	3.600	0.126	0.142
B	0.380	0.570	0.015	0.022
B1	1.524(BSC)		0.060(BSC)	
C	0.204	0.360	0.008	0.014
D	18.800	19.200	0.740	0.756
E	6.200	6.600	0.244	0.260
E1	7.320	7.920	0.288	0.312
e	2.540(BSC)		0.100(BSC)	
L	3.000	3.600	0.118	0.142
E2	8.400	9.000	0.331	0.354

Important Notice

- Green Micro chip reserves the right to change products and documents without notice. Customers should obtain and verify the completeness of the latest technical information before placing orders. Meanwhile, Green Micro chip shall not assume any responsibility or obligation for non-officially revised documents.
- Any parameters in the entire product specification are for reference only, and actual application testing shall prevail. When customers use the products for system design, they must comply with safety regulations and independently assume the following responsibilities: selecting suitable Green Micro chip products according to application requirements; completing design verification and full-link testing of the application; and ensuring that the application complies with safety regulations or other requirements of the target market. Customers shall bear all personal or property losses caused by design defects or illegal operations, which shall have no relation to Green Micro chip.
- Green Micro chip products are prohibited from being used in scenarios such as life support, military equipment, and key aerospace applications. All accidents and legal liabilities arising from out-of-scope use shall be borne by the user, and Green Micro chip shall not be held responsible.
- All technical resources of Green Micro chip (including data sheets and reference designs) are provided "as is", without guarantee of no defects or universality, and without any express or implied warranties. The documents are only authorized for product development and research described in this document. Unauthorized use of intellectual property, public reproduction, and reverse engineering are strictly prohibited. All claims and losses caused by illegal use shall be borne by the user, and Green Micro chip shall not be liable.