High Voltage Bipolar Latch Hall-effect Switch

Features

- Digital Bipolar latch Hall sensor
- 2.7V to 30V Operating Voltage Range
- -32V Reverse Supply Voltage
- 10μs Ultra-fast Response Time
- 50mA Maximum Continuous Output Current
- -40°C to +150°C Operating Temperature
- Open-drain output
- ESD >10kV（HBM）
- Output Limiting Current Protection
- Good RF Nosie Immunity
- TO92S-3L package

Applications

- Power Tools
- Flow Meters
- Valve and Solenoid Status
- Brushless Dc Motors
- Proximity Sensing
- Tachometers

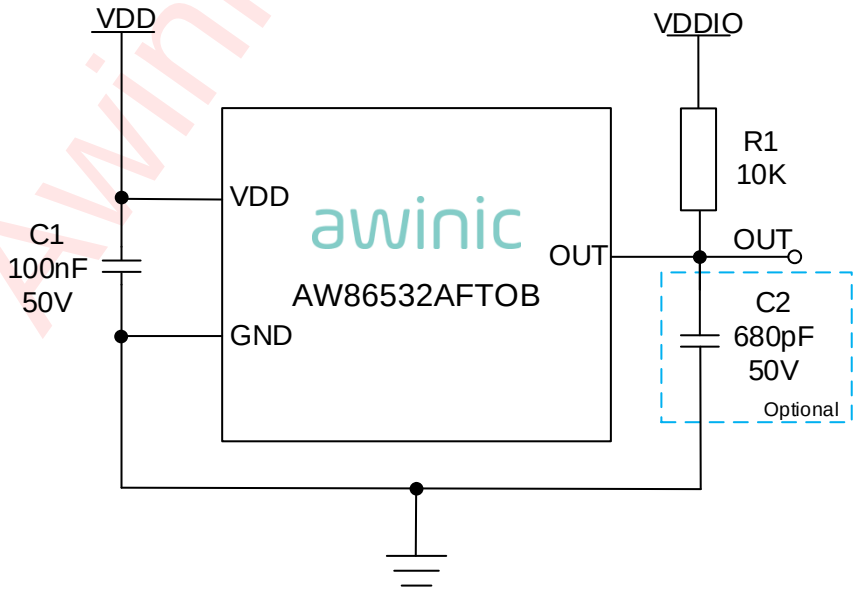
General Description

The AW86532AFTOB is a high - sensitivity and high - voltage Bipolar Latch Hall - effect switch integrated IC. It features an open - drain output and can operate with a power supply voltage ranging from 2.7V to 30V. Internally, it incorporates a voltage regulator, temperature compensation circuitry, and a small - signal amplifier.

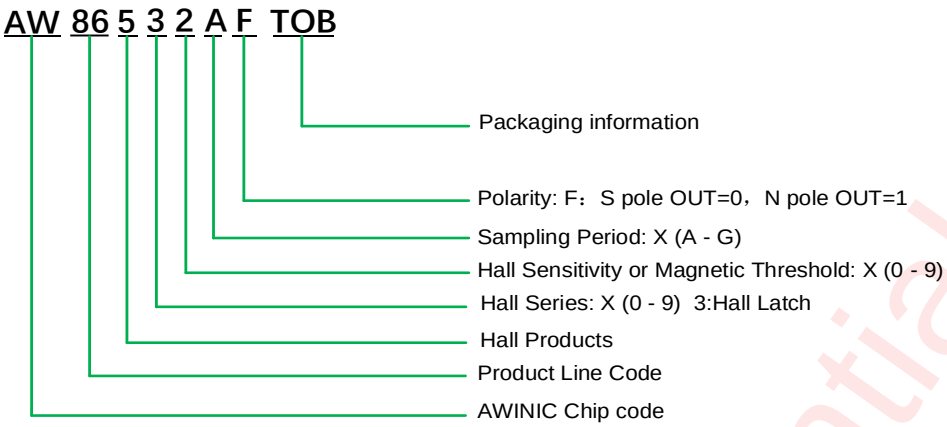
The AW86532AFTOB is designed to react to a bipolar magnetic field. When the magnetic flux density (B) is greater than the operating point (BopS), the output will turn on (low - level). When the magnetic field strength is greater than the release point(BrpN) , then the device output is pulled high; When the magnetic field strength is between BopS and BrpN, then the device output remains in the previous state.

The AW86532AFTOB is packaged in a surface-mount TO92S-3L package and is compliant with RoHS (Restriction of Hazardous Substances) standards.

Typical Application Circuit

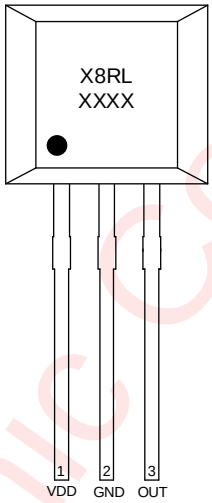


Naming Rule



Pin Configuration And Top Mark

AW86532AFTOB Marking
Top View

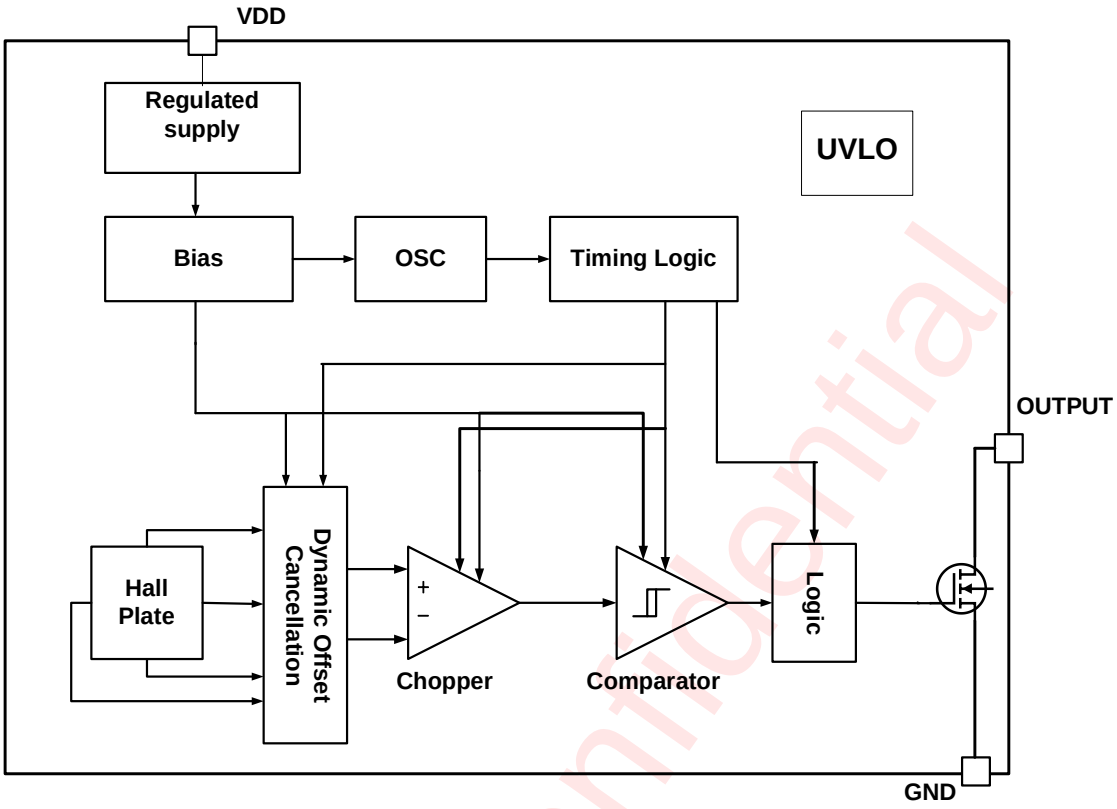


X8RL - AW86532AFTOB
XXXX - Production Tracing Code

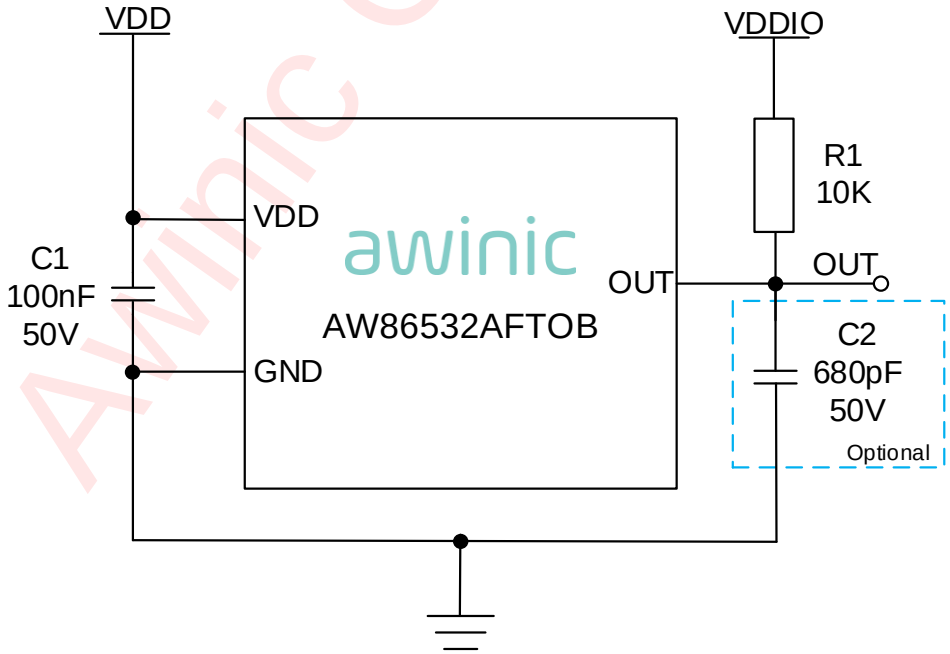
Pin Definition

No.	NAME	DESCRIPTION
1	VDD	Power Supply
2	GND	Ground
3	OUT	Latch open-drain output that responds to bipolar magnetic field The open drain output requires a resistor pullup

Functional Block Diagram



Typical Application Circuits



1：Please place C1 as close to the chip as possible.

2：C2 serves as a reserved EMI filter capacitor and can be added as needed. During the design phase, appropriate values for R1 and C2 should be selected according to the signal frequency characteristics to ensure effective filtering of EMI interference while avoiding excessive attenuation of the useful signal.

Ordering Information

Part Number	Temperature	Package	Marking	Moisture Sensitivity Level	Environmental Information	Delivery Form
AW86532AFTOB	-40℃～150℃	TO92S-3L	X8RL	NA	ROHS+HF	10000 units

Absolute Maximum Ratings(NOTE1)

PARAMETERS	RANGE
Supply Voltage	-32V to 33V
Output Voltage	-0.4V to V_{DD}+0.4V
Output Current	80mA
Operating Ambient Temperature T_A	-40°C to 150°C
Storage Temperature T_{STG}	-65°C to 150°C
Junction temperature T_J	-50°C to 165°C
Magnetic Flux	No limit
Package Power Dissipation	230mW
Lead temperature (soldering 10 seconds)	260°C
ESD Rating(NOTE2 3)	
HBM(Human Body Model)	±10kV
CDM(Charge Device Model)	±1.5kV
Latch-up	
Test Condition: JESD78F.02 - 2023	+IT: 200mA -IT: 200mA

NOTE1: Conditions out of those ranges listed in "absolute maximum ratings" may cause permanent damages to the device. In spite of the limits above, functional operation conditions of the device should within the ranges listed in "recommended operating conditions". Exposure to absolute-maximum-rated conditions for prolonged periods may affect device reliability.

NOTE2:The human body model is a 100pF capacitor discharged through a 1.5kΩ resistor into each pin. Test method: ANSI/ESDA/JEDEC JS-001-2024.

NOTE3:Charge Device Model test method: ANSI/ESDA/JEDEC JS-002-2022.

Electrical Characteristics

V_{DD}=12V, T_A=25°C for typical values (unless otherwise noted)

Parameter		Test Condition	Min	Typ	Max	Unit
INPUT VOLTAGES and CURRENTS						
V_{DD}	Supply Voltage Range		2.7		30	V
V_{CCR}	Reverse Supply Voltage			-32		V
V_{OUT_ON}	Output On Voltage	I_{OUT}=20mA,B > Bops		0.2	0.4	V
I_Q	Quiescent Current	I_{OUT}=0mA	2	2.52	5	mA
$I_{leakage}$	Output Leakage Current	V_{OUT} = 12V, B < Brps, Output off			1	μA
I_{CCR}	Reverse Supply Current	V_{DD} = -32V			6	μA
t_{ON}	Device Power-On Time	V_{DD} =12V	8	10	13	μs
Fc	Chopping Frequency			800		kHz
Fs	Sampling Frequency			200		kHz
t_D	Response Time Delay			3.75		μs
t_R	Output Rising Time	RL = 1kΩ, CL = 20pF		300	1000	ns
t_F	Output Falling Time	RL = 1kΩ, CL = 20pF		30		ns
I_{OUT}	Output Current Limit	V_{DD} =5V, B>Bops, Out pin short V_{DD}	30	50	80	mA

MAGNETIC CHARACTERISTICS(Note)

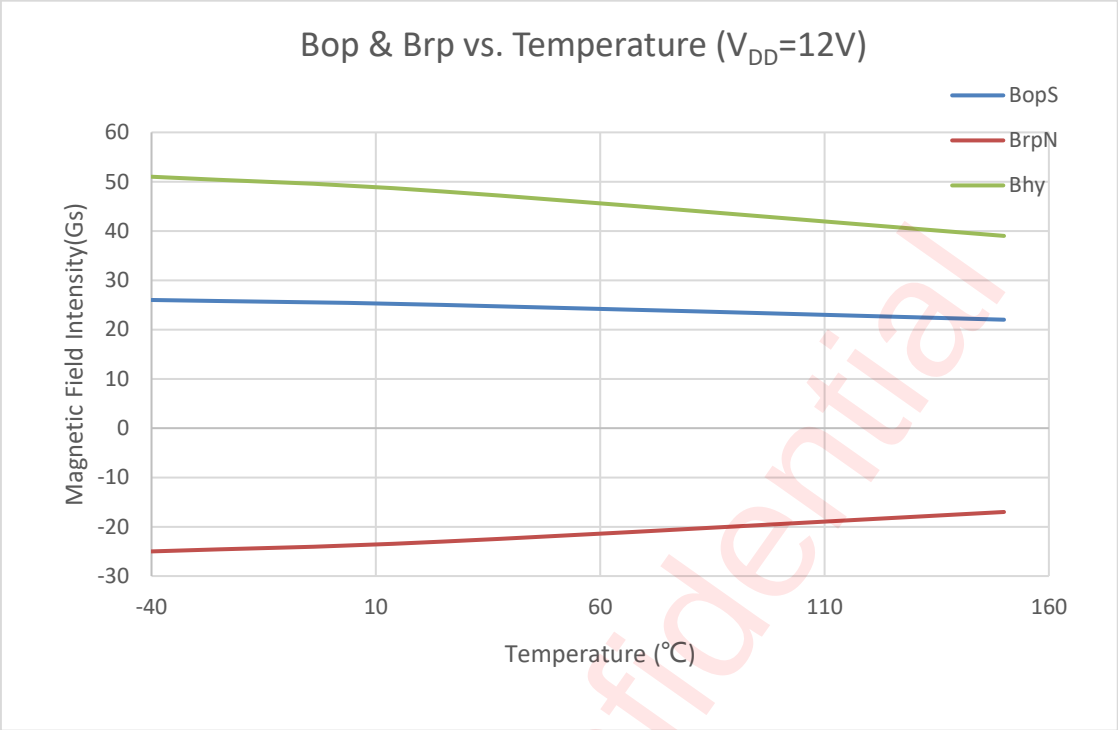
T_A=25ºC, V_{DD}=12V for typical values (unless otherwise noted) (1mT = 10Gauss)

Symbol	Characteristics	Test Condition	Min	Typ	Max	Unit
Bops (south pole to part marking side)	Operation Point	V_{DD} = 12V, TA = 25 ºC	16	25	34	Gauss
Brpn (north pole to part marking side)	Release Point	V_{DD} = 12V, TA = 25 ºC	-34	-25	-16	
Bhy (|Bopx|-|Brpx|)	Hysteresis	V_{DD} = 12V, TA = 25 ºC	-	50	-	

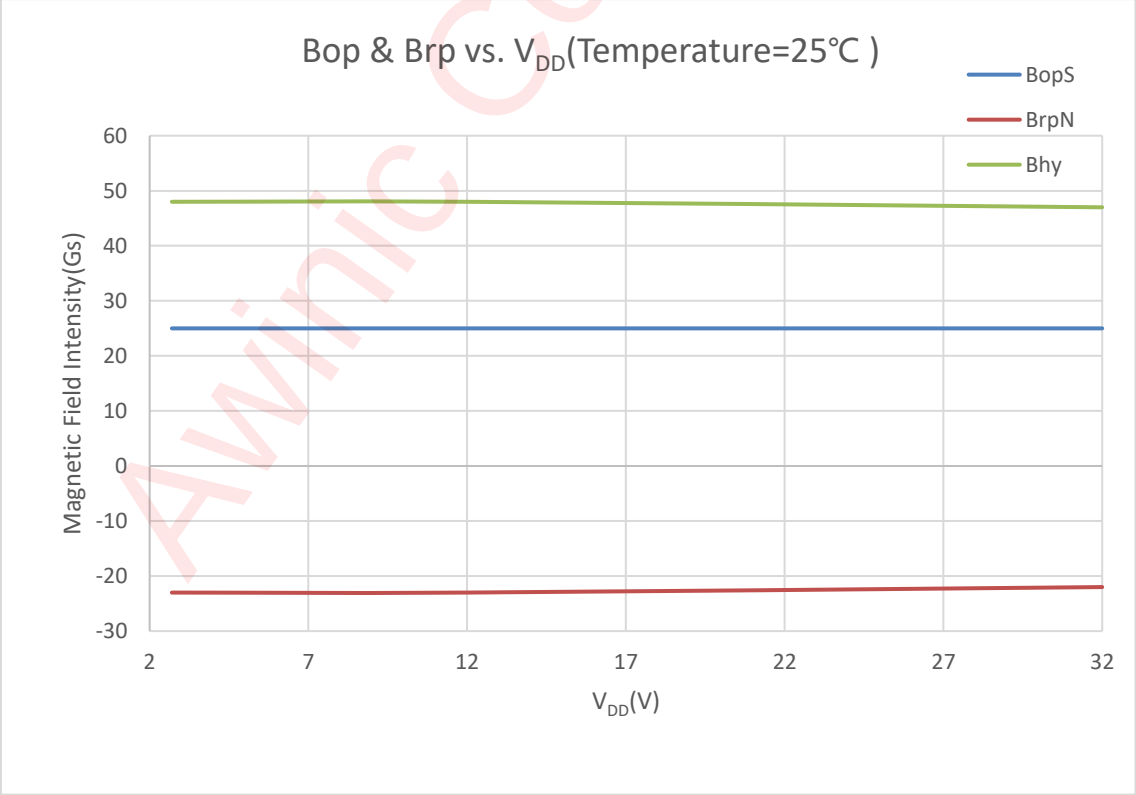
Notes: Typical data is at T_A=25°C, V_{DD}=12V.

Maximum and minimum parameters values over operating temperature range are not tested in production. They are guaranteed by design, characterization and process control. The magnetic characteristics may vary with supply voltage, operating temperature and after soldering.

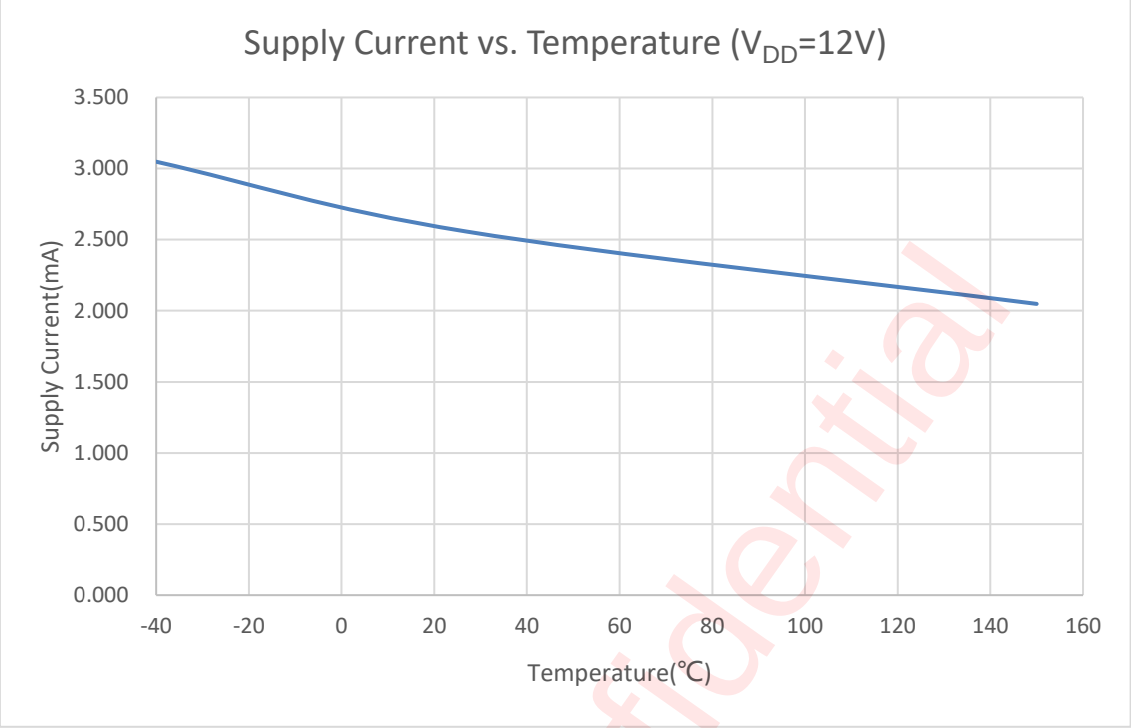
Typical Characteristics



Bop&Brp vs. Ta(V_{DD} =12V)



Bop&Brp vs. V_{DD}(Ta=25℃)

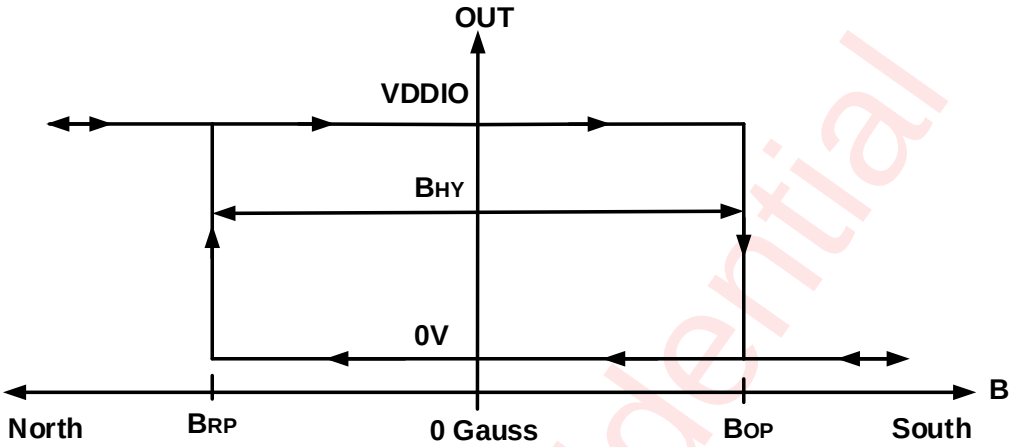


Supply Current vs. Ta(V_{DD}=12V)

Detailed Functional Description

Magnetic Response

When the magnetic flux density perpendicular to the package is larger than operate point(BopS) , the output will be turned on (pulled low) and held until B is larger than release point (BrpN). When the magnetic field strength is between BopS and BrpN, then the device output remains in the previous state.



Magnetic Flux Direction

As depicted in Figure 1, the AW86532AFTOB is responsive to the magnetic field component which is perpendicular to the top surface of the package.

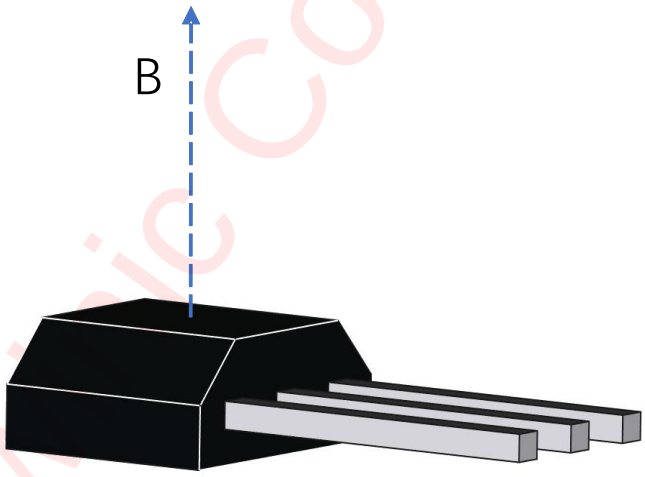


Figure 1 Direction of Sensitivity

In this document, magnetic flux moving from the bottom to the top of the package is defined as positive. The chip detects the S-pole magnetic field when a south magnetic pole is positioned near the top (marked-side) of the package or when a north magnetic pole is close to the backside of the package as shown in Figure 2.

Figure 2 S-pole magnetic field detection

The chip detects the N-pole magnetic field when a north magnetic pole is positioned near the top (marked-side) of the package or when a south magnetic pole is close to the backside of the package as shown in Figure 3

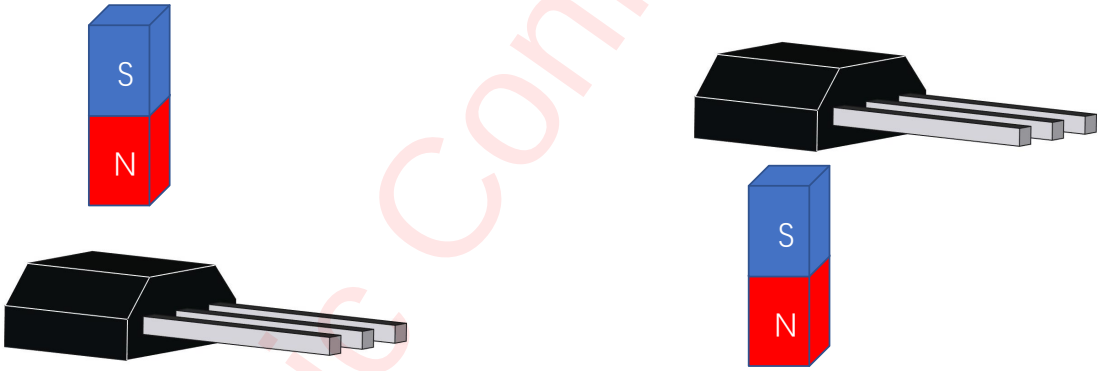
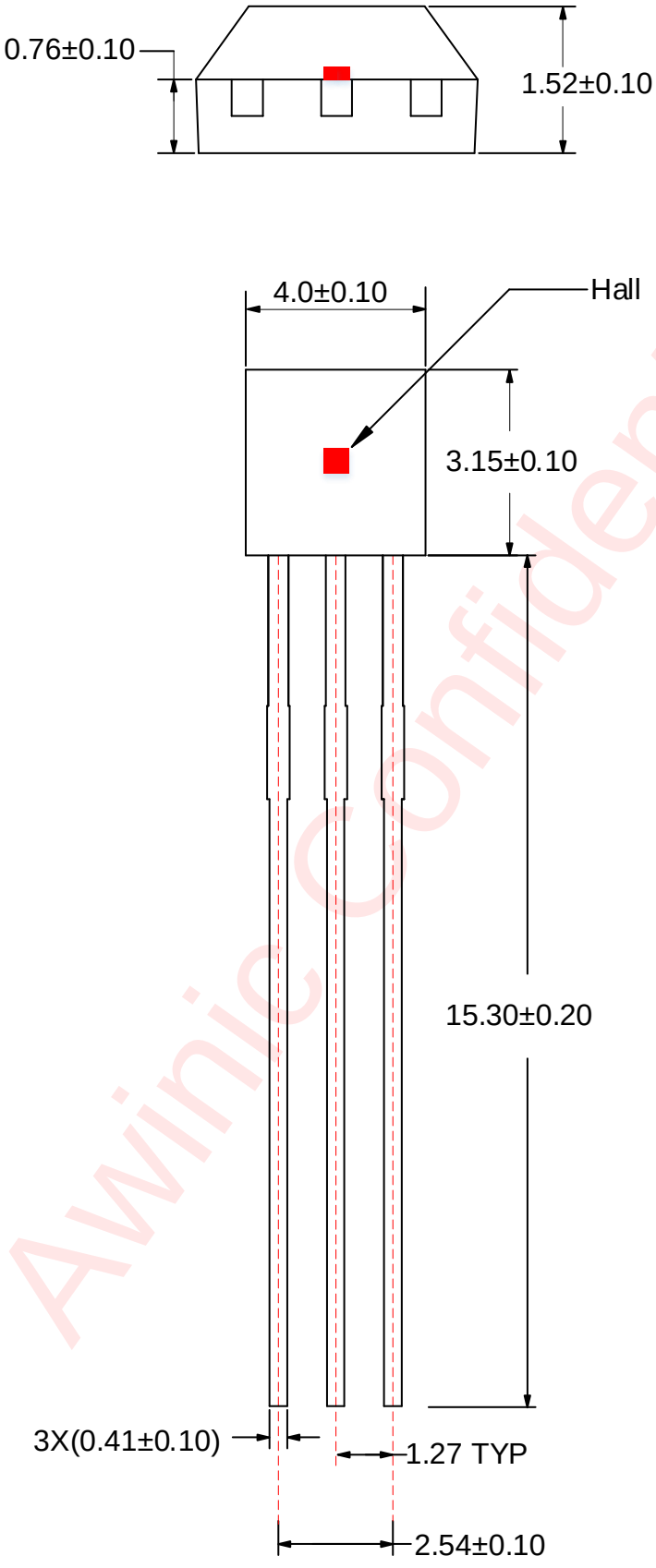


Figure 3 N-pole magnetic field detection

Package Description



Unit: mm

Revision History

Version	Date	Change Record
V1.0	Sep. 2025	Officially released

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