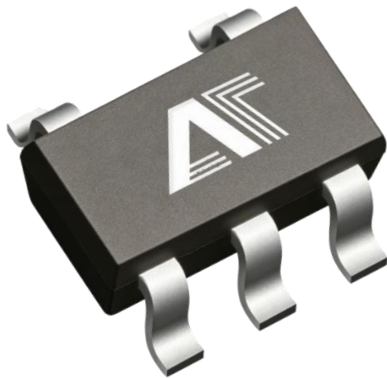




**Single channel bus
buffer gate**

SN74LVC1G125

Product Data Sheet

**AOTE DCC
RELEASE**

◆ Summary :

The SN74LVC1G125 device is a single channel bus buffer gate with a three state output, which can operate within a supply voltage range of 1.65V to 5.5V. When the output enable pin ($\overline{OE}$) input is at a high level, the output will be turned off. When the device is powered off, the loff circuit will disable the output to prevent backflow current from damaging the device. To ensure a high impedance state during power on or off, $\overline{OE}$ should be connected to VCC through a pull-up resistor, with the minimum value of the resistor determined by the current absorption capacity of the driver

The device is packaged in SOT23-5 and SC70-5. The working temperature range is from $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$

◆ Product features

- Small Package: SOT23-5、 SC70-5
- Working voltage range: 2.0V~5.5V
- ow power consumption: 0.1 μA (typical value)
- Working temperature range: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$
- Input voltage up to 5.5V
- High Output driver: When powered by 3.0V, output driver $\pm 24\text{mA}$
- The locking performance exceeds 100mA;

◆ Applications

- Communication receiver
- Cable modem termination system
- digital photo frame (DPF)
- High speed data acquisition and generation
- Motor control: high voltage
- Personal Navigation Device (GPS)
- Portable media player
- Video Communication System

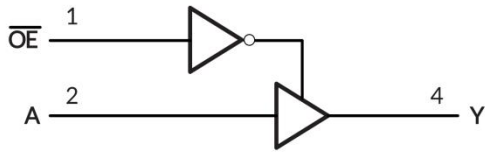
◆ Packaging and Order Description

Product Name	Order Model	Temperature Range ($^{\circ}\text{C}$)	boxed type	screen printing	MSL(3)	Packing Specification
SN74LVC1G125	SN74LVC1G125DCKR	$-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$	SC70-5	CM5	MSL3	Tape and Reel, 3000
	SN74LVC1G125DBVR	$-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$	SOT23-5	C255	MSL3	Tape and Reel, 3000

Attention:

- (1) This information is the latest data for the current version. If these data are updated, they will be promptly updated on our official website without further notice.
- (2) Silk screen printing may have additional codes for internal control traceability of products (including data codes and supplier codes) or marking the place of origin.
- (3) MSL, Humidity sensitivity level classified according to JEDEC industry standards.
- (4) equivalent SOT353

◆ **Logic diagram**



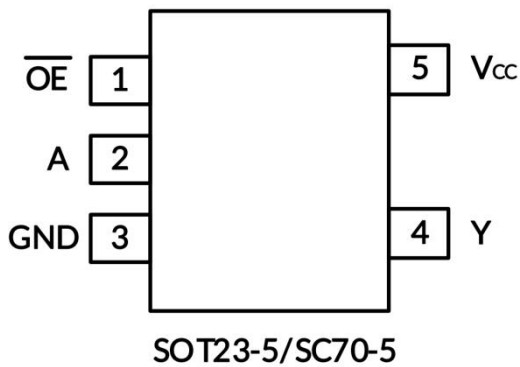
◆ **Menu**

Input		Output
$\overline{\text{OE}}$	A	Y
L	H	H
L	L	L
H	X	Z

H=high level L=low level X=irrelevant Z=high impedance off state

◆ **On-product information:**

• **Footage image**



Pin	Pin Name	I/O	Function Description
1	$\overline{\text{OE}}$	I	$\overline{\text{OE}}$ Enable input
2	A	I	A input
3	GND	-	grounding
4	Y	O	Y output
5	Vcc	-	power supply

◆ **Absolute maximum rating** Within the natural ventilation temperature range (unless otherwise specified)

	Parameter	Min	Max	Unit	
VCC	Power supply voltage range	-0.5	6.5	V	
VI	Input voltage range	-0.5	6.5	V	
Vo	The voltage range applicable to any output under high impedance or power-off conditions	-0.5	6.5	V	
Vo	The voltage range applicable to any output in either high or low level state	-0.5	VCC+0.5	V	
IIK	Input clamp current VI <0	-	-50	mA	
IOK	Output clamp current VO <0	-	-50	mA	
IO	Continuous output current	-	± 50	mA	
	Continuous current through VCC or GND	-	± 100	mA	
• current parameters					
θ JA	Thermal resistance to the environment	SOT23-5	-	230	°C/W
		SC70-5	-	380	
• current parameters					
temperature parameter	Junction temperature, TJ	-65	150	°C	
	Storage temperature range, Tstg	-65	150		

◆ **ESD level**

The following ESD information is only applicable to sensitive equipment operated within an ti-static protection zones

–	nominal value	Unit	 ESD sensitivity warning The scope of ESD damage can range from minor performance degradation to complete equipment failure. Precision integrated circuits may be more susceptible to damage as very small parameter changes can result in devices not meeting their published parameter specifications.
human model (HBM)	±4000	V	
Charged Device Model (CDM)	±1000		
Mechanical Model (MM)	±500		

Attention:

- (1) This only represents the limit values obtained by the product under testing conditions, and does not mean that the product can work normally under these conditions or other parameter conditions beyond the specifications. Exceeding the range specified by the absolute maximum rated value above will cause damage to the product, and it is impossible to predict the working state of the product outside of the above conditions. If the product operates under conditions other than those mentioned above for a long time, it may affect its performance.
- (2) If the input and output current ratings are followed, it may exceed the input and output negative voltage ratings.
- (3) The value of VCC is provided in the recommended working condition table.
- (4) The thermal impedance of the package is calculated according to JEDEC-51 standard:
- (5) The maximum power consumption is a function of T_{J (MAX)}, RθJA, and T_A. The maximum power consumption at any ambient temperature is Po=(T_{J (MAX)} - T_A)/RθJA, which is suitable for packaging directly soldered to PCB.

◆ Electrical Characteristics

(Within the natural ventilation temperature range (typical test conditions are: TA=+25 ° C, full temperature=-40 ° C~125 ° C)

◆ Recommended working conditions

Parameter	symbol	Test conditions	Min	Max	Unit
power supply voltage	V _{CC}	work	1.65	5.5	V
		Only retain data	1.5	5.5	
High level input voltage	V _{IH}	V _{CC} = 1.65V - 1.95V	0.65 x V _{CC}	-	V
		V _{CC} =2.3V - 2.7V	1.7		
		V _{CC} =3V - 3.6V	2.2		
		V _{CC} =4.5V - 5.5V	0.7 x V _{CC}		
Low level input voltage	V _{IL}	V _{CC} = 1.65V - 1.95V	-	0.3 x V _{CC}	V
		V _{CC} =2.3V - 2.7V		0.7	
		V _{CC} =3V - 3.6V		0.8	
		V _{CC} =4.5V - 5.5V		0.3 x V _{CC}	
input voltage	V _I	-	0	5.5	V
output voltage	V _O	-	0	V _{CC}	V
Input conversion up or down rate	t _r , t _f	V _{CC} = 1.8±0.15V, 2.5V±0.2V	-	20	ns/V
		V _{CC} =3.3V ±0.3V		10	
		V _{CC} =5V ±0.5V		5	
Working temperature range under natural ventilation conditions	T _A	-	-40	+125	°C

◆ Typical electrical parameters

Parameter	Test conditions	Vcc	temperature	Min	Typ	Max	Unit
V _{OH}	IOH = -100μA	1.65V-5.5V	Full temperature	VCC-0.1	-	-	V
	IOH = -4mA	1.65V		1.2			
	IOH = -8mA	2.3V		1.9			
	IOH = -16mA	3V		2.4			
	IOH = -24mA			2.3			
	IOH = -32mA	4.5V		3.8			
V _{OL}	IOH = 100μA	1.65V-5.5V	Full temperature	-	-	0.1	V
	IOH = 4mA	1.65V		-	-	0.45	
	IOH = 8mA	2.3V		-	-	0.3	
	IOH = 16mA	3V		-	-	0.4	
	IOH = 24mA			-	-	0.55	
	IOH = 32mA	4.5V		-	-	0.55	
I _l A or $\overline{OE}$ input	V _I =5.5V orGND	0V - 5.5V	+25°C Full temperature	-	±0.1	±5	μA
I _{off}	V _I or V _O =5.5V	0V	+25°C Full temperature	-	±0.1	±10	μA
I _{oz}	V _O = 0V-5.5V	3.6V	Full temperature	-	-	10	-
I _{cc}	V _I =5.5V or GND, I _O =0	1.65V - 5.5V	+25°C Full temperature	-	0.1	10	μA
Δ _{ICC}	One input at V _{cc} -0.6V, Other inputs at VCC or GND	3V - 5.5V	Full temperature	-	-	500	μA

◆ **Switch characteristics (CL=30pF/ 50pF)**

TA=25°C

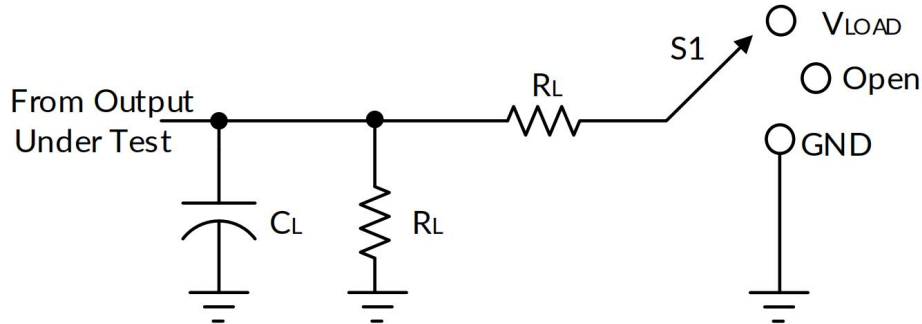
Parameter	input	output	V _{CC} =1.8V±0.15V Typ	V _{CC} =2.5V±0.2V Typ	V _{CC} =3.3V±0.3V Typ	V _{CC} =5V±0.5V Typ	Unit
t _{pd}	A	Y	12	7	6	4.3	ns
t _{en}	$\overline{\text{OE}}$	Y	14.9	7.8	6.5	4.8	ns
t _{dis}	$\overline{\text{OE}}$	Y	11.4	7.4	5.7	5.0	ns

◆ **Switch characteristics**

TA=25°C

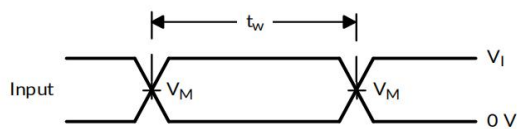
Parameter			Test conditions	V _{CC} =1.8V Typ	V _{CC} =2.5V Typ	V _{CC} =3.3V Typ	V _{CC} =5V Typ	Unit
C _{pd}	Powerconsump tion capacitor	Output Enable	f=10MHz	18	18	19	21	pF
		Output disabled		2	2	2	4	

◆ **Parameter measurement information**
open-drain output

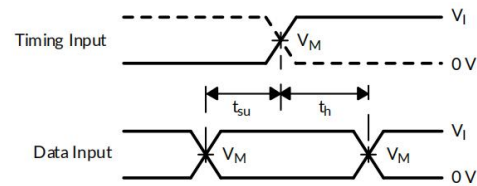


TEST	S1
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND

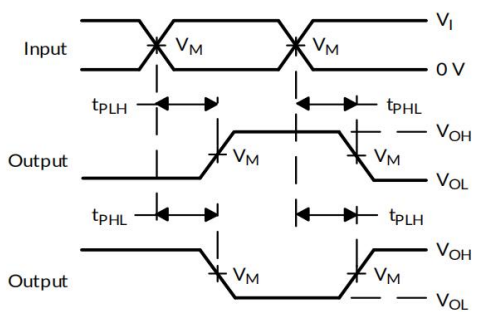
VCC	INPUTS		VM	VLOAD	CL	RL	VΔ
	VI	tr/tf					
1.8V±0.15V	VCC	≤2ns	VCC/2	2 x VCC	30pF	1KΩ	0.15V
2.5V±0.2V	VCC	≤2ns	VCC/2	2 x VCC	30pF	500Ω	0.15V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	VCC	≤2.5ns	VCC/2	2 x VCC	50pF	500Ω	0.3V



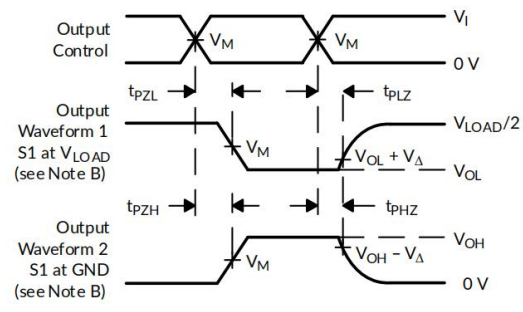
VOLTAGE WAVEFORMS
PULSE DURATION



VOLTAGE WAVEFORMS
SETUP AND HOLD TIMES



VOLTAGE WAVEFORMS PROPAGATION DELAY
TIMES INVERSION AND NONINVERTING OUTPUTS



VOLTAGE WAVEFORMS ENABLE AND DISABLE TIMES
LOW-AND HIGH-LEVEL ENABLING

Note: A.CL includes probe and fixture capacitors.

B. Waveform 1 is used for outputs with internal conditions, where the output is low unless disabled by the output controller. Waveform 2 is used for outputs with internal conditions, where the output is high unless disabled by the output controller.

C. All input pulses are provided by a generator with the following characteristics: PRR≤10MHz, Zo=50Ω

D. Output one measurement at a time, with a transition for each measurement.

E. t_{PLZ} and t_{PHZ} are the same as t_{dis} .

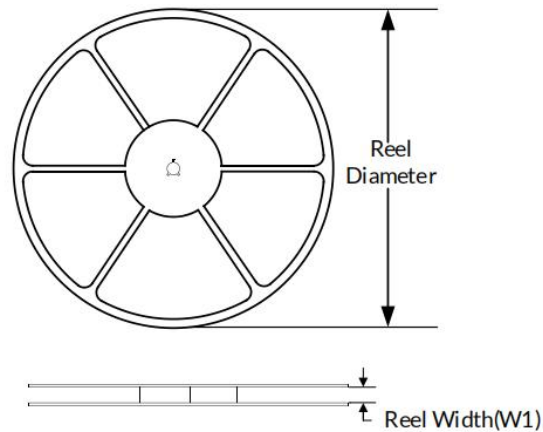
F. t_{PZL} and t_{PZH} are the same as t_{en} .

G. t_{PLH} and t_{PHL} are the same as t_{pd} .

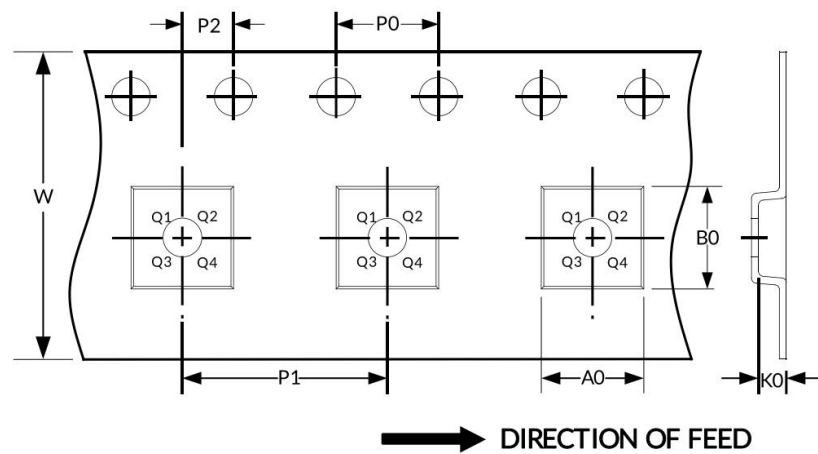
H. Not all parameters and waveforms are applicable to all devices.

◆ Packaging specifications size

REEL DIMENSIONS



TAPE DIMENSION



Attention: The pictures are for reference only. Please use the physical object as the standard

◆ Key parameter table

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SC70-5	7"	9.5	2.25	2.55	1.20	4.0	4.0	2.0	8.0	Q3
SOT23-5	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3

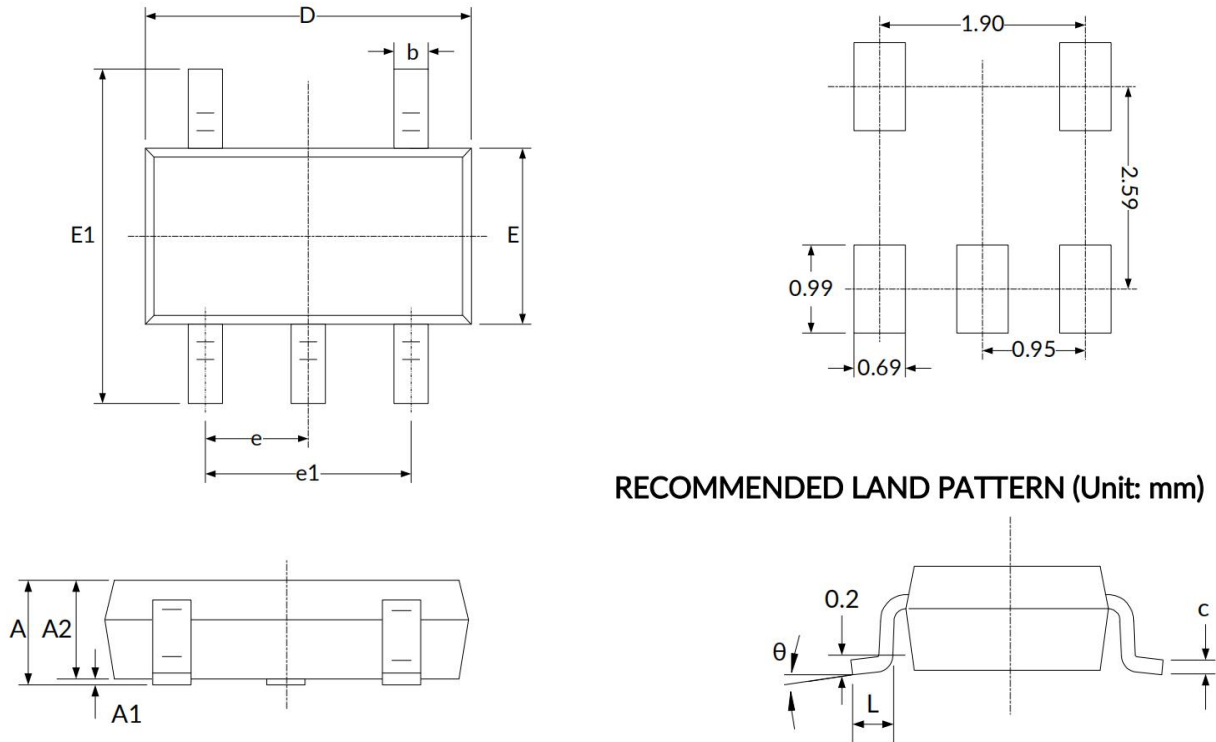
Attention: The pictures are for reference only. Please use the physical object as the standard

◆ Packaging specifications and dimensions

Attention:

- 1、Excluding plastic wrap or metal protrusions with a maximum of 0.15mm on each side.
- 2、BSC (Basic Center Spacing), where the "basic" spacing is the nominal spacing.
- 3、This image is subject to change without prior notice

SOT23-5



RECOMMENDED LAND PATTERN (Unit: mm)

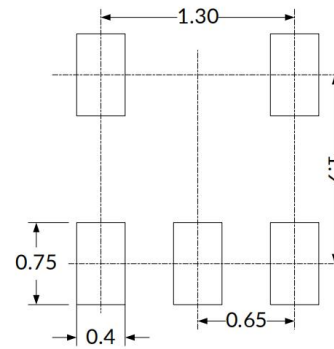
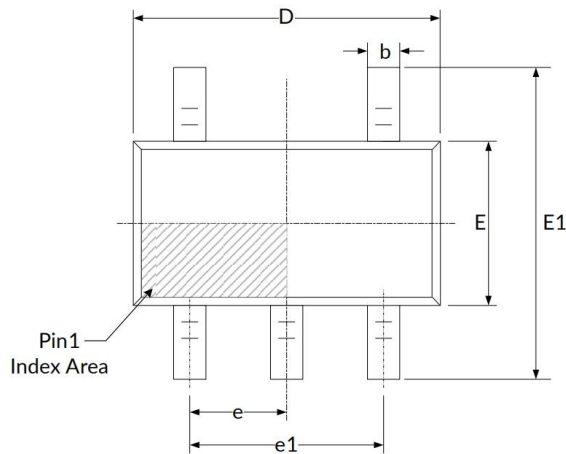
symbol	Dimensions (in millimeters)		Dimensions (in inches)	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
C	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 (BSC)		0.037 (BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

◆ Packaging specifications and dimensions

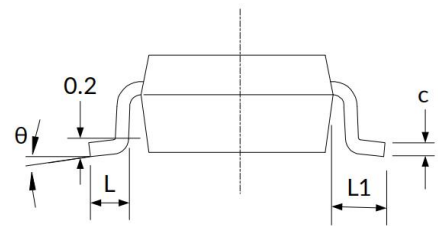
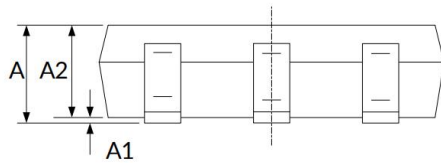
Attention:

- 1、Excluding plastic wrap or metal protrusions with a maximum of 0.15mm on each side.
- 2、BSC (Basic Center Spacing), where the "basic" spacing is the nominal spacing.
- 3、This image is subject to change without prior notice

SC70-5



RECOMMENDED LAND PATTERN (Unit: mm)



symbol	Dimensions (in millimeters)		Dimensions (in inches)	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 (BSC)		0.026 (BSC)	
e1	1.300 (BSC)		0.051 (BSC)	
L	0.260	0.460	0.010	0.018
L1	0.525		0.021	
θ	0°	8°	0°	8°

◆ Attention

- AOTE implements dynamic technical updates. Specifications are subject to change. Refer to the official website for the latest version.
- Users must strictly adhere to specified conditions. Failures caused by misuse (overload, high temperature, incompatible circuits) are excluded from warranty.
- Contact technical support for customized validation in critical applications (medical devices, industrial control).
- This document is valid until December 31, 2026 Updates will be notified on the official website.
- For further clarification on technical specifications or application solutions, please contact us through official channels: