

FEATURES

Two DTA114Y chips in a package

Mounting possible with SOT-563 automatic mounting machines

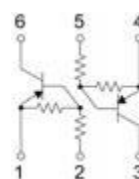
Transistor elements are independent,eliminating interference

Mouting cost and area be cut in half

SOT-563



Marking: B9



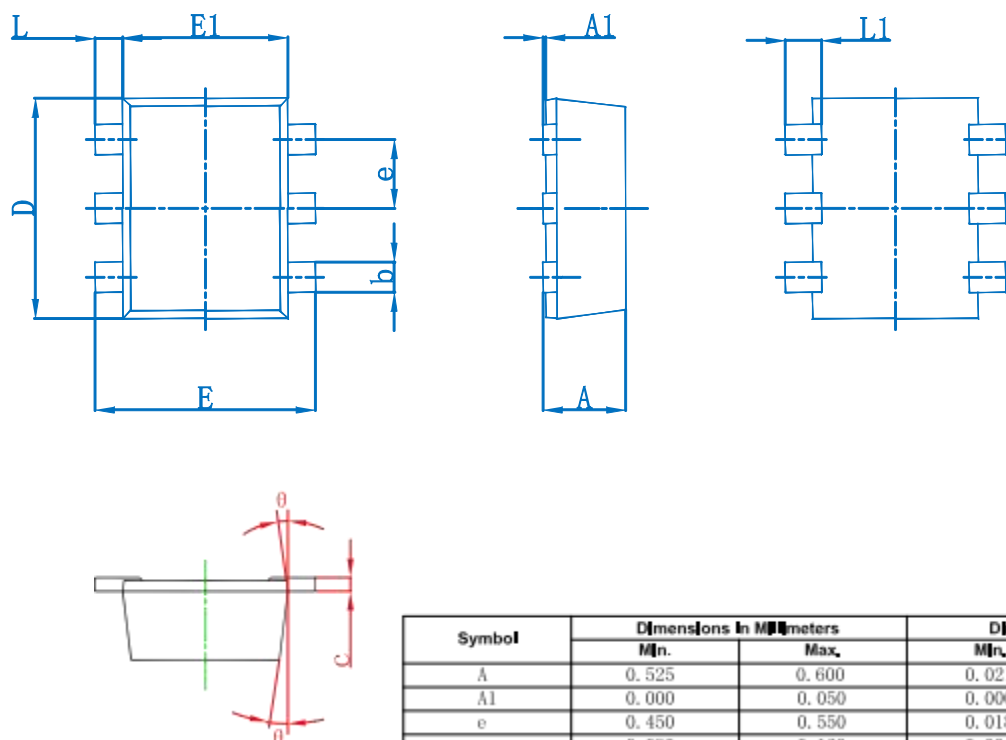
Absolute maximum ratings ($T_a=25^{\circ}\text{C}$)

Symbol	Parameter	Value	Units
V_{CC}	Supply Voltage	-50	V
$I_{C(MAX)}$	Output Current	-100	mA
V_i	Input Voltage	-40 to +6	V
P_D	Power Dissipation	150	mW
T_J, T_{stg}	Operation Junction and Storage Temperature Range	-55~+150	$^{\circ}\text{C}$

Electrical Characteristics ($T_a=25^{\circ}\text{C}$)

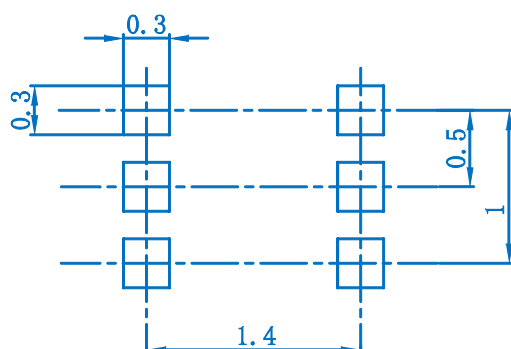
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Input turn-on voltage	$V_{i(on)}$	$V_O=-0.3\text{V}, I_O=-1\text{mA}$			-1.4	V
Input cut-off voltage	$V_{i(off)}$	$V_{CC}=-5\text{V}, I_O=-100\mu\text{A}$	-0.3			V
Output voltage	$V_{O(on)}$	$I_O=-5\text{mA}, I_i=-0.25\text{mA}$			-0.3	V
Input cut-off current	I_i	$V_i=-5\text{V}$			-0.88	mA
Output cut-off current	$I_{O(off)}$	$V_{CC}=-50\text{V}, V_i=0$			-0.5	μA
DC current gain	G_i	$V_O=-5\text{V}, I_O=-5\text{mA}$	68			
Transition frequency	f_T	$V_O=-10\text{V}, I_O=5\text{mA}, f=100\text{MHz}$		250		MHz
Input resistance	R_i		7		13	K Ω
Resistance ratio	R_2/R_1		3.7		5.7	

SOT-563 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min.	Max.	Min.	Max.
A	0.525	0.600	0.021	0.024
A1	0.000	0.050	0.000	0.002
e	0.450	0.550	0.018	0.022
c	0.090	0.160	0.004	0.006
D	1.500	1.700	0.059	0.067
b	0.170	0.270	0.007	0.011
E1	1.100	1.300	0.043	0.051
E	1.500	1.700	0.059	0.067
L	0.100	0.300	0.004	0.012
L1	0.200	0.400	0.008	0.016
θ	10°REF.		10°REF.	

SOT-563 Suggested Pad Layout



- Note:
1. Controlling dimension: in millimeters.
 2. General tolerance: ± 0.05 mm.
 3. The pad layout is for reference purposes only.