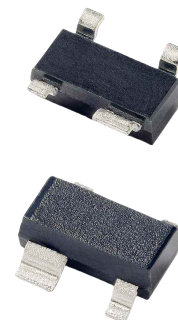


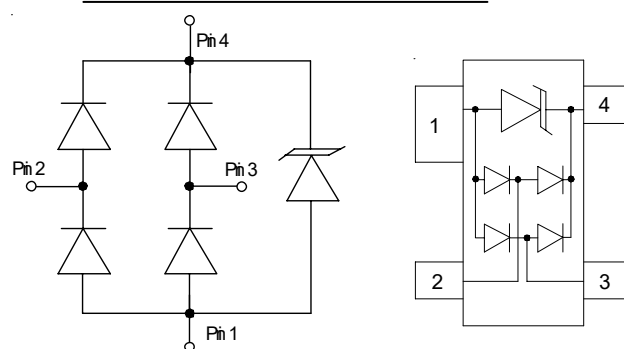
Features

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- Ultra low capacitance: 1.0pF typical
- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- 4-pin SOT-143 package
- Protects two data lines and one power line
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
Air discharge: $\pm 25\text{kV}$
Contact discharge: $\pm 20\text{kV}$
 - IEC61000-4-5 (Lightning) 8A (8/20 μs)
- RoHS Compliant



SOT-143



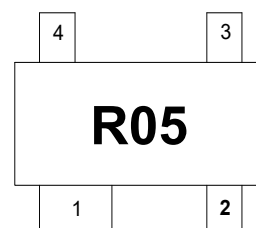
Circuit diagram

Mechanical Characteristics

- Package: SOT-143
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound.
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

- Cellular Handsets and Accessories
- Notebooks and Handhelds
- Personal Digital Assistants
- Portable Instrumentation
- Digital Cameras
- Peripherals
- Audio Players, Keypads, Side Keys, LCD
- USB 2.0



R05= Device Marking Code

Marking (Top View)

Ordering Information

Part Number	Packaging	Reel Size
ESD05V14T-LC	3000/Tape & Reel	7 inch

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

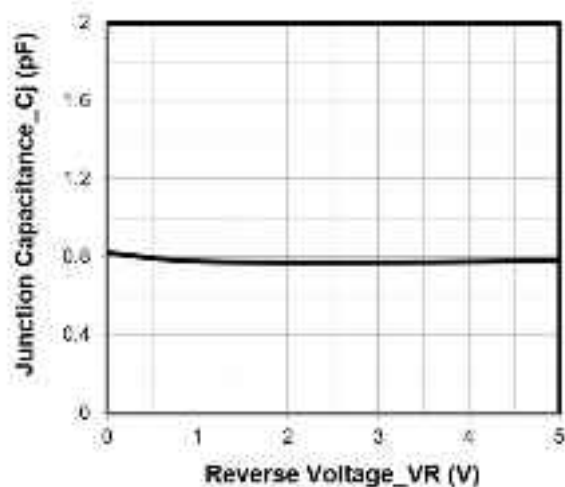
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	P _{pk}	100	W
Peak Pulse Current (8/20μs)	I _{PP}	8	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	±25 ±20	kV
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

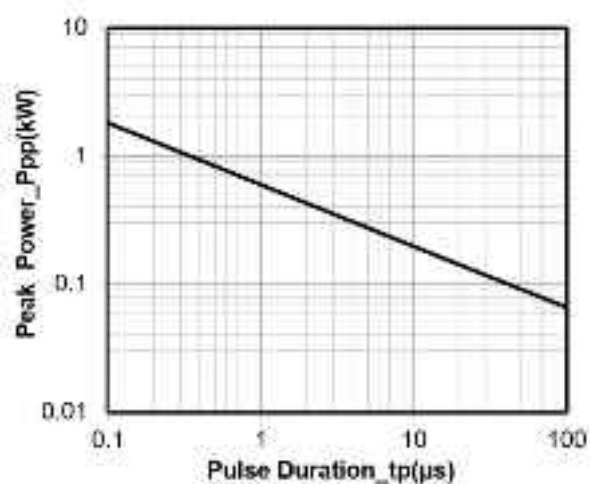
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}			5	V	Any I/O pin to ground
Breakdown Voltage	V _{BR}	6			V	I _T = 1mA, any I/O pin to ground
Reverse Leakage Current	I _R			0.2	μA	V _{RWM} = 5V, any I/O pin to ground
Forward Voltage	V _F		0.95	1.2	V	I _F = 15mA, ground to I/O
Clamping Voltage	V _C		9	12	V	I _{PP} = 1A (8 x 20μs pulse), any I/O pin to ground
Clamping Voltage	V _C		13	15	V	I _{PP} = 8A (8 x 20μs pulse), any I/O pin to ground
Clamping Voltage	V _C		15	18	V	I _{PP} = 15A (8 x 20μs pulse), any V _{CC} pin 4 to ground
Clamping Voltage	C _J		0.45	0.50	pF	V _R = 0V, f = 1MHz, any I/O pin to I/O pin
Junction Capacitance	C _J		1.0		pF	V _R = 0V, f = 1MHz, any I/O pin to ground

Note 1: I/O pins are Pin 2 and 3

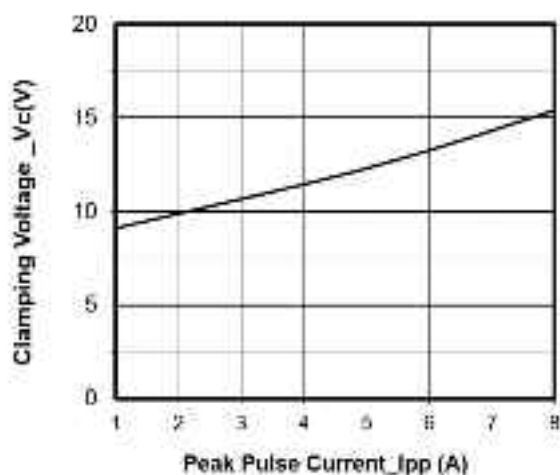
Typical Performance Characteristics (TA=25°C unless otherwise Specified)



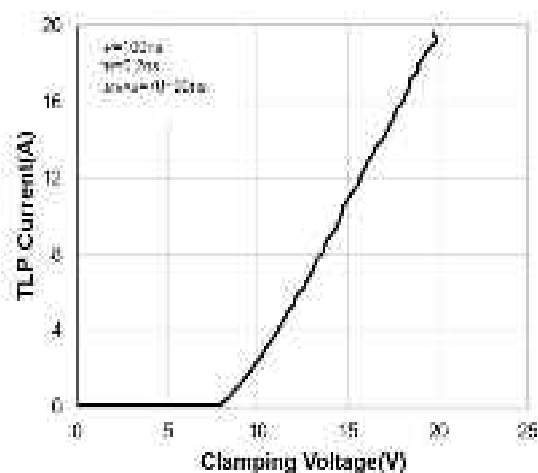
Junction Capacitance vs. Reverse Voltage



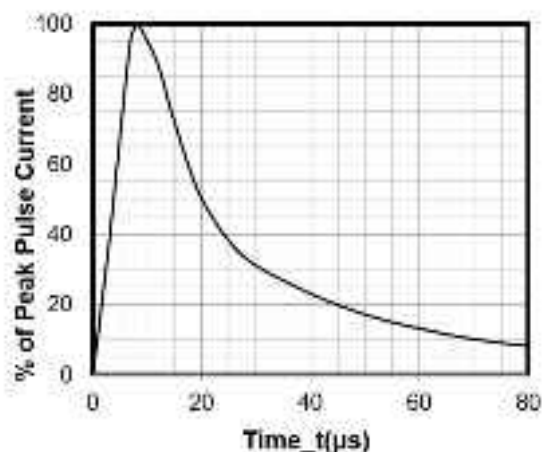
Peak Pulse Power vs. Pulse Time



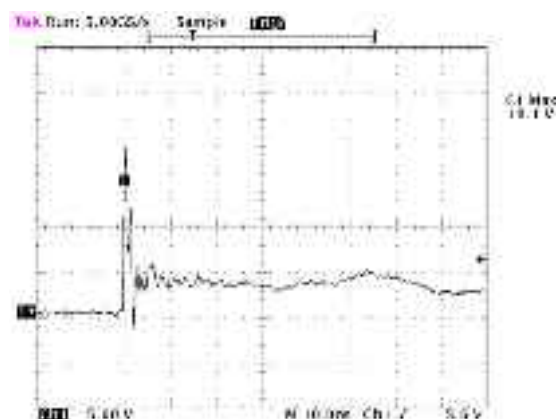
Clamping Voltage vs. Peak Pulse Current



TLP Curve



8 X 20 μ s Pulse Waveform

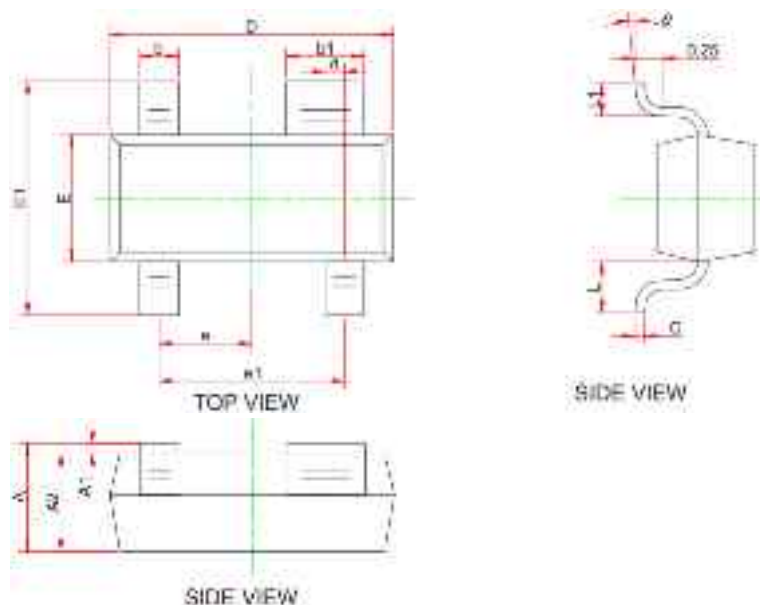


Note: Data is taken with a 10x attenuator

ESD Clamping Voltage

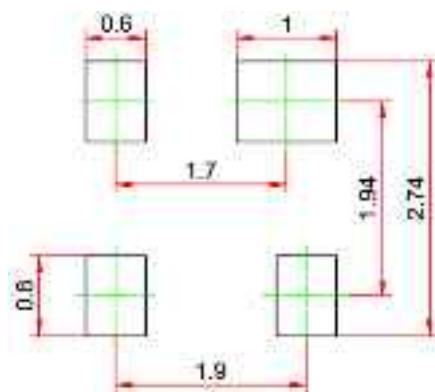
8 kV Contact per IEC61000-4-2

SOT-143 Package Outline Drawing



SYS	MILLIMETERS		
	MIN	NOM	MAX
A	0.90	-	1.15
A1	0.00	0.05	0.10
A2	0.90	-	1.05
b	0.30	0.40	0.50
b1	0.75	-	0.90
c	0.08	-	0.15
D	2.80	2.90	3.00
d	0.20 Typ		
E	1.20	1.30	1.40
E1	2.25	2.40	2.55
e	0.95 Typ		
e1	1.80	1.90	2.00
L	0.55 Ref		
L1	0.30	0.40	0.50
Θ	0°	-	8°

Suggested Land Pattern



Unit(mm)