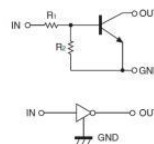


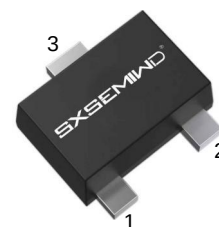
FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

Equivalent Circuit



SOT-723



MARKING:29

1. IN 2. GND 3. OUT

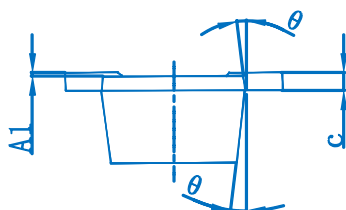
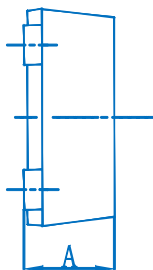
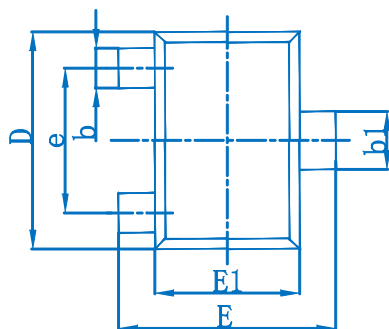
MAXIMUM RATINGS(Ta=25℃ unless otherwise noted)

Symbol	Parameter	Limit	Unit
V_{CC}	Supply Voltage	50	V
V_{IN}	Input Voltage	-10~+40	V
I_O	Output Current	100	mA
P_D	Power Dissipation	100	mW
T_J, T_{stg}	Operation Junction and Storage Temperature Range	-55~+150	℃

ELECTRICAL CHARACTERISTICS (Ta=25℃ unless otherwise specified)

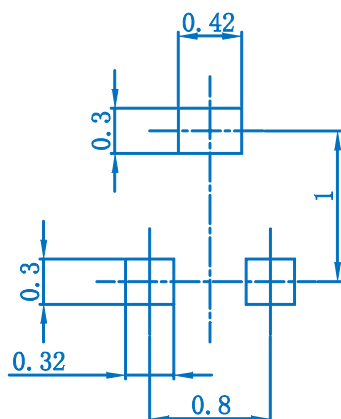
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	$V_{I(off)}$	$V_{CC}=5V, I_O=100\mu A$	0.5			V
	$V_{I(on)}$	$V_O=0.3V, I_O=1mA$			3	V
Output voltage	$V_{O(on)}$	$I_O/I_I=5mA/0.25mA$		0.1	0.3	V
Input current	I_I	$V_I=5V$			0.15	mA
Output current	$I_{O(off)}$	$V_{CC}=50V, V_I=0V$			0.5	μA
DC current gain	G_I	$V_O=5V, I_O=5mA$	82			
Input resistance	R_I		70	100	130	k Ω
Resistance ratio	R_2/R_1		0.8	1	1.2	
Transition frequency	f_T	$V_O=10V, I_O=5mA, f=100MHz$		250		MHz

SOT-723 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.430	0.500	0.017	0.020
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
b1	0.270	0.370	0.011	0.015
c	0.080	0.150	0.003	0.006
D	1.150	1.250	0.045	0.049
E	1.150	1.250	0.045	0.049
E1	0.750	0.850	0.030	0.033
e	0.800TYP.		0.031TYP.	
θ	7° REF.		7° REF.	

SOT-723 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.