

REVISIONS					
REV.	ECN NO.	LOCATION	DESCRIPTION	DATE	DESIGN
A0	N/A		INITIAL RELEASE	/	/
A1	N/A		图面升级整理图面	2025.1.17	阿树

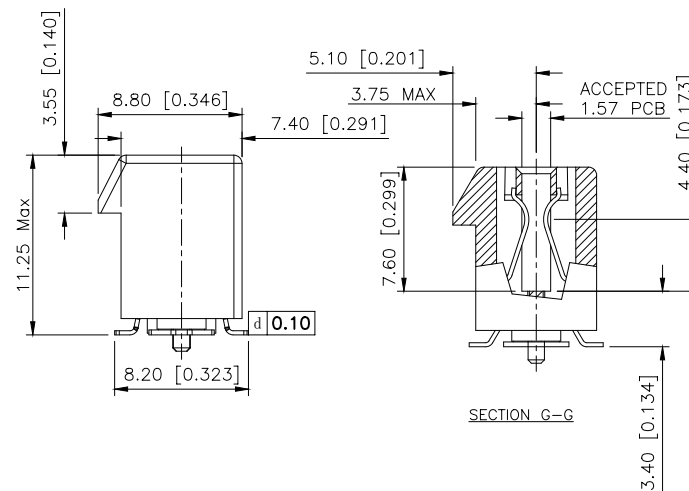
PART NO.:

X1650 W V S-36-9T V15
① ② ③ ④ ⑤ ⑥ ⑦

NOTES 备注:

- MATERIAL 材料:
 - HOUSING 胶壳: THERMOPLASTIC, HIGH TEMP, UL94V-0, COLOR: SEE PART No..
 - CONTACTS 端子: COPPER ALLOY.
 - HOLD DOWN 压端: COPPER ALLOY.
- FINISH 电镀:
 - CONTACT 端子:
 - SELECTIVE GOLD PLATING ON THE CONTACT AREA.
 - 100μ" MIN. MATTE-TIN PLATING ON THE SOLDERTAILS, 50μ" MIN. NICKEL UNDERPLATING OVERALL.
 - HOLD DOWN 压端:
 - 100μ" MIN. MATTE-TIN PLATING WITH 50μ" MIN. NICKEL UNDERPLATE OVERALL.

- ①Series No
②Category:W-Wafer
③Welding Board Angle:V-Stright 180°
④Welding Way: S-SMT
⑤No.of Pins: 36~164 Pin
⑥Plastic material: PA9T/PA46 UL94V-0
⑦Contact Plating:V01-Gold Flash
V15-Gold 15u"
V30-Gold 30u"



DIMENSIONS					
POSITION	A±0.20	B ^{+0.20} _{-0.08}	C±0.25	D±0.25	E±0.30
36	6.00	7.65	9.15	25	27
64	20.00	21.65	23.15	39	41
98	37.00	38.65	40.15	56	58
164	70.00	71.65	73.15	89	91

AuGLAR	±5°
X.	±0.40
X.x	±0.30
X.xx	±0.20
X.xxx	±0.15
UNSPECIFIED TOLERANCES	



DESIGN	阿树	VIEW:		MODEL TYPE:	/
CHECKED		UNIT:	mm/in	PART NO.:	X1650WVS-36-9TV15
APPROVED		SIZE:	A4	DWG NO.:	X-X1650WVS-36-9TV15
				SCALE	PAGE
				N/A	1/3
				REVISION	A1

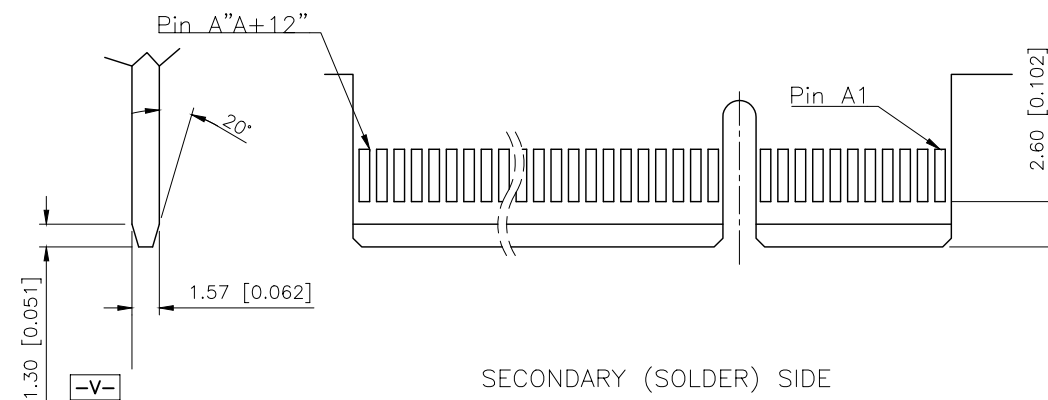
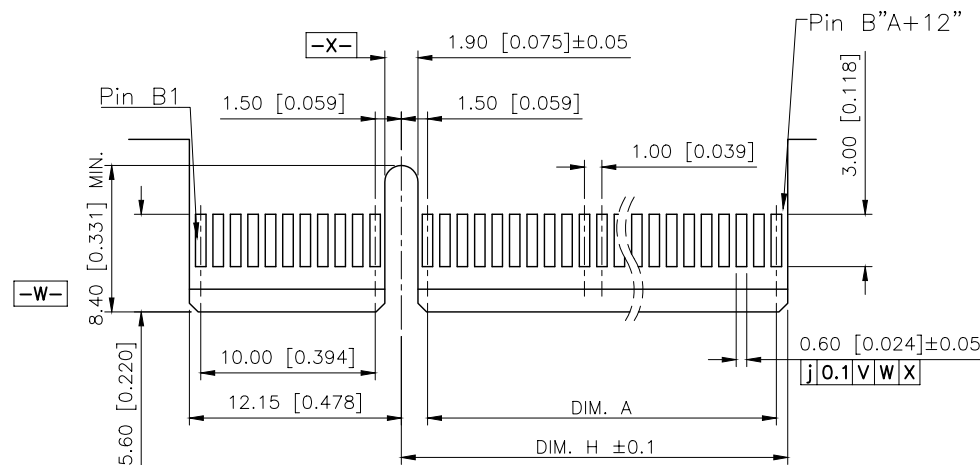
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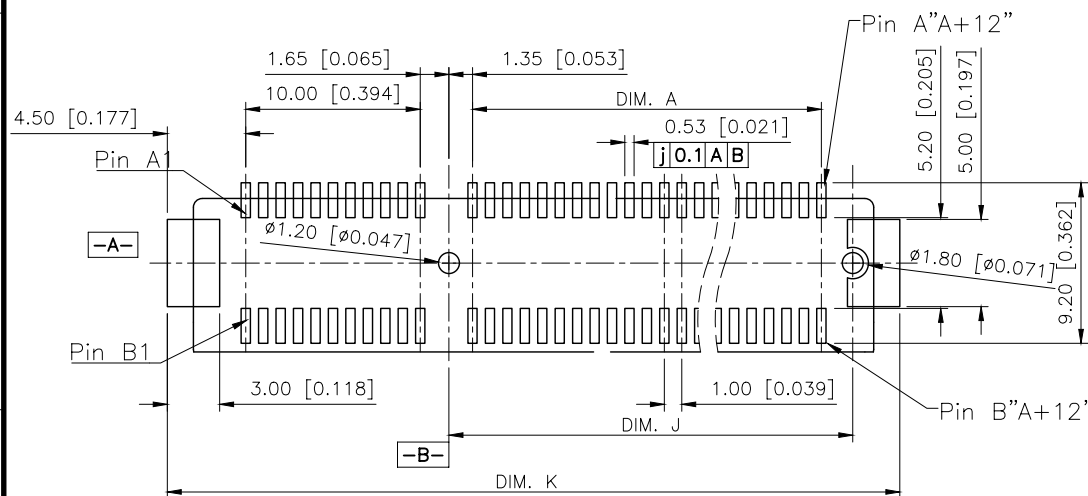
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SECONDARY (SOLDER) SIDE

← I/O PANEL DIRECTION
PRIMARY (COMPONENT) SIDE

NOTES: CHAMFER EDGE MUST BE FREE OF
CUTTING BURRS



POS	DIM. A	DIM. H	DIM. J	DIM. K
36	6	8.15	9.15	28.0
64	20	22.15	23.15	42.0
98	37	39.15	40.15	59.0
164	70	72.15	73.15	92.0

RECOMMENDED PCB LAYOUT
(COMPONENT SIDE)
TOLERANCE: ±0.05

AuGLAR	±5°
X.	±0.40
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UNSPECIFIED TOLERAUCES	



DESIGN	阿树	VIEWS	MODEL TYPE:
CHECKED		UNIT: mm/in	PART NO.:
APPROVED		SIZE: A4	DWG NO.:
XKB INDUSTRIAL PRECISION CO.,LIMITED		SCALE	PAGE
		N/A	2/3
		REVISION	A1

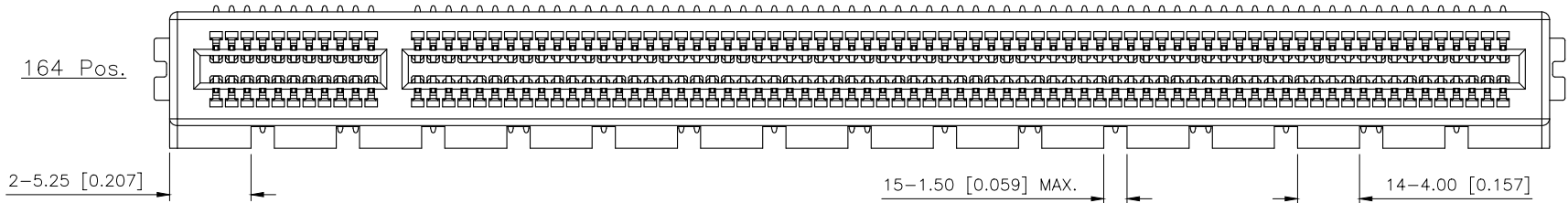
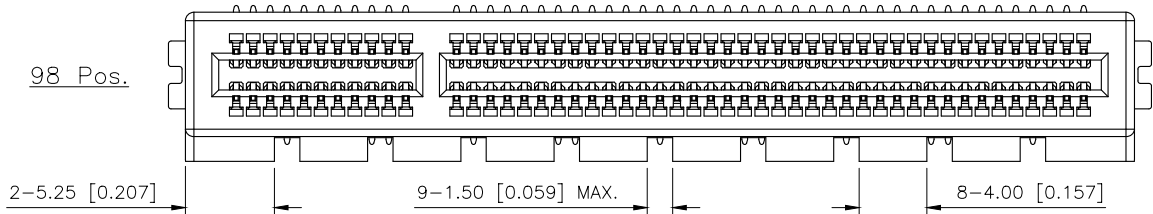
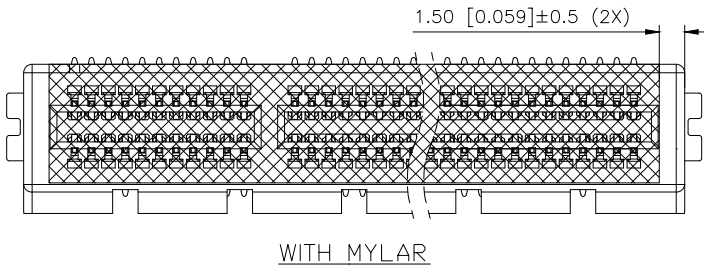
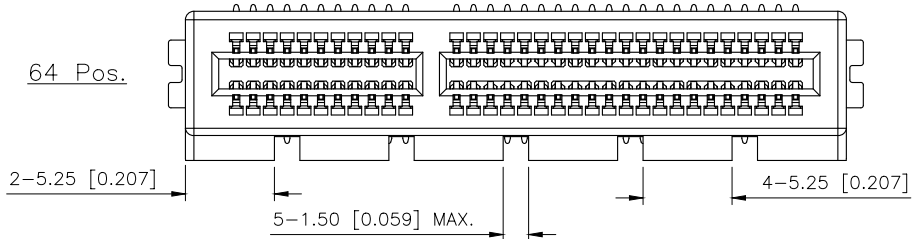
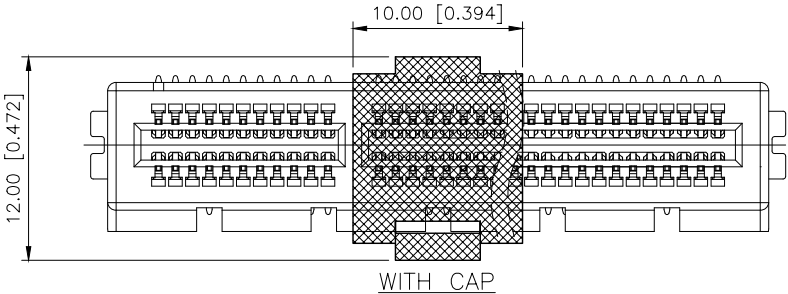
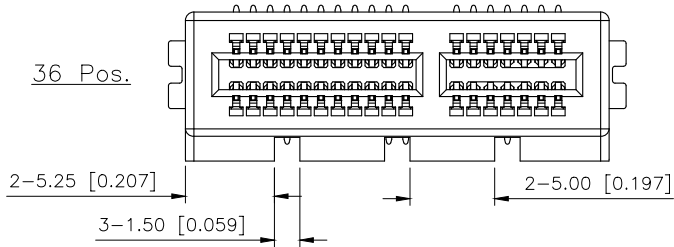
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