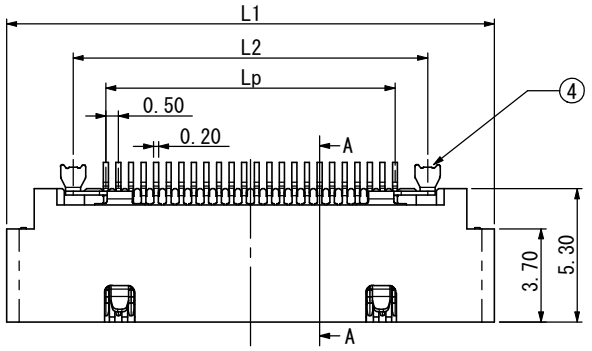
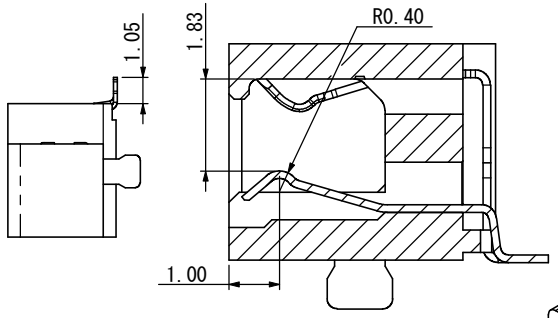
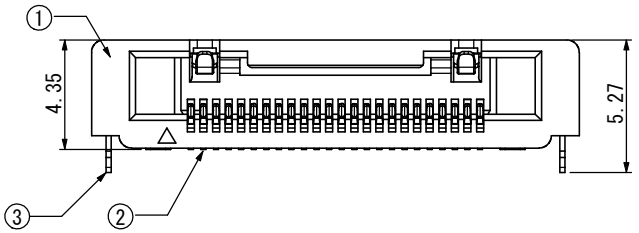
	REVISION DESCRIPTION	APP.	DRW.	DATE	CHANGE No.
△B X 1	HSG design change	K. Abe	O. Shimizu	13/10/22	———
△A X					
△A X					

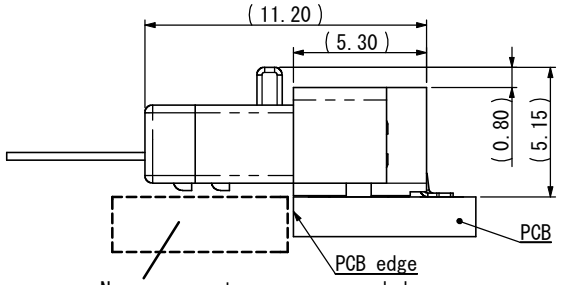
Pin count	Lp	L1	L2	L3	P/N
50	24.5	32.4	27.1	31.05	CNU018S-050-0012
32	15.5	23.4	18.1	22.05	CNU018S-032-0012
24	11.5	19.4	14.1	18.05	CNU018S-024-0012





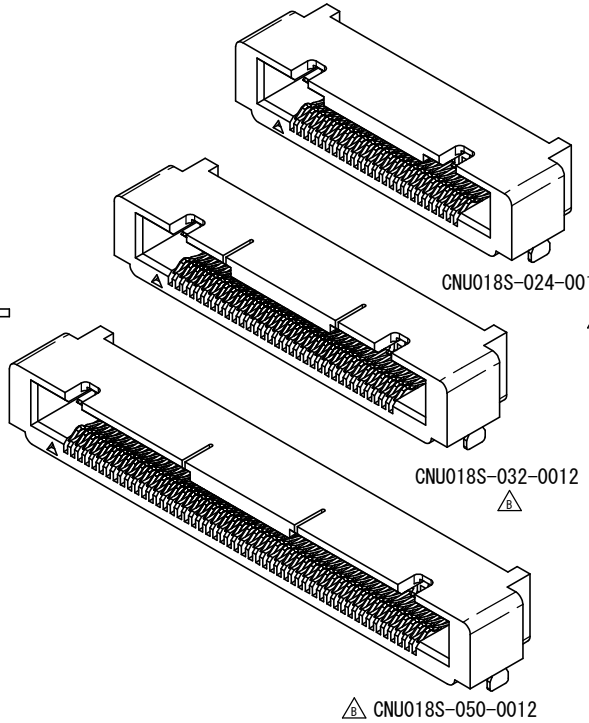
SEC. A-A(S=10/1)

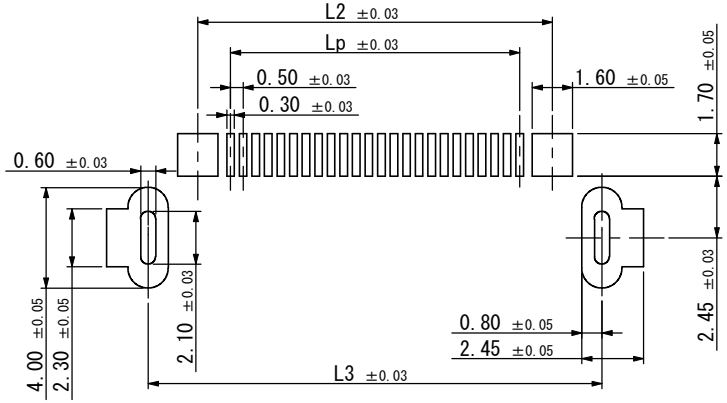




No compornent area recommended

CNU018P-xxx-0001 mating condition





Recommended PCB layout(REF.)

Note.
1. Coplanarity between terminal and reinforcement bracket terminal should be 0.10mm max.
2. Performance shall be refered to YEU-F2012.
3. Applicable plgu
   CNU018P-xxx-0001

Item No.	Description	QNT.	Material	Content
4	Reinforcement bracket-2	2	SUS	Ni/Au
3	Reinforcement bracket-1	2	SUS	Ni/Au
2	Contact	n	Copper alloy	Ni/Au
1	Insulator	1	LCP	Black

Tolerance ±0.25 Unless otherwise specified.

YAMAICHI ELECTRONICS USA INC.

CLASS

	APP.	APP.	CHK.	DRW.	DSGN.
SCALE 5/1					
DIMENTION mm		13/05/09	13/05/09	13/05/09	13/05/09
ANGLE PROJ 3rd		K. Abe	K. Abe	O. Shimizu	O. Shimizu

TITLE CNU018S-xxx-0012

DRW No. YEU-A2199 | Sheet No. / | REV. B

A3-001

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CNU018S-024-0012 CNU018S-050-0012 CNU018S-032-0012