

OM SENI

ESD9B3.3ST5G, ESD9B5.0ST5G

Transient Voltage Suppressors

Micro-Packaged Diodes for ESD Protection

The ESD9B Series is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium.

Specification Features

- Low Capacitance 15 pF
- Low Clamping Voltage
- Small Body Outline Dimensions: 0.039" x 0.024" (1.0mm x 0.60mm)
- Low Body Height: 0.016" (0.4 mm)
- Stand-off Voltage: 3.3 V, 5 V
- Low Leakage
- Response Time is < 1 ns
- IEC61000-4-2 Level 4 ESD Protection
- SZ Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- This is a Pb-Free Device

Mechanical Characteristics

LEAD FINISH: 100% Matte Sn (Tin)

MOUNTING POSITION: Any

QUALIFIED MAX REFLOW TEMPERATURE: 260°C

Device Meets MSL 1 Requirements

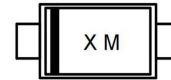
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Contact Air		± 18 ± 18	kV
IEC 61000-4-4 (EFT)		40	A
Total Power Dissipation on FR-5 Board (Note 1) @ $T_A = 25^\circ\text{C}$	P_D	300	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	400	$^\circ\text{C/W}$
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$
Lead Solder Temperature - Maximum (10 Second Duration)	T_L	260	$^\circ\text{C}$



SOD-923
CASE 514AB

MARKING DIAGRAM



X = Specific Device Code
M Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
ESD9B3.3ST5G	SOD-923 (Pb-Free)	8000/Tape & Reel
ESD9B5.0ST5G	SOD-923 (Pb-Free)	8000/Tape & Reel

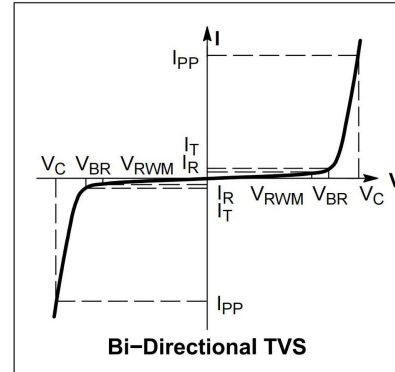
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

OM SENI

ELECTRICAL CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
C	Capacitance @ $V_R = 0\text{ V}$ and $f = 1.0\text{ MHz}$



ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Device	Device Marking	V_{RWM} (V)	I_R (nA) @ V_{RWM}	V_{BR} (V) @ I_T (Note 2)		I_T	C (pF)	V_C Per IEC61000-4-2 (Note 3)	V_C (V) Max Per 8 x 20 μs (Note 4)	
		Max	Max	Min	Max	mA	Typ		$I_{PP} = 1\text{ A}$	$I_{PP} = 2\text{ A}$
ESD9B3.3ST5G	2*	3.3	100	5.0	7.0	1.0	15	Figures 1 and 2 See Below	10.5	11.5
ESD9B5.0ST5G, SZESD9B5.0ST5G	E	5.0	100	5.8	7.8	1.0	15	Figures 1 and 2 See Below	12.5	15.0

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

* Rotated 270° .

2. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C .

3. For test procedure see Figures 3 and 4 and Application Note AND8307/D.

4. Surge current waveforms per Figure 5.

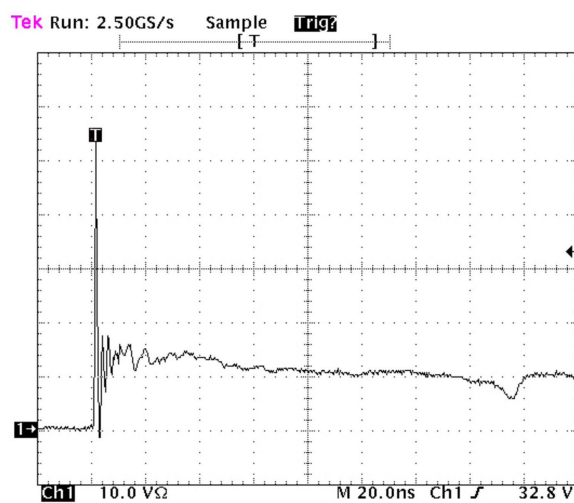


Figure 1. ESD Clamping Voltage Screenshot
Positive 8 kV Contact per IEC61000-4-2

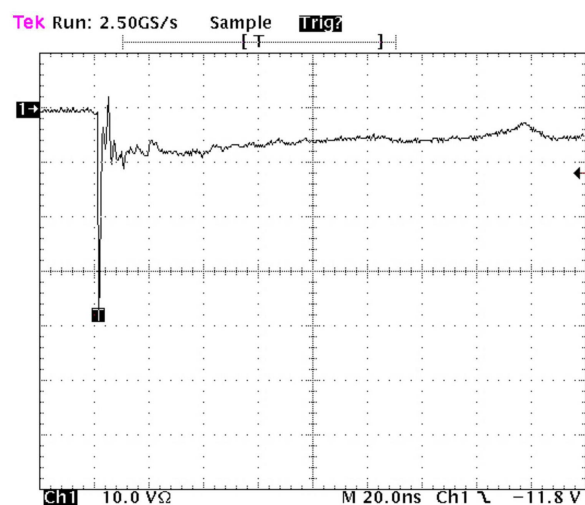


Figure 2. ESD Clamping Voltage Screenshot
Negative 8 kV Contact per IEC61000-4-2

OM SENI

IEC 61000-4-2 Spec.

Level	Test Voltage (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8

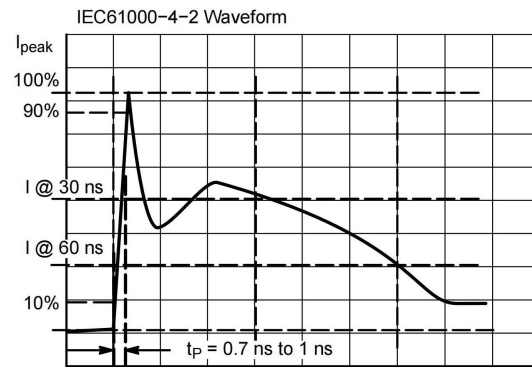


Figure 3. IEC61000-4-2 Spec

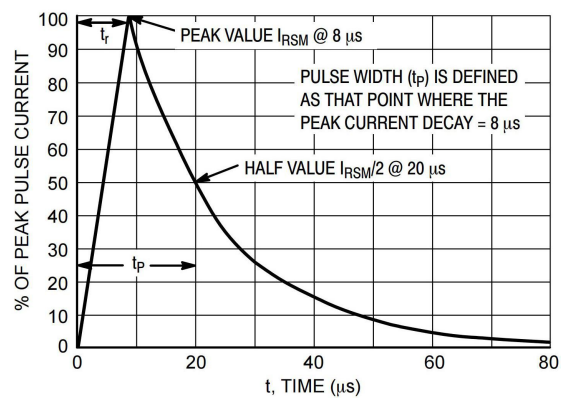


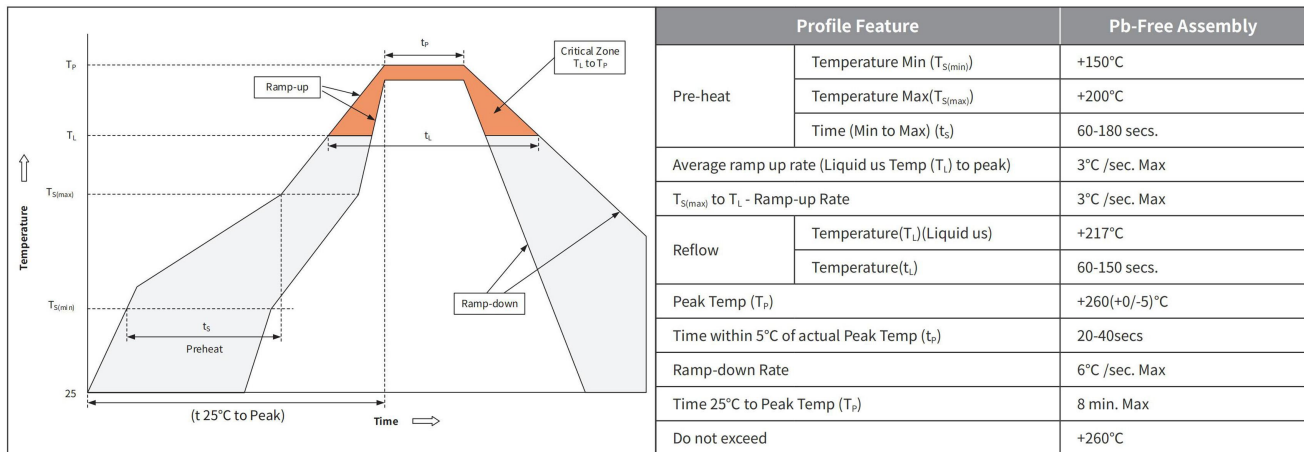
Figure 5. 8 X 20 μs Pulse Waveform

OM SENI

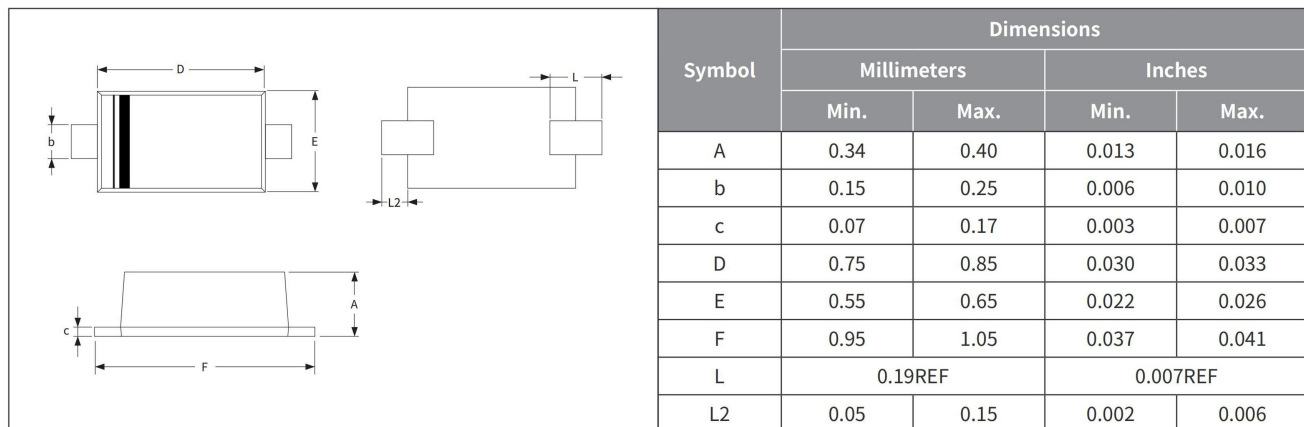
Ordering Information

PREFERRED P/N	PACKAGE	SIZE(mm)	DELIVERY MODE	MPQ(PCS)
	SOD-923	1.00×0.60×0.37	7"	8000

Recommended Soldering Conditions



Package Outline Dimensions (SOD-923)



Suggested Pad Layout

